Tigran Kurkchiyants ENGR 378.04 Pre-lab 3

١.	G	D	Q	Q
•	0	0	Ō	0
	0	0	1	
	0	١	0	0
	O	ι	1	1
	1	0	0	0
	1	0	1	0
	l	l	Ð	1
	١	1	1	l

٢	K	Q	լ Q [†]
0	O	0	0
0	0	ı	1
0	١	0	0
0	1	ι	٥
l	0	0	ı
(0	١	1
1	1	0	1
ı	١	ι	0

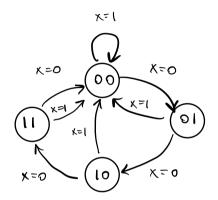
Output depends on the enable signal G and input signal D. When the latch is not enabled (i.e. G=0), the autput is in retention state (i.e. keeps its previous output).

When G=1, output follows the input signal D.

The FF is triggered by the falling edge of the clock. I acts as 'set' & K acts as 'reset'. When both I & K are zero, FF does not change its state. When both I & K are one, the output is taggled.

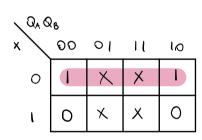
2.	Present	Nex+	state	J	A	K	·A	ل	B	K	В
	state	X=o	X =1	X=0	`X = (X=0	X=1	X=O	X=[χ÷Ο	X =1
	00	01	00	0	0	0	0	ı	0	0	0
				0	0	ı	ı	١	0	1	1
	01	10	00	1	0	0	0	0	0	1	1
				1	0	ı	1	١	1	1	ı
	10	(1	00	0	0	0	l	ı	0	0	٥
				ı	l	٥	1	ı	0	l	ı
	11	00	00	0	0	l	ı	0	0		
				l	ı	l	1	١	l	l	ı

Present	Nex+	- state		A	K	·A	۲	В	K	В
state	X=o	X = (X=0	`X = (X=0	X=1	X=O	X = 1	χ÷Ο	X =1
0 0	01	00	0	0	X	Х	١	0	X	X
01	10	00	١	0	Х	X	Х	Х	١	1
10	[]	00	X	Х	0	1		0	X	X
11	00	00	X	Х	١	l	Х	X	١	1



QA QB									
x \	ଚଚ	01	11	10					
0	0		X	Х					
ι	0	0	X	X					
T - 0 "									





$$J_{\mathfrak{b}} = \chi'$$

