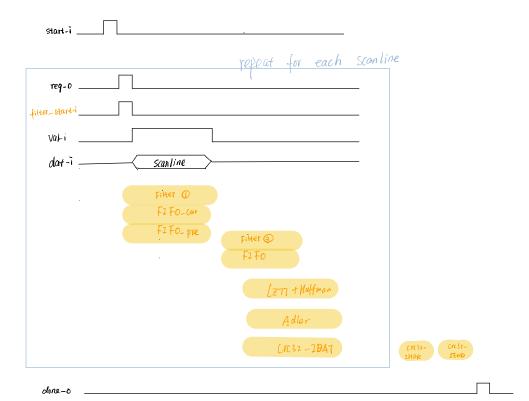


done-o

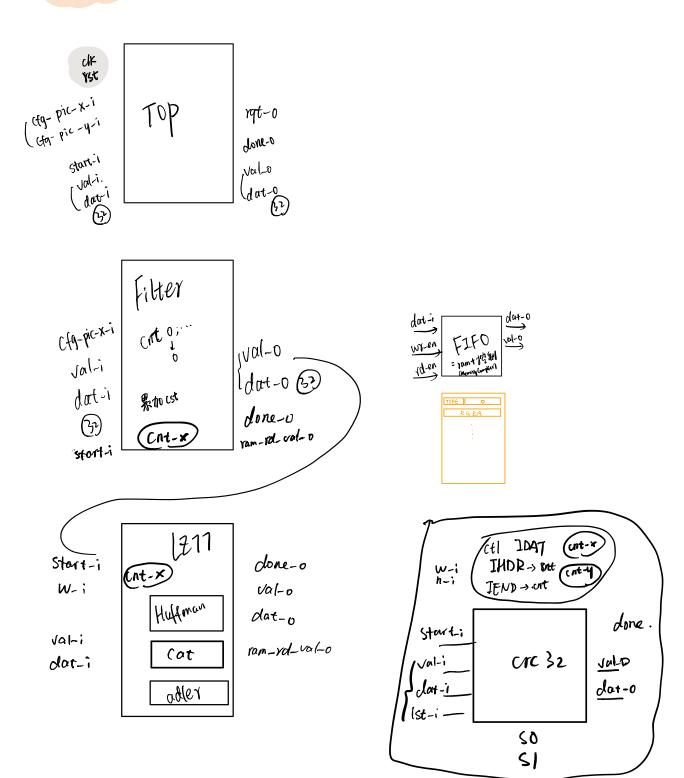
reg-o

dat-o

val-o



## 为模块



## lz17

1. UPDATE cycle: m+1 +1 fifo Delay

Cycle: 1

② input :

67-(x-lent 4m)

Z. SEARCH

# define PARALLEL 1/2/4

generate PAPALLEL

[lg[64\*idx-parallel+0] = input [0] == whole [0+idx-parallel]

& flg [m(idx-1)]

Jand-miniral-idx-1

generate

min-fg (idx) = min-idx == 64

her

find-minimal-idx-1 
$$\rightarrow$$
 bst-len

hst-idx

literal

fla

yal-i

excel 0

length 257-276 + 3

excel 1

clistance  $\rightarrow$  excel 0 +3

excel 2  $\rightarrow$  code

literal

literal

excel 1

congth  $\rightarrow$  excel 0 +3

excel 2  $\rightarrow$  excel 2  $\rightarrow$ 

拼 32