Subroutine/function

#### Introduction

- open subroutine
  - ✓ replace/extend text
  - ✓ macro
  - ✓ no run-time overhead w.r.t registers
- closed subroutine
  - ✓ call/return
  - ✓ context switching (register saving)
  - ✓ (de)allocation of stack frame
  - ✓ parameter passing: register or stack
  - ✓ return value/address

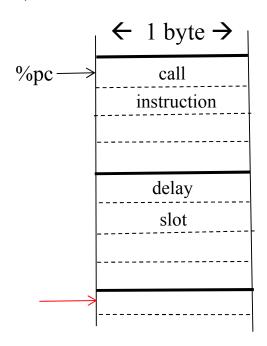
```
call subr
! delay slot
! return address
...
subr: save %sp, ..., %sp
...
ret
restore ! delay slot
```

#### Subroutine call

- 1) call label
  - ✓ transfer control to label (%pc update)
  - ✓ store %pc(before update) to %o7 as return address
- 2) jmpl R+A, S
  - ✓ transfer control to R+A (R: register, A: register/imm)
  - ✓ store %pc to register S

(Be aware of delay slots!)

- Return instructions
  - ✓ delay slot
  - l) ret
  - 2) jmp1 % i7 + 8, % g0 (%pc)  $\leftarrow$  (%i7 + 8)
  - 3) jmpl %S+8, %g0
  - 4) retl (return from leaf subroutine)



### Subroutine call/return example

```
.global
            main
main: save %sp, -96, %sp
     mov 2, %o0
     mov 3, %o1
      call add2
      nop
      ret
      restore
add2: save %sp, -96, %sp
      add %i0, %i1, %i0
      ret
      restore
```

< Instruction execution sequence >

1. call instruction

%pc ← address 'add2'

- 2. delay slot instruction
- 3. first instruction in subroutine
- 4. ret (%pc  $\leftarrow$  %i7 + 8)
- 5. delay slot instruction
- 6. instruction in main routine

#### call instruction (machine) format

Bit number	31 30	29	0
field	op	displacement	

 $\checkmark$  op = 01, displacement= address (pc-relative)

```
✓ Ex

call add4

mov 5, %o1

ret

restore
add4: save %sp, -96, %sp
```

#### Stack frame structure

$% \frac{1}{2} \left( \frac{1}{2} \right) $	$9\%$ sp $\rightarrow$	register window saving area (64B)	
area		Return Structure pointer (4B)	
		First 6 parameters (24B)	
		rest of parameters (as needed)	call
%fp - n are	a	locals (as needed)	0
%fp + n	%fp→	register window saving area (64B)	
area		Return Structure pointer(4B)	
		First 6 parameters (24B)	ler
_		rest of parameters (as needed)	call
		locals (as needed)	

• save %sp, -96, %sp: stack frame and register set allocation

## Register set

• 32 registers through mapping

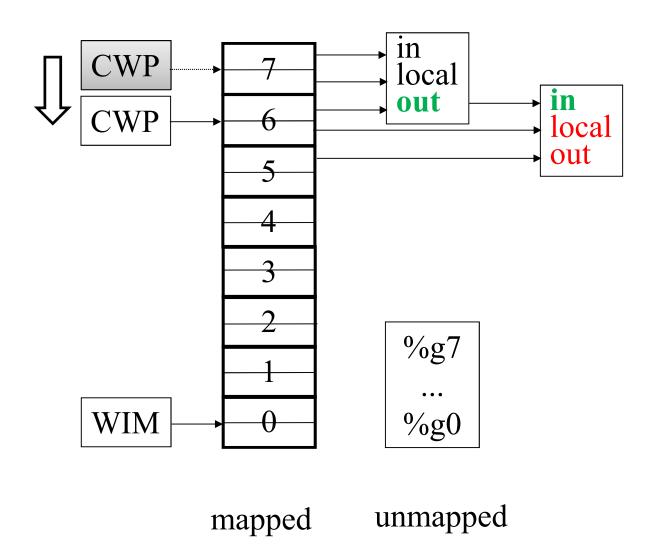
Group	name/mnemonics	function	mapping
global	%r0 - %r7(%g?)	global register	No
out	%r8 - %r15(%o?)	outgoing params	Yes
local	%r16 - %r23(%l?)	local vars	Yes
in	%r24 - %r31(%i?)	incoming params	Yes

- A typical SPARC processor has 128 registers for mapping
  - = 16 registers per set \* 8 sets
- SPARC allows calling subroutine without register saving
  - ✓ register saving: execution time overhead
- Related instruction: save/restore

## Register saving

```
main()
                                                                         memory
                                           max 용
 int x, y, z;
 register int max;
                              %10
                                                                                      z 용
y 용
x 용
 result=getre(x,y)
getre(x,y)
                                              i 용
                                                                                      \mathbf{Z}
                                                               getre
                                                                                      y
                                                                 용
int x, y, z;
                               %10
                                                                                      X
register int i;
                                                                                      \mathbf{Z}
                                                               main
                                                                                      y
                                                                 용
                                                                                      \mathbf{X}
```

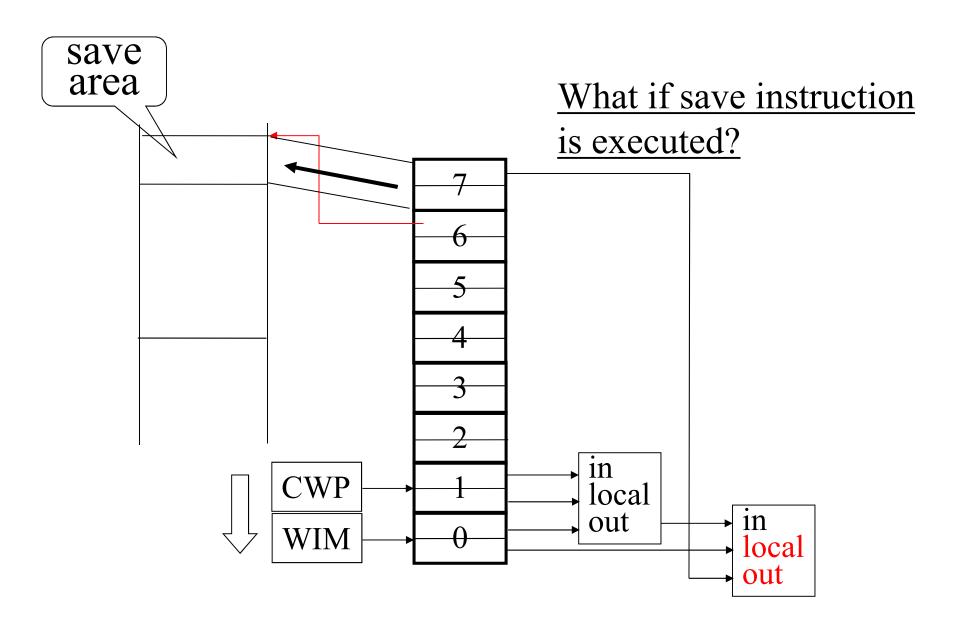
# Register file structure



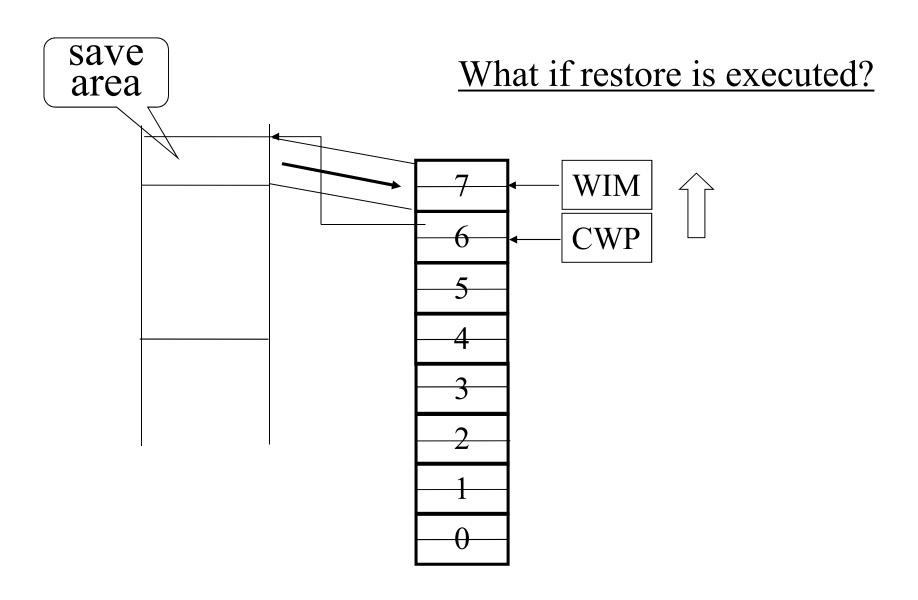
### Register window

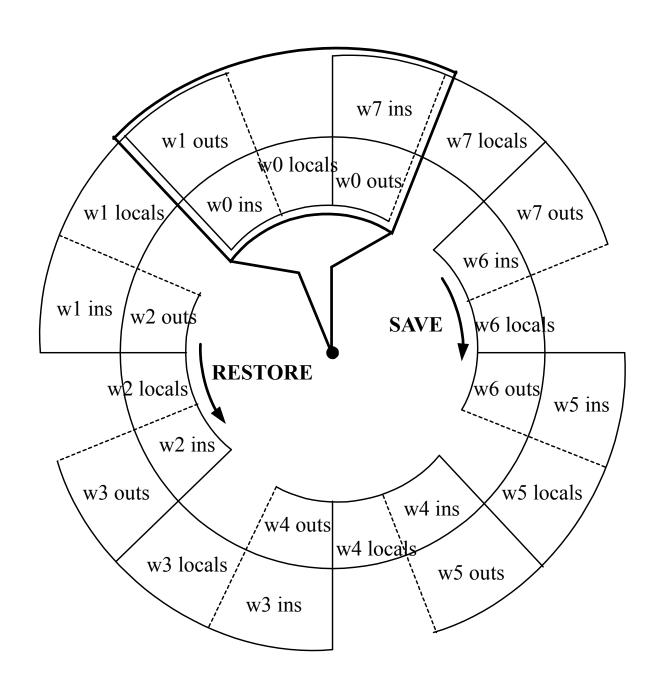
- CWP(current window pointer)
  - ✓ pointing current active register set
- WIM(window invalid mask)
  - ✓ pointing the last available register set
- Effect of save instruction
  - register set allocation
  - out registers of caller is the same as in registers of callee
    - overlapped register window
    - > %sp (%o6), %fp(%i6)
- Effect of restore instruction
- Register windows overflow / underflow

## Register window overflow

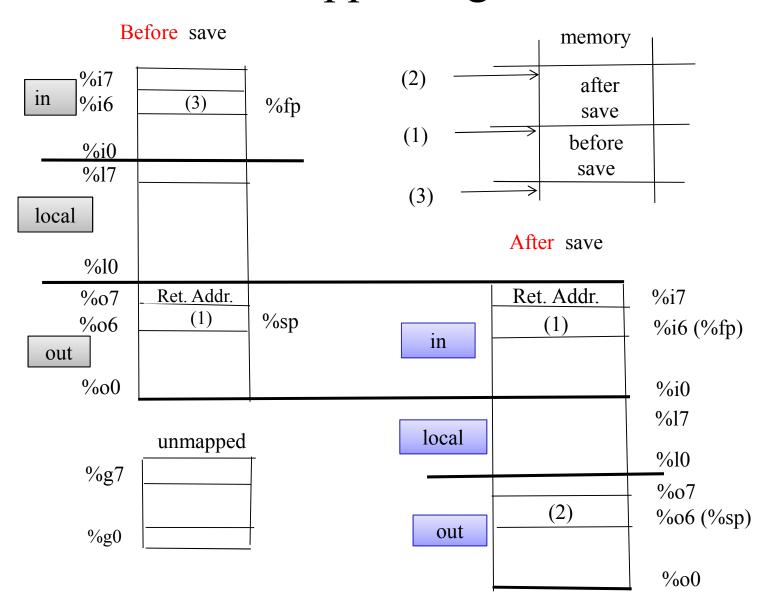


# Register window underflow





## Overlapped register window



## Arguments passing

```
1) in-line method
 ✓ Embedded in code
                                                  Call addr
      call
            addr
                                                    nop
       nop
                                                     3
      .word 3, 4
                                                    4
addr:
     save %sp, -96, %sp
                                                  save ...
           [\%i7 + 8], \%i0
       ld
       ld
           [\%i7 + 12], \%i1
       add %i0, %i1, %i0
                                                      %i7
                                     %o7
      jmpl \%i7 + 16, \%g0
                                   %PC
       restore
```

#### 2) Using stack

- ✓ Excessive memory access
- ✓ Most widely used method

For Sub1

For Main

```
main:
        store in < stack >
        call sub1
        nop
sub1:
       save %sp, ..., %sp
        read from < stack >
        ret
        restore
         < stack >
```

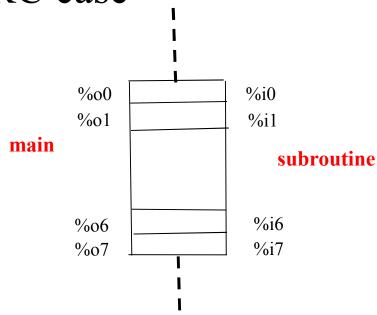
3) Using registers: SPARC case

✓ use out registers

✓ maximum number: 6

✓ %o6: %sp

✓ %o7: return address



✓ arguments more than 6 are stored in stack

# • Example 1 mov 3, %o0 mov 5, %o1 call .mul nop

#### • Example 2

```
int main(){
    int sum;
    sum = add4(1, 2, 3, 4);
}

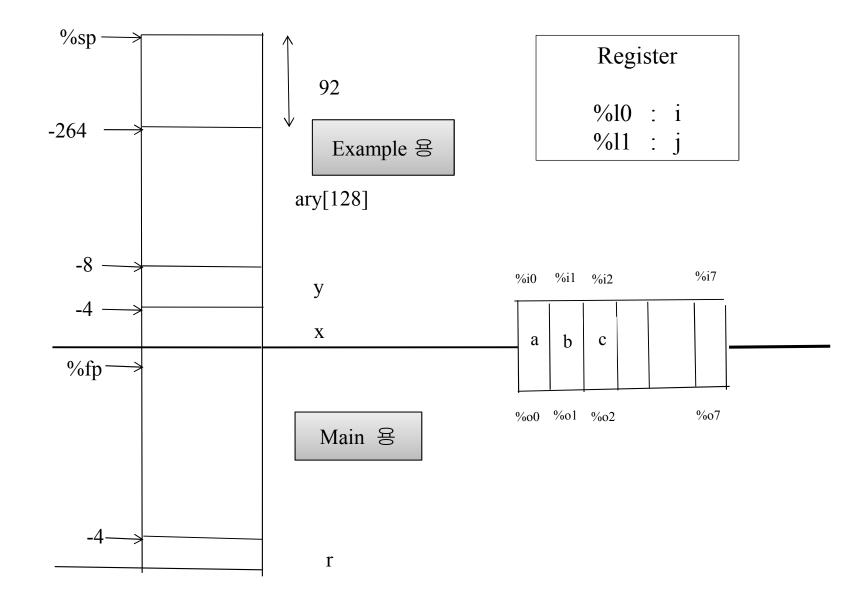
int add4(int a, int b, int c, int d){
    return a + b + c + d;
}
```

```
.global main
main: save %sp, -96, %sp
     mov 1, %o0
     mov 2, %o1
     mov 3, %o2
     call add4
     mov 4, %o3
     ret
      restore
      .global add4
add4: save %sp, -96, %sp
     add %i0, %i1, %i0
     add %i2, %i0, %i0
                                 add
                                     %i3, %i0, %i0
     ret
                                 ret
     restore %i3, %i0, %o0
                                 restore
```

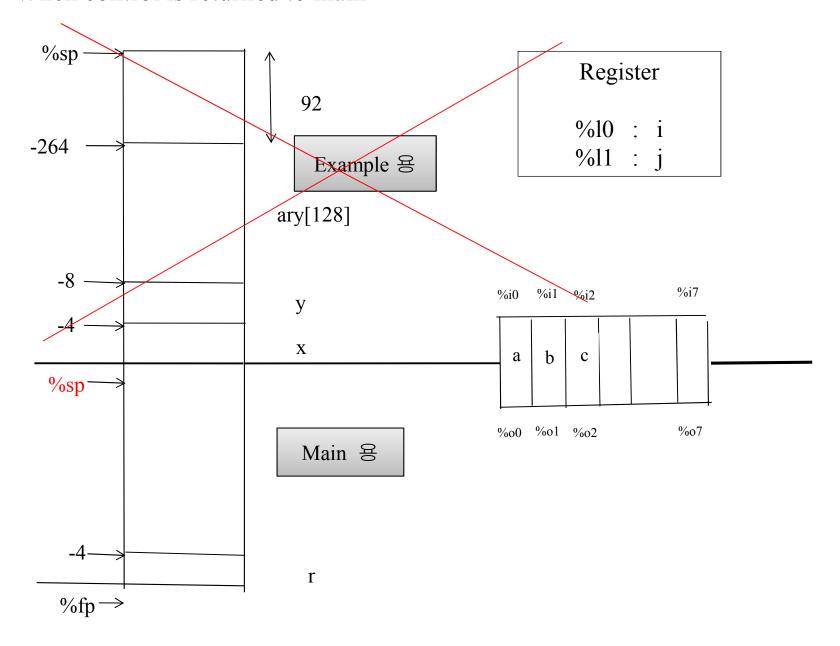
## Program Example

```
int example(int a, int b, char c) {
 int x, y;
 short ary[128];
 register int i, j;
 x = a + b;
 i = c + 64;
                                 main() {
 ary[i] = c + a;
                                   int r;
 y = x * a;
                                   r = example(3, 5, 4);
j = x + i;
 return x + y;
```

• Variables in stack when subroutine example is executed



#### • When control is returned to main



#### ! arguments

! a r in %i0

! b r in %i1

! c r in %i2

#### ! local variables

$$x s = -4$$

$$y s = -8$$

ary 
$$s = -264$$

#### ! register variables

! i r in %10

! j r in %11

#### .global example

add %i0, %i1, %o0 ! 
$$x = a + b$$

$$! \quad x = a + b$$

st 
$$\%00, [\%fp + x_s]$$

```
add
       %i2, 64, %l0
                             ! i = c + 64
       %i0, %i2, %o0
add
                             ! c + a
sll
       %10, 1, %o1
                              ! i*2
add
       %fp, ary s, %o2
sth
       \%00, [\%01 + \%02]! store in ary[i]
ld
       [\%fp + x \ s], \%oo ! y = x * a
call
       .mul
                                       add %00, %01, %i0
       %i0, %o1
                                       ret
mov
                                       restore
       \%00, [\%fp + y s]
st
       [\%fp + x \ s], \%o0 ! j = x + i
ld
       %10, %o0, %11
add
ld
       [\%fp + x s], \%o0
                             ! return x + y
ld
       [\%fp + y s], \%o1
ret
restore %00, %01, %00
                             ! add %00, %01, %00
```

.global main

main: save 
$$%sp$$
,  $-(64 + 4 + 24 + 4) & -8$ ,  $%sp$ 

mov 3, %o0

mov 5, %o1

mov 4, %o2

call example

nop

st %o0, [%fp - 4]

mov 1, %g1

ta 0