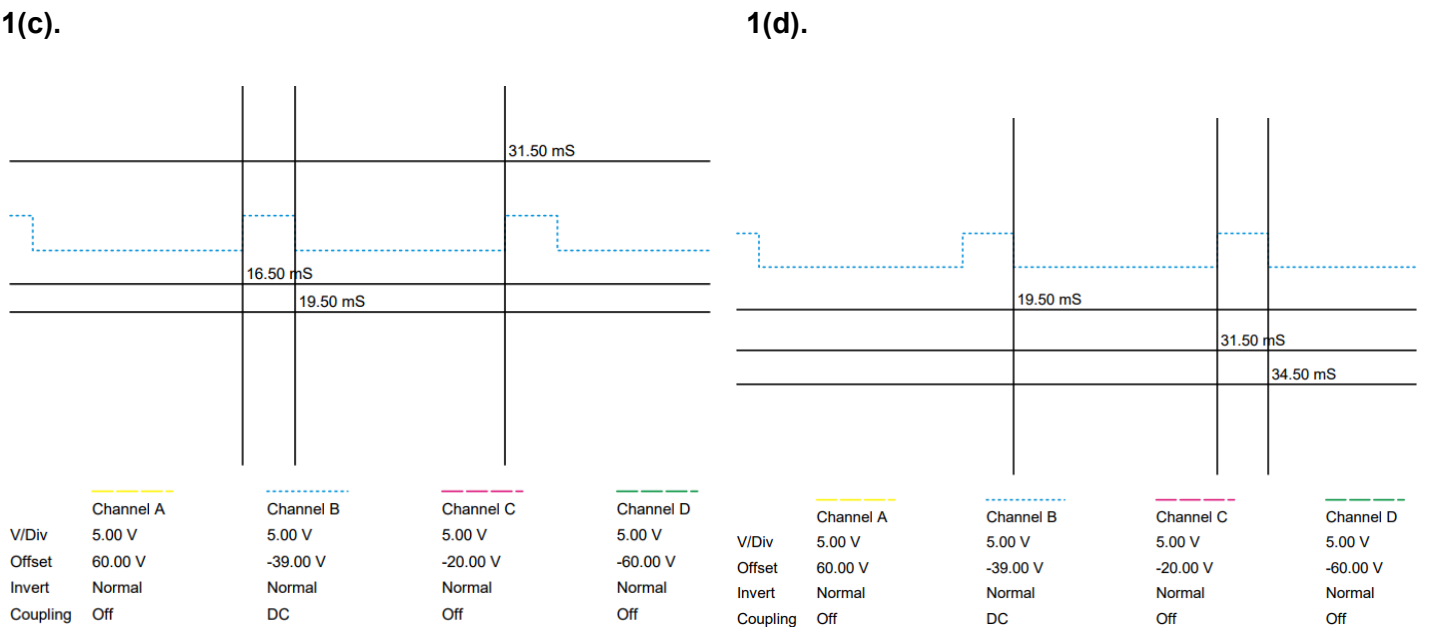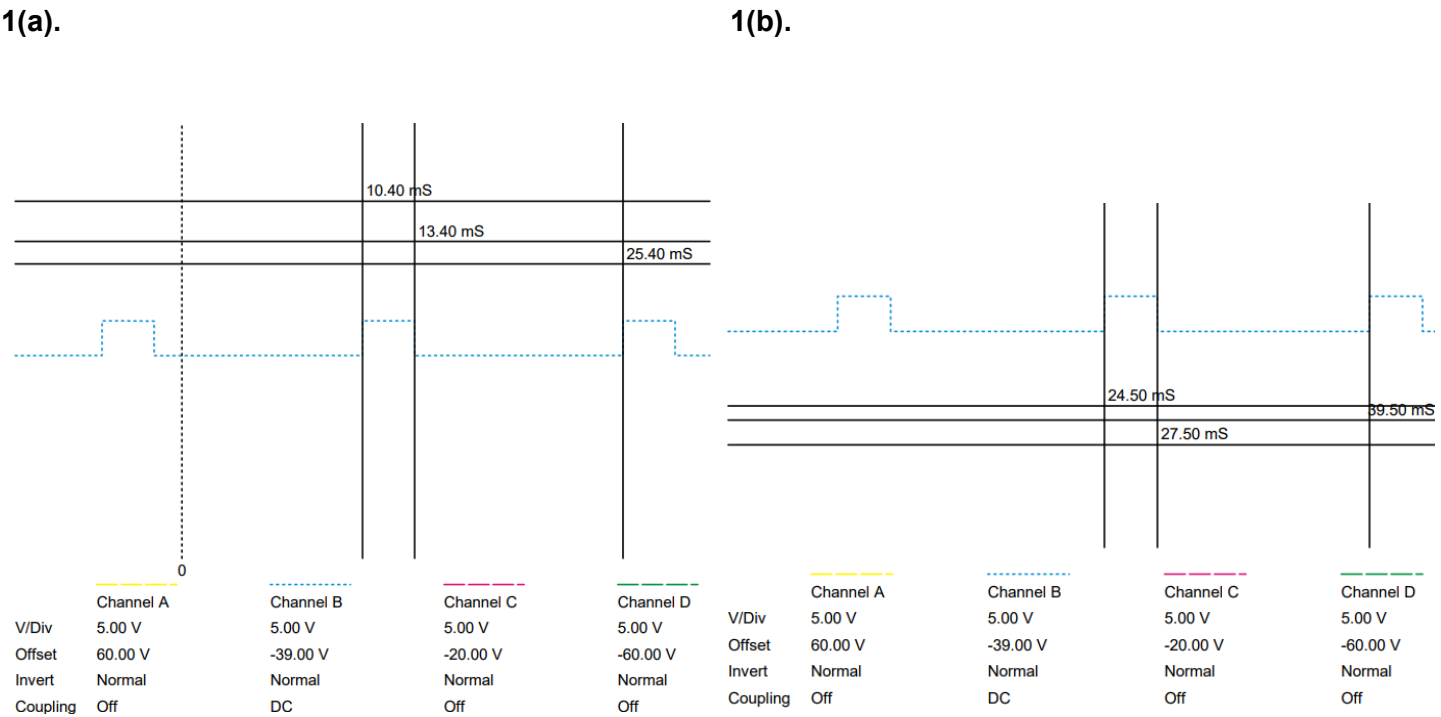# OUTPUTS:
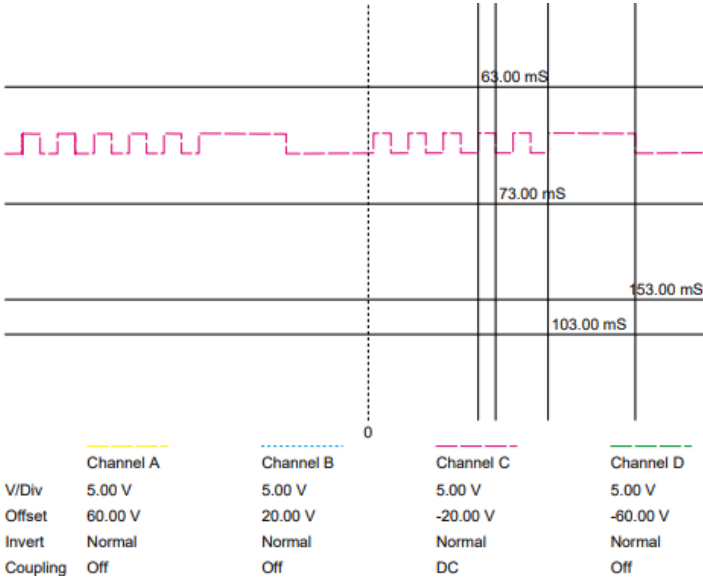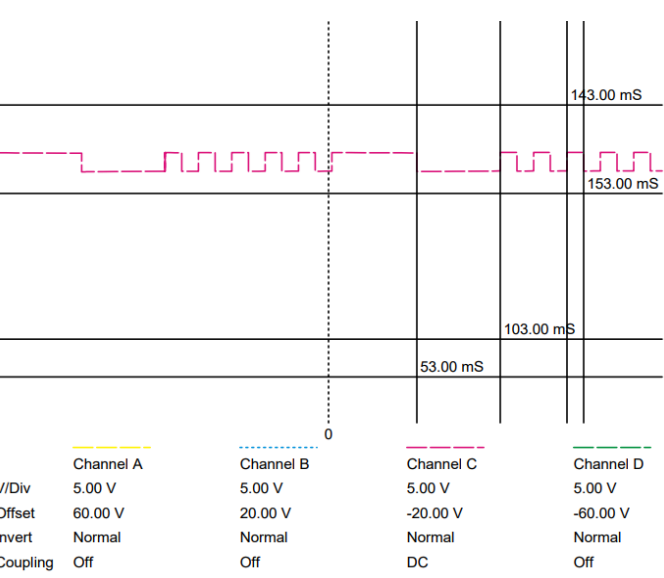
The illustrations depicting the oscilloscope output from the Proteus simulation are provided in the following screenshots:

1(a).

10.40 mS
13.40 mS
25.40 mS

	Channel A	Channel B	Channel C	Channel D
V/Div	5.00 V	5.00 V	5.00 V	5.00 V
Offset	60.00 V	-39.00 V	-20.00 V	-60.00 V
Invert	Normal	Normal	Normal	Normal
Coupling	Off	DC	Off	Off

1(b).

24.50 mS
39.50 mS
27.50 mS

	Channel A	Channel B	Channel C	Channel D
V/Div	5.00 V	5.00 V	5.00 V	5.00 V
Offset	60.00 V	-39.00 V	-20.00 V	-60.00 V
Invert	Normal	Normal	Normal	Normal
Coupling	Off	DC	Off	Off

1(c).

31.50 mS
16.50 mS
19.50 mS

	Channel A	Channel B	Channel C	Channel D
V/Div	5.00 V	5.00 V	5.00 V	5.00 V
Offset	60.00 V	-39.00 V	-20.00 V	-60.00 V
Invert	Normal	Normal	Normal	Normal
Coupling	Off	DC	Off	Off

1(d).

19.50 mS
31.50 mS
34.50 mS

	Channel A	Channel B	Channel C	Channel D
V/Div	5.00 V	5.00 V	5.00 V	5.00 V
Offset	60.00 V	-39.00 V	-20.00 V	-60.00 V
Invert	Normal	Normal	Normal	Normal
Coupling	Off	DC	Off	Off

2(a).

	Channel A	Channel B	Channel C	Channel D
V/Div	5.00 V	5.00 V	5.00 V	5.00 V
Offset	60.00 V	20.00 V	-20.00 V	-60.00 V
Invert	Normal	Normal	Normal	Normal
Coupling	Off	Off	DC	Off

2(b).

	Channel A	Channel B	Channel C	Channel D
V/Div	5.00 V	5.00 V	5.00 V	5.00 V
Offset	60.00 V	20.00 V	-20.00 V	-60.00 V
Invert	Normal	Normal	Normal	Normal
Coupling	Off	Off	DC	Off

2(c).

	Channel A	Channel B	Channel C	Channel D
V/Div	5.00 V	5.00 V	5.00 V	5.00 V
Offset	60.00 V	20.00 V	-20.00 V	-60.00 V
Invert	Normal	Normal	Normal	Normal
Coupling	Off	Off	AC	Off

2(c).

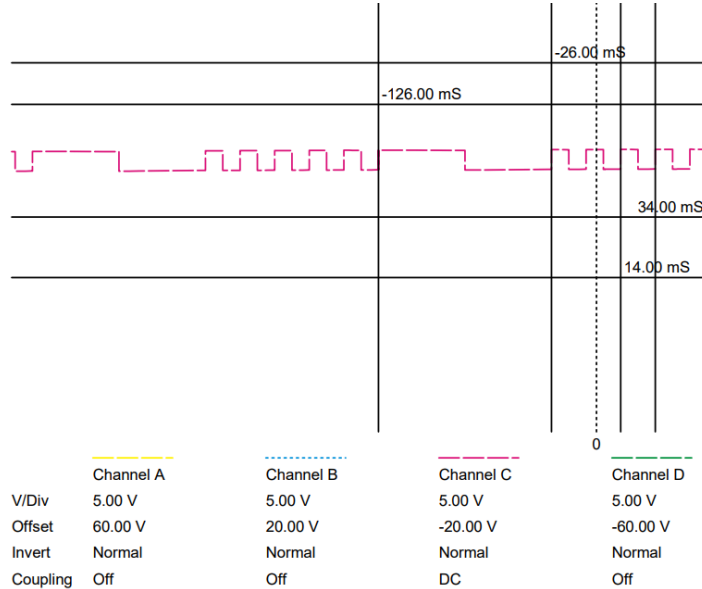|	Channel A	Channel B	Channel C	Channel D
V/Div	5.00 V	5.00 V	5.00 V	5.00 V
Offset	60.00 V	20.00 V	-20.00 V	-60.00 V
Invert	Normal	Normal	Normal	Normal
Coupling	Off	Off	DC	Off

3. Proteus Simulation for interrupt driven digital clock