Timothy Dee, Justin Long, Brandon, McDonnel

Background

Project Outline

Initial Implementation

Intermediate Implementation

Remotely Connected Electric Field Generator

for Particle Separation in a Fluid

Presented by *Team May1612* on 27 April 2016

Timothy Dee, Justin Long, Brandon, McDonnel Iowa State University

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Dielectrophoresis (DEP)

Background
Project Outline

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Initial Implementation

- A dielectric particle in a non uniform electric field experiences a force
 - 2 Different potential fields and frequencies has an effect on the net force
- 3 First studied in 1950s by Herbert Pohl
- 4 Recently revived due to the ability to manipulate micro-particles and cells.

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Real World Application

Background
Project Outline

Initial Implementation

- Openation of the properties of the properties
 - 2 Act as filter
 - 3 Research in separating cancerous cells from healthy cells
- 4 Separate platelets from whole blood
- 5 Separate red and white blood cells
- 6 Strains of bacteria and viruses

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Project Description

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Project Outline

Initial Implementation

- 1 A system to aid in the research of DEP
- 2 Allow for quicker setup times
- 3 Control Voltage and Frequency via the web
 - 1 to 60 VPP
 - 2 10k to 1Mhz
- 4 Hold output for long time periods
- 5 Small Form Factor
- 6 Easy to use
- Plug and play

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Project Outline

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Project Structure

- 1 Raspberry Pi
- Web Interface
- 3 Web Server
- 4 Frequency Control Solution
- 5 Voltage Control Solution

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Initial Implementation

- Raspberry Pi
 - 1 Host web server
 - 2 Remote manipulation of circuit output
 - 3 Web interface can provide additional functionality
 - 4 GPIO pins input to circuit
- 2 Circuit Output
 - 1 Frequency generated by GPIO pin
 - 2 GPIO waveform integrated to get sine wave
 - 3 Sine wave amplified to form output

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Project Outline

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- Raspberry Pi controls Integrated circuit components
- 2 Minigen Signal Generator
- 3 SPI communications
- 4 Produces frequency 10 Khz 4 Mhz
- 5 Digital Potentiometers
- 6 SPI communications
- Vary resistance to control amplifier
- 8 Amplifier controls voltage output from circuit

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Project Outline

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Problems and Setbacks

- Mosfet Amplifier
- 2 Digital Potentiometer
- 3 Resistance drops with AC signal
- 4 Distorted the sine wave
- 6 Op Amps
- 6 Slew Rates
- 7 Gain Bandwidth
- 8 Minigen
- 9 B23 Bug

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Digital Potentiometer Amplifier Circuit

Background

Project Outline

Initial Implementation



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Intermediate



- picture
- 2 information

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Project Outline

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Problems and Setbacks

- Lost a group member
- 2 BJT Switch
- 3 Control through GPIO pin
- 4 Current Leaks through when logically off
- 6 Relay
- 6 Operating Frequency not sufficient
- Brandon
- **8** We have had to make quite a few adjustments from our original plan.
- This is especially the case with our digital potentiometers.

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SSR Circuit Implementation

Background

Project Outline

Initial Implementation



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Current Circuit Design

Background

Project Outline

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Intermediate Implementation

1 "design diagram"

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Background Project Outline

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Intermediate Implementation **Web Interface**

- Hosted Locally
- 2 Able to be seen on intranet
- 3 Controls Voltage and Frequency
- 4 Provides Additional Functionality

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Project Outline

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Questions?

- Dielectrophoresis (DEP)
- 2 Circuit Design
- 3 Digital Potentiometer/ Operation Amplifier
- 4 MOSFET/ Programmable Gain Amplifiers (PGA)
- 6 Web Interface
- 6 Design Documents

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Implementati

Work Breakdown

- Initial Planning
- 2 Project Website
- 3 Reports
- 4 Circuit Design
- 6 Web Server
- 6 SOC Communications
- PCB Design