Timothy Dee, Justin Long, Brandon McDonnell

Dielectrophoresis

Project Overview

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Questions

Remotely Connected Electric Field Generator

for Particle Separation in a Fluid

Presented by *Team May1612* on 27 April 2016

Timothy Dee, Justin Long, Brandon McDonnell Iowa State University

Timothy Dee. Justin Long. **Brandon McDonnell**

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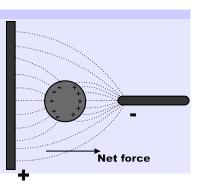
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Dielectrophoresis (DEP)

- A dielectric particle in a non uniform electric field experiences a force
- Different potential fields and frequencies has an effect on the net force
- First studied in 1950s by Herbert Pohl



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Real World Application

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Dielectrophoresis

- Recently revived due to the ability to manipulate micro-particles and cells.
- Potential to separate particles in spinal fluid
- Act as filter
- Research in separating cancerous cells from healthy cells
- Separate platelets from whole blood
- Separate red and white blood cells
- Separate Strains of bacteria and viruses from living cells

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Project Description

- A system to aid in of DEP research
- Allow for quicker setup times
- Control Voltage and Frequency via the web
 - 1 to 60 VPP
 - 10k to 1Mhz
- Hold output for long time periods
- Small Form Factor
- · Easy to use
- Plug and play

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Project Structure

- Raspberry Pi
- Web Interface
- Web Server
- Frequency Control Solution
- Voltage Control Solution



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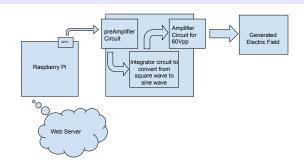
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Initial Implementation

- Raspberry Pi
 - Host web server
 - Remote manipulation of circuit output
 - Web interface can provide additional functionality
 - GPIO pins input to circuit
- Circuit Output
 - Frequency generated by GPIO pin
 - GPIO waveform integrated to get sine wave
 - Sine wave amplified to form output



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Concerns

- Raspberry Pi
 - Complexity of programming
 - GPIO pins may only be turned on and off
 - On-off mechanism must be used to generate waveform
 - Current load
- Circuit Output
 - · Complexity of construction
 - No guarantees about cleanliness of GPIO pin waveform
 - High risk of failure

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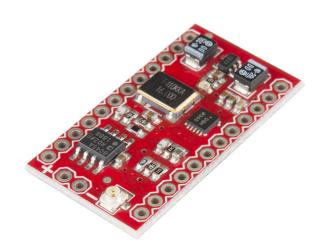
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Minigen Function Generator

- SPI communications
- Small form factor
- Output programmable frequency
- Produces 1 Khz to 4 Mhz waveforms



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Intermediate Design

- Raspberry Pi controls Integrated circuit components
- Minigen used to produce frequency
 - Digital Potentiometers
 - SPI communications
 - Vary resistance to control amplifier
- Amplifier controls voltage output from circuit

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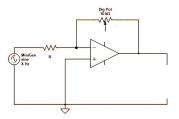
Questions

Digital Potentiometer Amplifier Circuit

Properties

Utilizes digital potentiometer as feedback resistor

$$V_{out} = \frac{-R_F}{R_{IN}} * Minigen_{SIGNAL}$$



Problems

- · Distortion of signal
- · Resistance drops with AC signal

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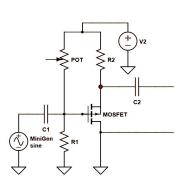
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MOSFET Amplifier

Properties

- Utilizes digital pot in a different way
- Amplification utilizes transistor



- Distortion of signal remains
- Concluded digital potentiometer is source of problem

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Redesign Amplifier

Idea Overview

- Previous problems stem from voltage modification solutions
- Solution: Use integrated circuit component to modify voltage

Amplifier Properties

- Three stages of amplification
- One PGA and two stages with constant gain
 - 20 V_{pp} per stage
 - Summing amplifier sums stages
 - PGA achieves 8 steps within one stage
 - Switches increase output by $20V_{pp}$
- Use transistors as switches flipped using GPIO pins

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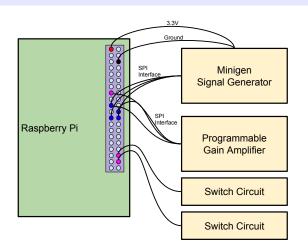
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Configuration

Programmable Gain Amplifier(PGA)

- Three pins encode gain
- 8 Gain Options from 0 to 7



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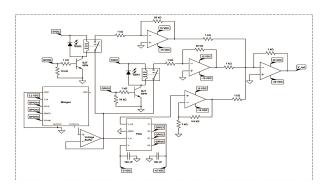
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SSR Circuit Implementation



Solid State Relay (SSR)

- Uses LED and photo-resistor to allow current though
- Hoped to fix waveform distortion issues

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Problems

Programmable Gain Amplifier(PGA)

- Easy to destroy
- Functionally works well

Transistor Switch Circuit

BJT Leaks when logically off

Solid State Relay

- Could not function at high enough frequency
- Even moderately high AC signals at input cause output of 0

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Overview

- Raspberry Pi controls integrated circuit components
- Minigen Function Generator
 - SPI communications
 - Produces frequency 10 Khz 4 Mhz
- Programmable Gain Amplifier(PGA)
 - GPIO communications
 - 8 voltage options (0-7)
- Two-stage amplification
- Summing Amplifier
 - Sums output from amplification stages

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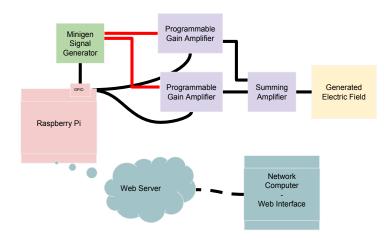
Inal Design

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Systems Diagram



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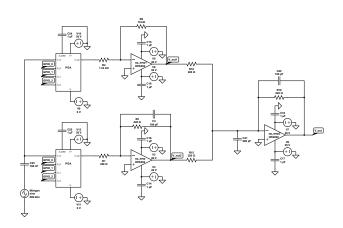
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Questions

Amplifier Circuit

- Two stages with PGA and constant gain amplifiers
 - Upper stage constant amplifier Gain 7.5
 - Lower stage constant amplifier Gain 1.07
 - PGA's both having variable gain
- Summing amplifier



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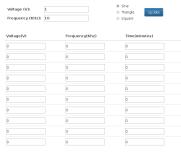
Current State

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Web Interface

- Hosted Locally
- Able to be seen on intranet
- Voltage and Frequency controls
- Provides Additional Functionality

Set Voltage and Frequency





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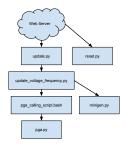
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Software Components



- Script organization of the Raspberry Pi
- Delegation of Responsibility
- Scripts correspond to hardware components

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Problems

- 1 Minigen B23 Bug
- 2 Current op-amps have insufficient Gain-Bandwidth Product
 - Insufficient frequency
 - 2 Insufficient voltage
- 3 Current draw from Raspberry Pi

Solutions

- Most probably a hardware issue
- 2 An op-amp with necessary specifications exists, 598-1449-ND
- 3 Ensure few additional components connected to the Pi

Cost

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Itemized Expenditures

Item	Quantity	Price(\$)
Raspberry Pi 3 Kit	1	49.99
Micro SD card	1	9.99
Minigen Functi- on Generator	1	29.95
Op Amps	3	4.41
PGA	2	8.00
Miscellaneous Components	-	10.5
Total	-	104.84

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Logistical Setbacks

- · Lack of manpower
- Loss of a team member at semester break
- Point of contact left company

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Deliverables

- · Raspberry Pi loaded with controlling code
- User manual
- Current circuit implementation
- PCB design
- Simulation files









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Question

Questions?

Discussion Points

- Dielectrophoresis (DEP)
- Circuit Design
- Digital Potentiometer/ Operation Amplifier
- MOSFET/ Programmable Gain Amplifiers (PGA)
- Web Interface
- Final Documentation

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Work Breakdown

Items

- Initial Planning
- Project Website
- Reports and documentation
- Circuit Design
- Web Server
- SOC Communications
- PCB Design