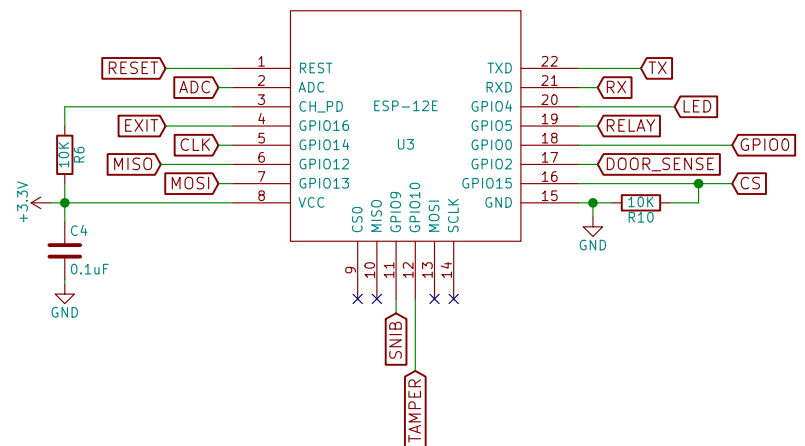
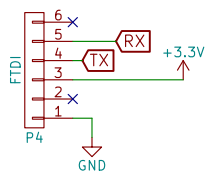
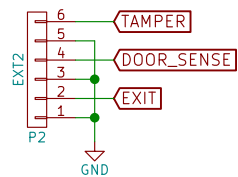


The left diagram shows a circuit where a 3.3V supply is connected to a 10K resistor (R9). The other end of R9 is connected to a GPIO pin. A piezo buzzer is connected between the GPIO pin and ground. The buzzer is labeled with pins 1, 2, and 3, and is connected to ground at pin 3. The buzzer is also labeled with 'Q2' and 'Q_NMOS_GDS'.

The right diagram shows a similar circuit. A 3.3V supply is connected to a 10K resistor (R11). The other end of R11 is connected to a RESET pin. A piezo buzzer is connected between the RESET pin and ground. The buzzer is labeled with pins 1, 2, and 3, and is connected to ground at pin 3. The buzzer is also labeled with 'Q2' and 'Q_NMOS_GDS'.



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