137 HW #7

1) True

2) I. DMA Controller
   II. 100 clock cycles

3) 200 Mhz * 32 bit = 6400 $\frac{\text{mbits}}{\text{sec}}$ * $\frac{1 \text{ byte}}{8 \text{ bits}}$

   = 800 Megabytes / sec

4) 200 Mhz * 64 bit = 12800 $\frac{\text{Mbits}}{\text{sec}}$ * $\frac{1 \text{ byte}}{8 \text{ bits}}$

   = 1600 MB/sec

5) 32 bits * $\frac{1 \text{ byte}}{8 \text{ bits}}$ = 4 bytes

   200 Mhz * 4 bytes = 800 mb/sec * 2

   = 1600 mbs