23WW6.5 Kit to test Hybrid core on MTL-P

**Kit Ingredients**

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Ingredient | version | Official BKC ? | Location | Change | Owner |
| dCode | v101.8020.070c.8020 | No | [Dcode-link](file:///\\amr\ec\proj\sv\Debug\PowerOn\MTL\MTL_P68_A0\Pcode_Dcode\dcode-mtl-mp-b0\v8101.8020.0.0) | 8018 Base Pcode + die biasing flow + cross over voltage | Kodua |
| pCode | v101.8020.070c.8020 | No | [location](file:///\\amr\ec\proj\sv\Debug\PowerOn\MTL\MTL_P68_A0\Pcode_Dcode\pcode-mtl-mp-b0\v101.8020.0.0) |  | Kodua |
| ucode |  |  |  |  |  |
| Bios | 3053 | No | [location](file:///\\amr.corp.intel.com\ec\proj\sv\Debug\PowerOn\MTL\MTL_P68_B0\IFWI\IFWI_Releases\OWR\2023WW06.2.2_MTL-P_B0_OWR_csme1613\Payloads) |  | Sumit |
| Ifwi | 6.2.1 | No | [location](file:///\\amr.corp.intel.com\ec\proj\sv\Debug\PowerOn\MTL\MTL_P68_B0\IFWI\IFWI_Releases\OWR\2023WW06.2.2_MTL-P_B0_OWR_csme1613) |  | Sumit |
| RWC weights | WW 4.1 | Yes | [HSD](https://hsdes.intel.com/appstore/article/#/22016467019) |  | Beja, Hadas |
| RWC Thresholds | WW 4.1 | Yes | [HSD](https://hsdes.intel.com/appstore/article/#/22016467019) |  | Beja, Hadas |
| Atom weights | WW 4.1 | Yes | [HSD](https://hsdes.intel.com/appstore/article/#/22016467025) |  | Beja, Hadas |
| Atom thresholds | WW 4.1 | Yes | [HSD](https://hsdes.intel.com/appstore/article/#/22016467025) |  | Beja, Hadas |
| PPM package | WW6.1 | No | [link](file:///C:\Users\sumitban\OneDrive%20-%20Intel%20Corporation\Documents\GitHub\HGS-kit\Tools-and-Automation-Scripts\enable-disable-parking-scripts) InstallOSsetting.cmd |  | Nikhil |
| Hetero parking script | WW6.1 | No | [link](file:///C:\Users\sumitban\OneDrive%20-%20Intel%20Corporation\Documents\GitHub\HGS-kit\Tools-and-Automation-Scripts\enable-disable-parking-scripts) HeteroParking.cmd |  | Nikhil |
|  |  |  |  |  |  |

**Test Results**

1. 8020 pcode/dcode integration results – Pending
2. PnP validation of the parking script - Pending

**Test Results**

1. Random hangs seen with hetero parking script with QDF Q0OT ([22016658699](https://hsdes.intel.com/appstore/article/#/22016658699) )
2. Performance issues seen with PPM script ([14018636848](https://hsdes.intel.com/appstore/article/#/14018636848))

**Platform Settings for all hybrid tests**

1. DDR5 Board
   1. reworks (Sumit will add reworks)
   2. Memory used (sumit will add memory details)
2. LPDDR5 board
   1. reworks (Sumit will add reworks)
   2. memory used (sumit will add memory details)
3. Silicon : MTL-H 682 B0 silicon
   1. 45W QDF ( Sumit will add non-buggy QDF)

**BIOS Settings for all hybrid tests**

1. Default settings
2. Ensure C-states is ON (Sumit will update path)
3. Ensure fast boot is ON (Sumit will update path)

**OS Settings for all hybrid tests**

1. Default settings
2. AC plugged -> Balanced mode by default.
3. Wi-Fi is enabled and connected.
4. Ensure fast boot is ON (Sumit will update path)

**BKM Documents**

|  |  |  |
| --- | --- | --- |
| Ingredient | Document | Demo/video link |
| How to get pcode/dcode version? | WIP | WIP |
| How to get weights and thresholds? | WIP | WIP |
| How to update weights and thresholds? | WIP | WIP |
| How to check current PPM settings? | WIP | WIP |

**Tools**

|  |  |  |  |
| --- | --- | --- | --- |
| Tool | Verison | location | Comments |
| CPUID dump |  |  |  |
| CPUID\_GUI\_tool |  |  |  |
| Disable VBS script |  |  |  |
| Enumeration tool |  |  |  |
| Freakout tool |  |  |  |
| HGS Heterostats tool |  |  |  |
| pTAT tool |  |  |  |
| Sysinternals |  |  |  |
| Trace Scripts |  |  |  |
| vTune package |  |  |  |
| socwatch |  |  |  |
|  |  |  |  |