

Ver.	Eff_Date	ECN No.	Author	Change Description
1.0C	10-29-10	E070201041043	P. Y. Wang (PDKD)	Migration to OA by Cadence AFM
<p>Approvals:</p> <p>Please refer EDW workflow to see detail approval records</p>				<p>Title</p> <p><b>TSMC 65 NM CMOS RF MIXED SIGNAL GENERAL PURPOSE PLUS 1P9M SALICIDE CU_LOWK 1.0&amp;2.5V PDK (CADENCE OA6.1 VERSION) (WITH UNDER-DRIVE 1.8V / 2.5V OVER-DRIVE 3.3V DEVICES) (INCLUDES: CRN65G+ 2.5V)</b></p>
				<p>Document No. : T-N65-CM-SP-018-K3</p>
				<p>File:</p> <p>PDK_CRN65GP_v1.0c_official_IC61_20101010_all.tar.gz</p> <p>Size: 584,943,544 (bytes)</p>

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1.0A	08-07-08	E070200830020	P. Y. Wang	Original (This is the first OA6.1 version of CRN65GP PDK)
1.0B	01-21-09	E070200904030	P. Y. Wang	<ol style="list-style-type: none"> <li>1. Change RTMOM layout from rectangle dummy OD to strip dummy OD.</li> <li>2. Enhance RTMOM layout connectivity from top metal layer to all RTMOM stack layers and the connection pin from top/bottom to "L-shape" increase route flexibility.</li> <li>3. Add 1p8m_5x2z0u RC Raphael Table in order to support parasitic devices.</li> <li>4. Fix mosfet layout pcell issue in "SDshrink" and "routePolydir" option.</li> <li>5. Solve Cadence tool issue of device hspiceD current probe problem</li> <li>6. Fix eldoD netlist out of two terminal resistors: rmod rmodwo rnpoly rnpolywo rpod rpodwo rppoly rppolywo (TSMC PDK fix the propMapping "nil l sumL w sumW" --&gt; "nil" this revise will avoid the sumL/sumW into l/w )</li> <li>7. Change the default value typo of mimcap_udc*(2T/3T)and the default value typo of mimcap* (2T/3T)</li> <li>8. Fix TSMC Utility -&gt; "Add MOM key license" error when user installs "MM" (mixed-mode pdk).</li> <li>9. Fix AMS netlist out due to Cadence AMS netlist out tool issue. (Resistors AMS netlist problem in serial/parallel in resistor segment)</li> </ol>
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