

IMPEDANCE TABLE

LAYER	IMPEDANCE		WIDTH	SPACE
	SINGLE END	DIFF		
L01 & L04	50 OHM	-----	6 MILS	-----
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L01 & L04	-----	90 OHM	6 MILS	8 MILS
L01 & L04	-----	100 OHM	5 MILS	8 MILS

FABRICATION NOTE

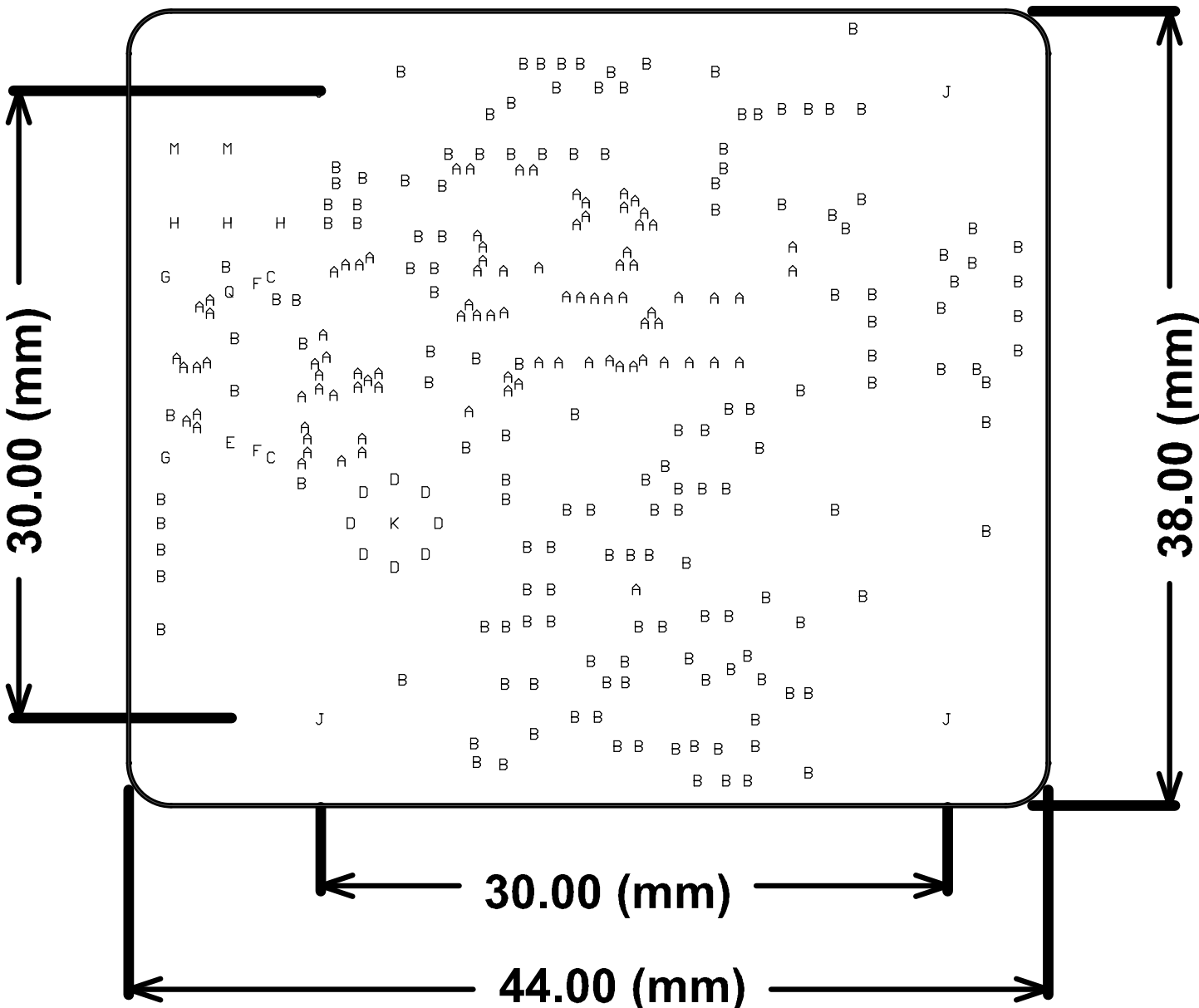
NOTES: (UNLESS OTHERWISE SPECIFIED)

1. FABRICATE PER IPC–6012B TYPE 2 CLASS 3
2. MATERIAL: BASE MATERIAL : FR4 Tg 180
3. BOARD THICKNESS: 1.6MM +/- 10% TOL.
4. NO OF LAYERS: 04
5. SURFACE FINISH: ENIG FINISH, 1u Gold
6. SOLDERMASK CLEARANCE IS GIVEN SAME AS PAD SIZE EXCEPT FIDUCIALS.
VENDOR SHALL MODIFY THE MASK CLEARANCE AS PER THE REQUIRED MANUFACTURING TOLERANCE
- COLOR: Matte Black
7. SILKSCREEN:DOUBLE SIDE WITH PERMANENT NON CONDUCTIVE WHITE EPOXY INK
SILKSCREEN MAY BE TRIMMED OFF ANY SOLDERED ENTITY.
8. BOW AND TWIST NOT TO EXCEED 0.180MM (0.007") PER INCH AS MEASURED PER IPC–TM–650.
9. 100%CONTINUITY TESTING USING DATABASE NETLIST SHALL BE PERFORMED.
10. VENDOR SHOULD FOLLOW ROHS COMPLAINT PROCESS FOR MANUFACTURING.

Layer	Name	Material	Thickness	Constant	Board Layer Stack
	Top Overlay				
	Top Solder	Solder Resist	0.80mil	3.5	
1	Top Layer		1.38mil		
	Dielectric 2	PP-006	3.94mil	4.05	
2	L2_GND	CF-004	0.69mil		
	Dielectric 1	Core-009	49.80mil	4.05	
3	L3_PWR	CF-004	0.69mil		
	Dielectric 3	PP-006	3.94mil	4.05	
4	Bottom Layer		1.38mil		
	Bottom Solder	Solder Resist	0.80mil	3.5	
	Bottom Overlay				

Total board thickness:

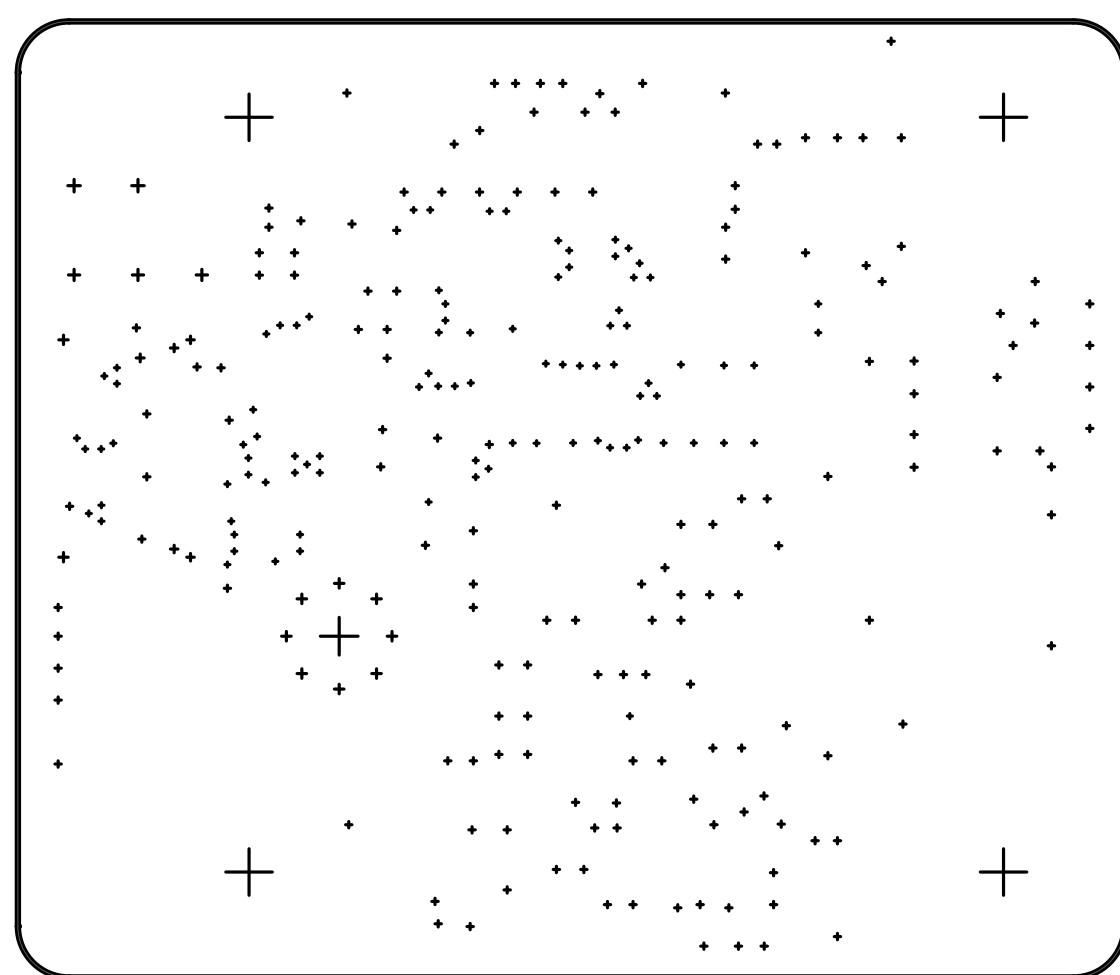
63.41mil



DRILL DRAWING/NO BOTTOM PASTEMASK
BOARD OUTLINE

Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair
A	90	8.00mil (0.203mm)	PTH	Round	Top Layer - Bottom Layer
B	155	10.00mil (0.254mm)	PTH	Round	Top Layer - Bottom Layer
D	8	15.00mil (0.381mm)	PTH	Round	Top Layer - Bottom Layer
E	1	19.69mil (0.500mm)	NPTH	Slot	Top Layer - Bottom Layer
F	2	23.62mil (0.600mm)	PTH	Slot	Top Layer - Bottom Layer
C	2	23.62mil (0.600mm)	PTH	Slot	Top Layer - Bottom Layer
Q	1	24.80mil (0.630mm)	NPTH	Round	Top Layer - Bottom Layer
G	2	29.53mil (0.750mm)	PTH	Slot	Top Layer - Bottom Layer
H	3	35.43mil (0.900mm)	PTH	Round	Top Layer - Bottom Layer
M	2	39.37mil (1.000mm)	PTH	Round	Top Layer - Bottom Layer
K	1	118.11mil (3.000mm)	PTH	Round	Top Layer - Bottom Layer
J	4	145.67mil (3.700mm)	PTH	Round	Top Layer - Bottom Layer
	271 Total				

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout



DRILL GUIDE
BOARD OUTLINE