

A

B

C

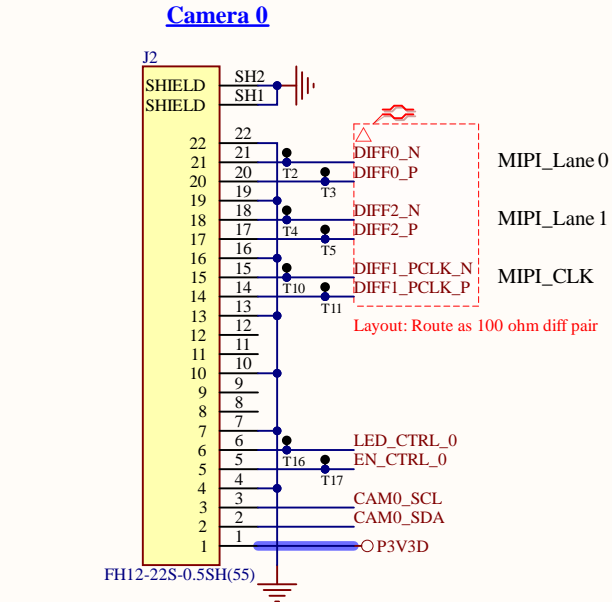
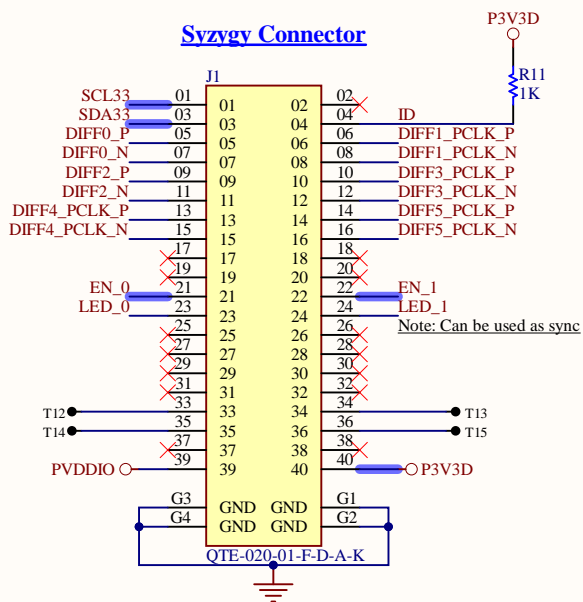
D

A

B

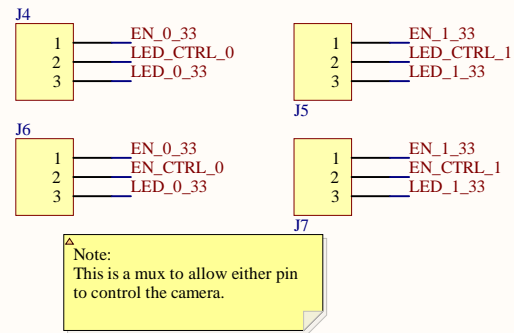
C

D

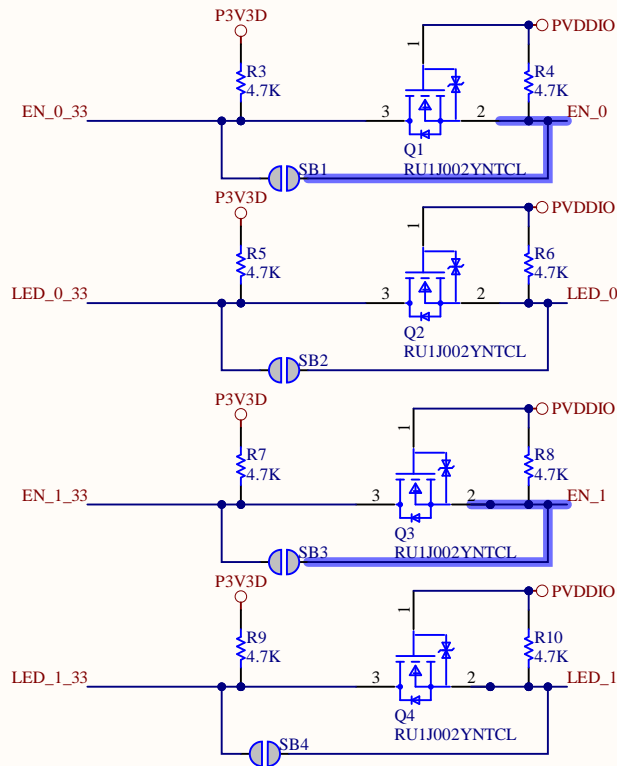


Layout Note: MIPI test points are exposed solder mask on vias
Layout Note: Place all TP's on Top side.

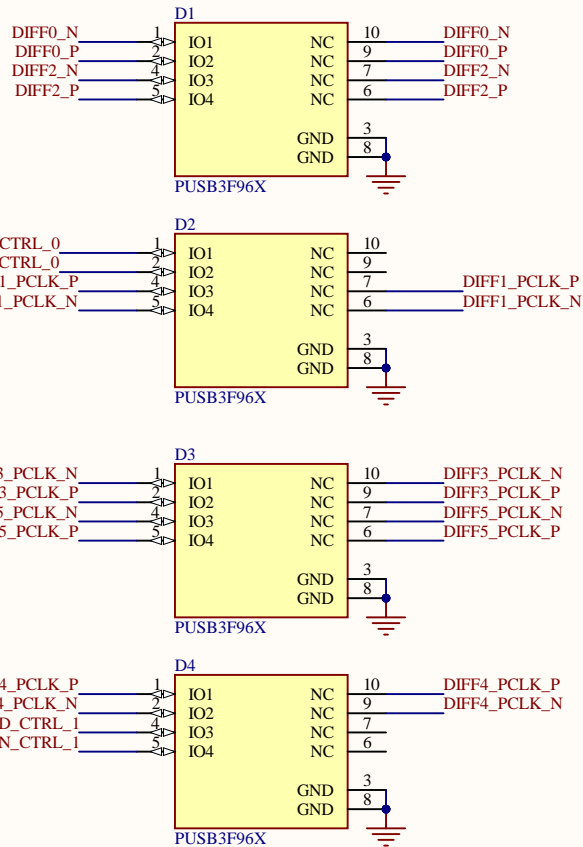
LED and Enable Selection



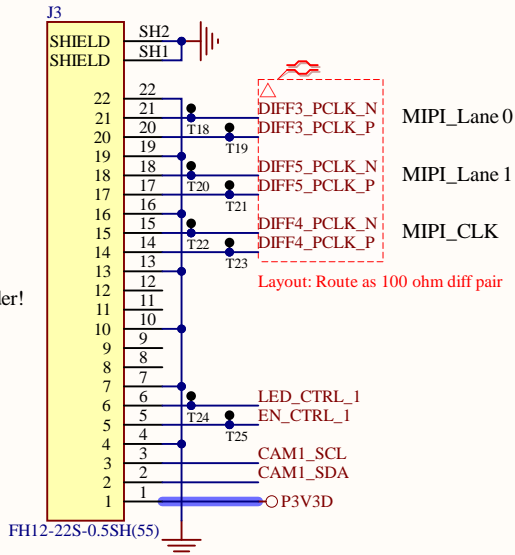
Level Translation



ESD Protection



Camera 1



I2C Mux

