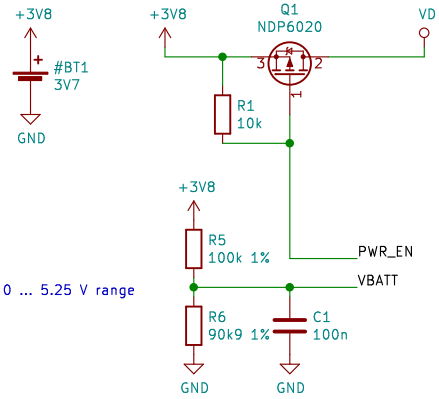
# Power




# Design

Connector: trigger
- 4x analog/digital input (active low or high)
- inputs are filtered

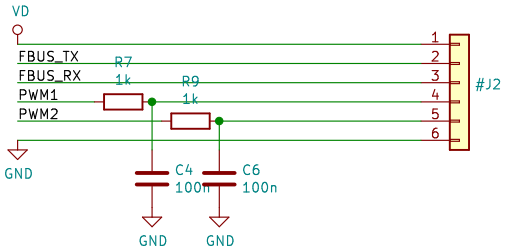
Connector: rpi zero - Nokia
- fbus (rx + tx)
- 2x analog out for battery faking
  * BSI: 100k PU to 2.8 V and batt resistor is 82k.
    Faked voltage should be 1.26 V
  * BTEMP: 100k PU to 2.8 V and batt NTC is 50k nom
    Faked voltage should be 0.93 V
- power (behind fet?)

Connector: programmer
- UPDI (1-wire if at reset pin)
- power and gnd

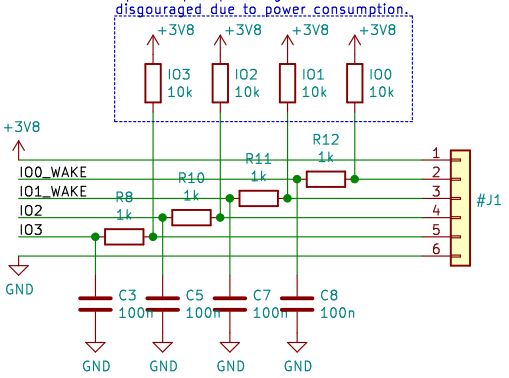
Interface: attiny - rpi zero
- i2c (for future)
- 1x digital event from rpi to attiny
- RPi 3V3 measurement for pwm calculations

Interface: attiny - power
- vbatt measurement
- enable power via P-channel fet

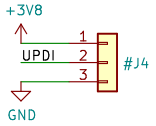
# Connectors



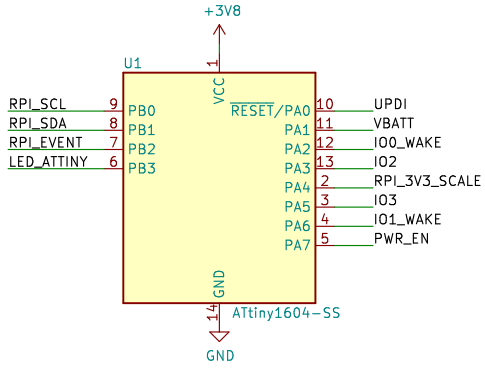

Optional pullups. Usage should be discouraged due to power consumption.

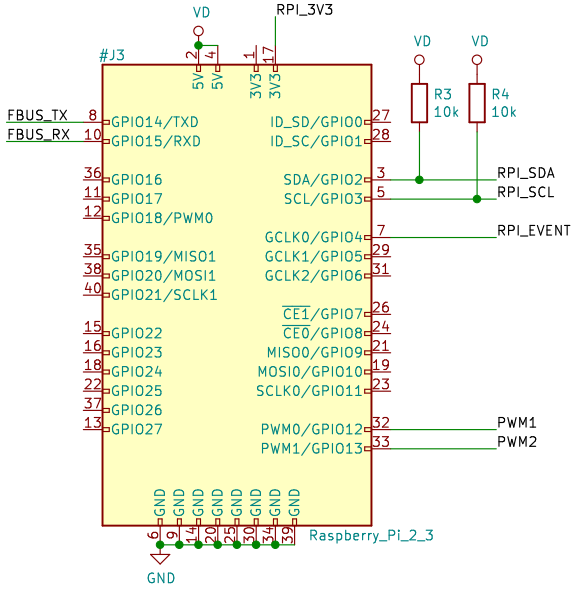


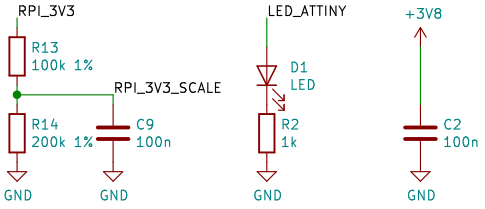





# MCU / RPi




| | | |
|---|---|---|
| Sheet: / | | |
| File: rpizero-lowpower.sch | | |
| **Title:** | | |
| Size: A4 | Date: 2019-06-27 | **Rev:** |
| KiCad E.D.A. kicad (5.1.2)-1 | | Id: 1/1 |