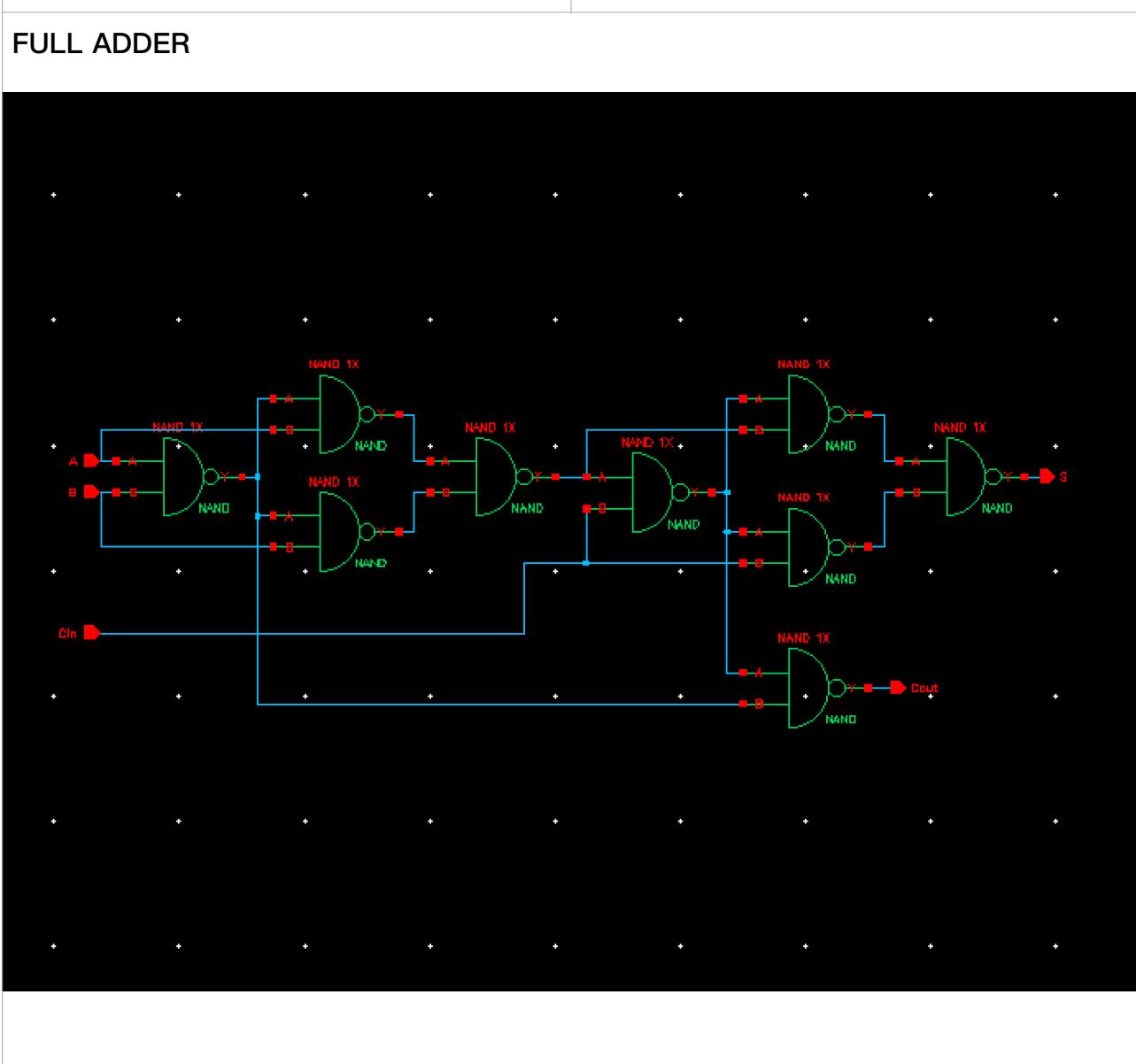
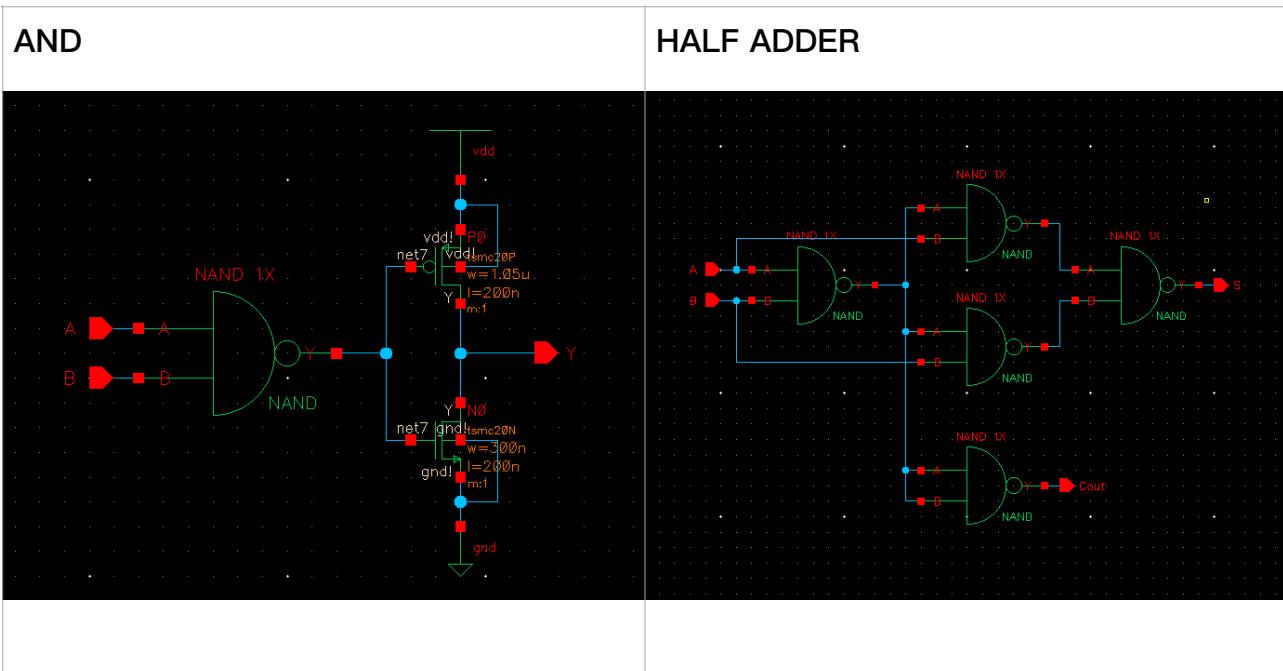
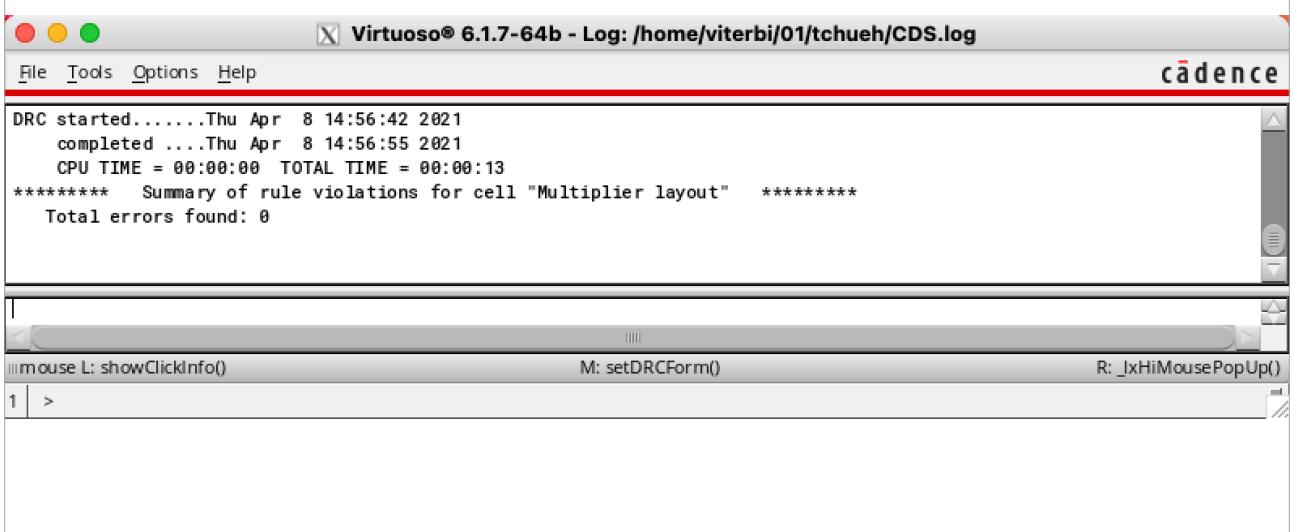
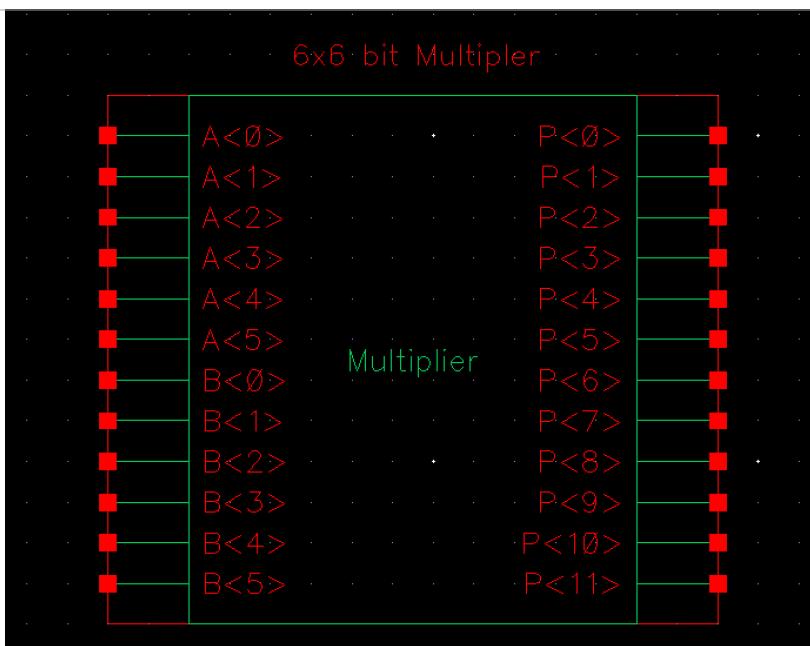
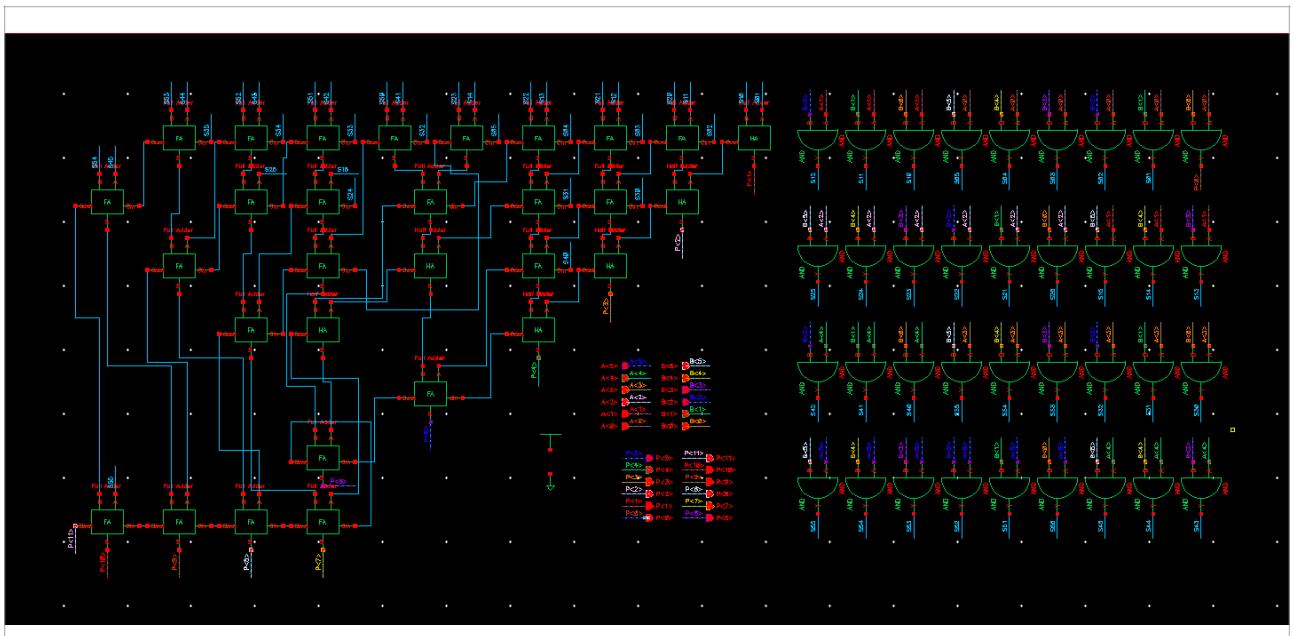
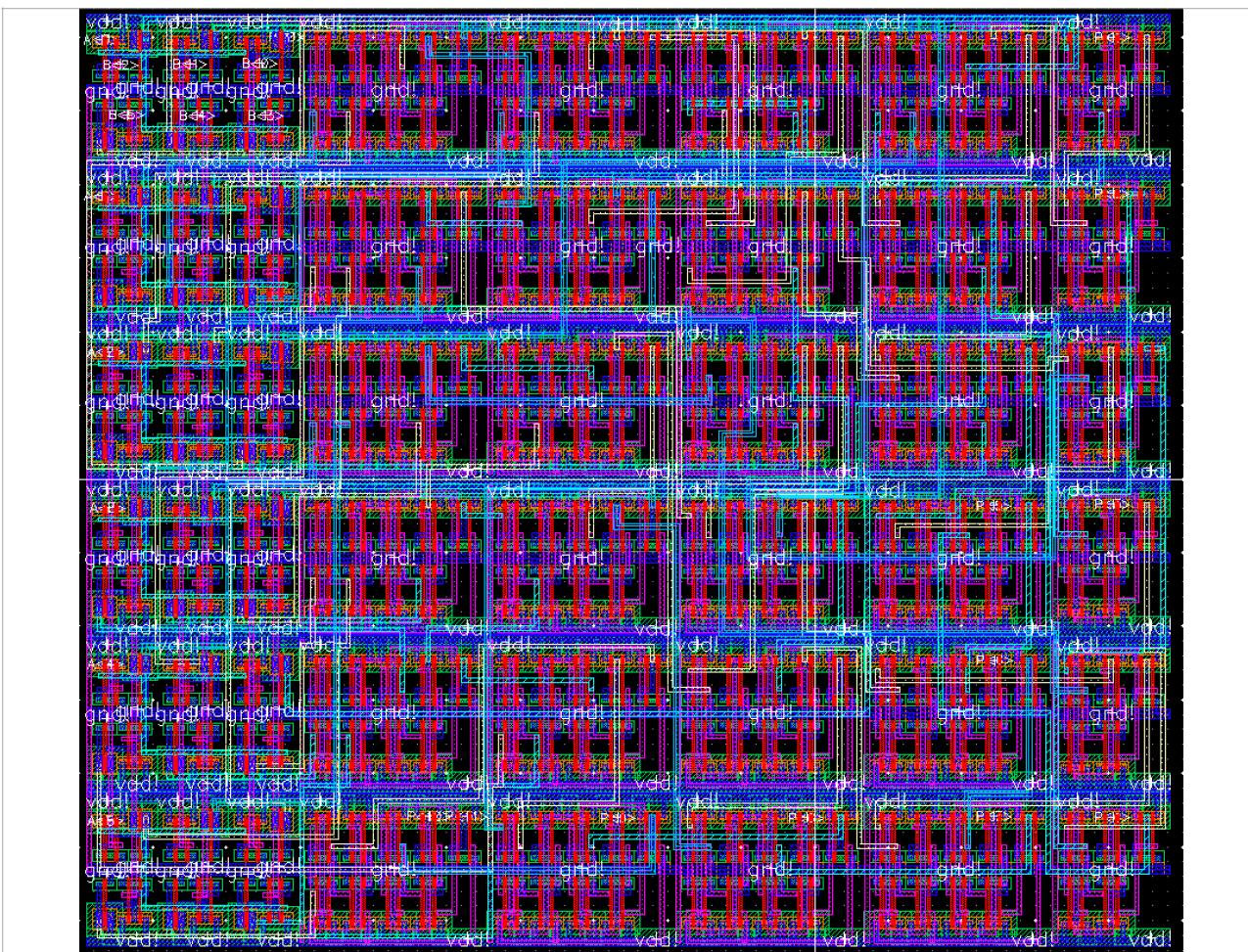


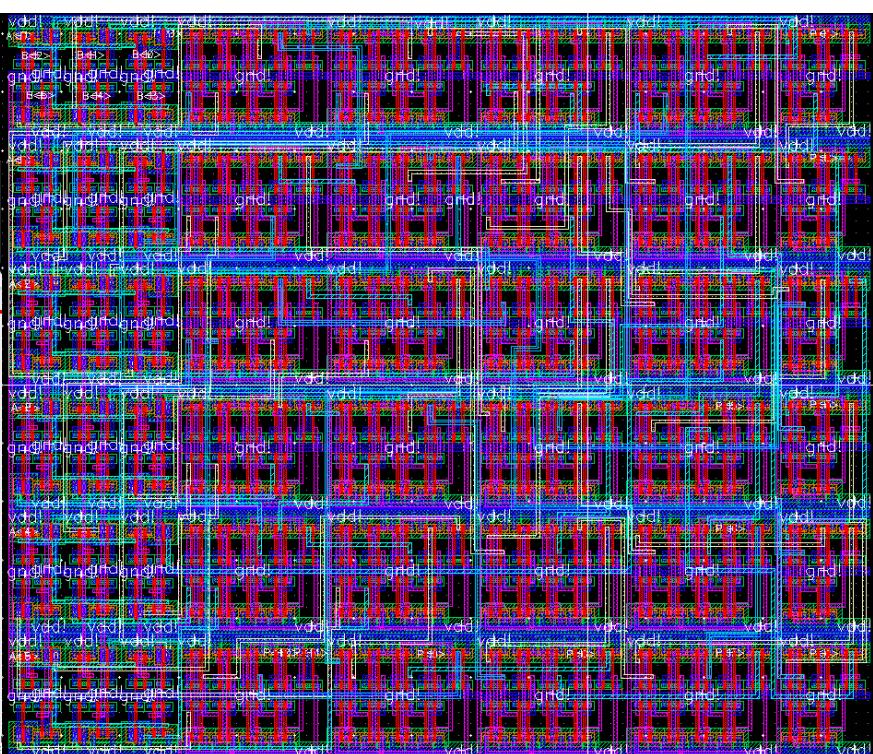
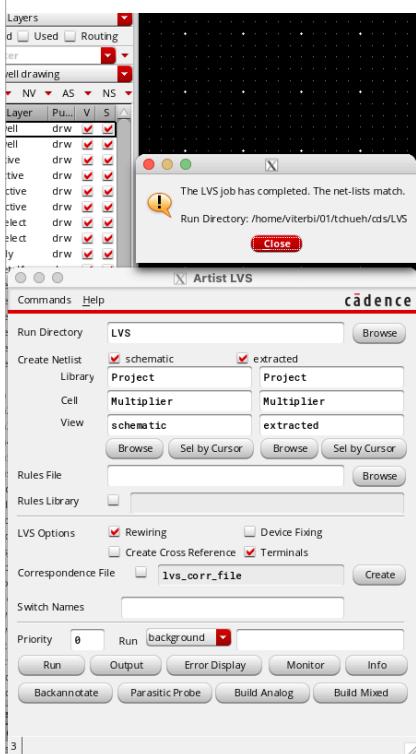
Ti-Shen Chueh 3751474091



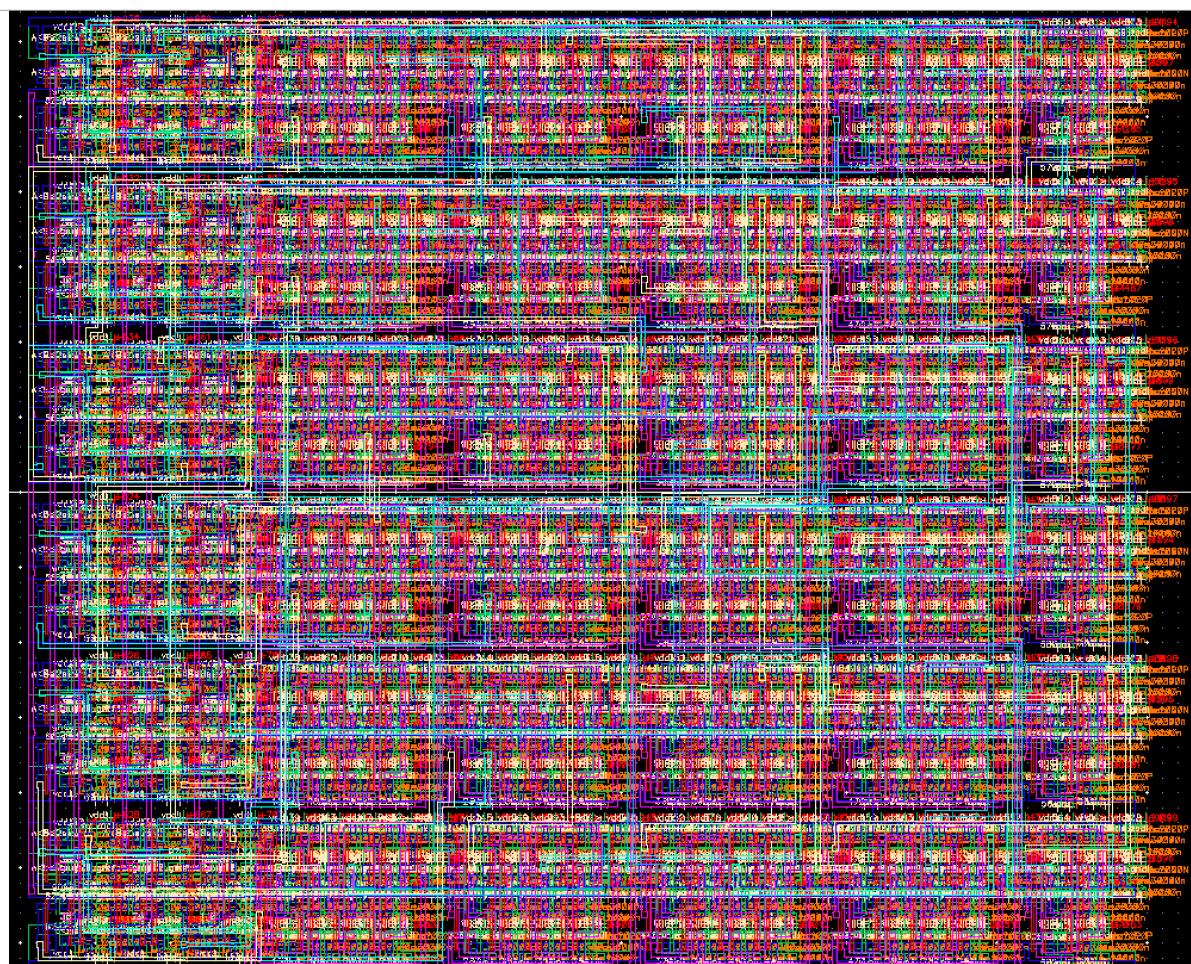
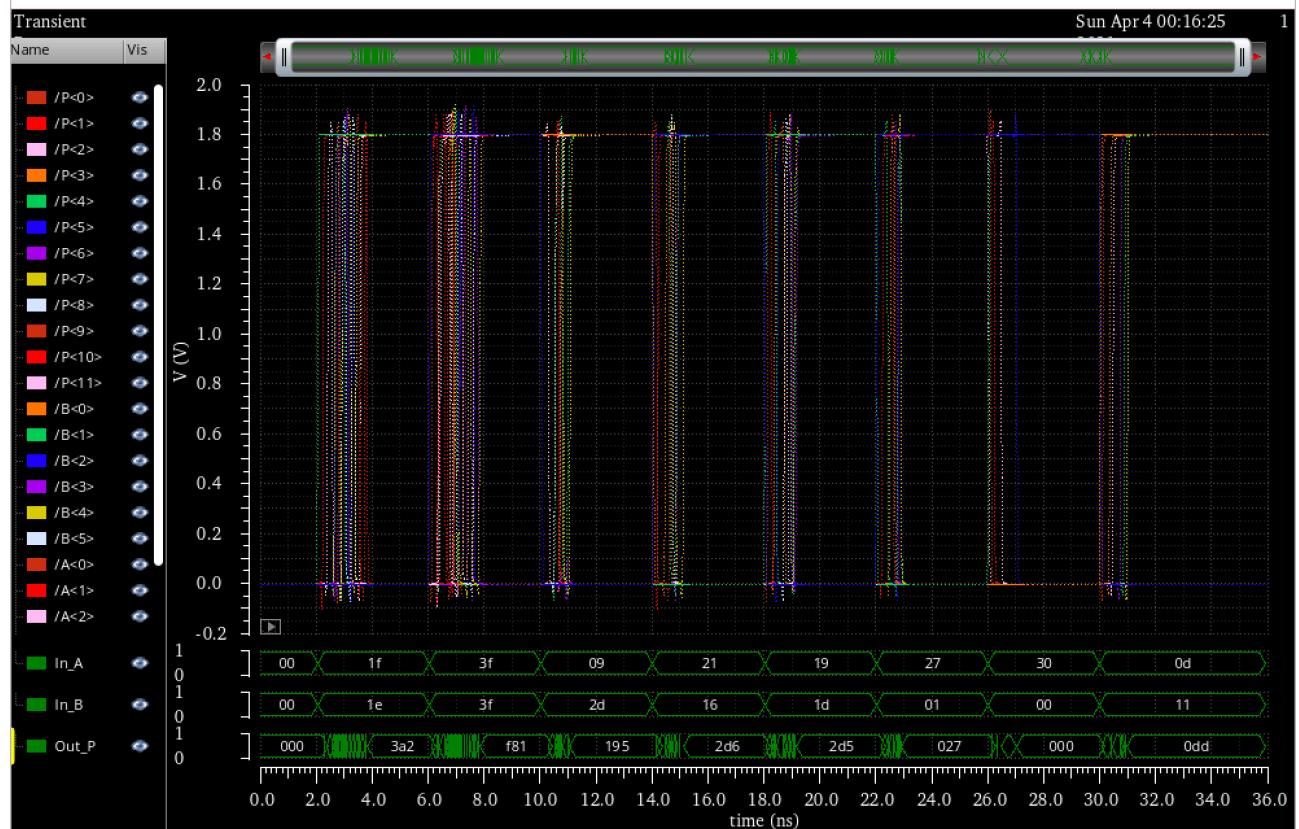




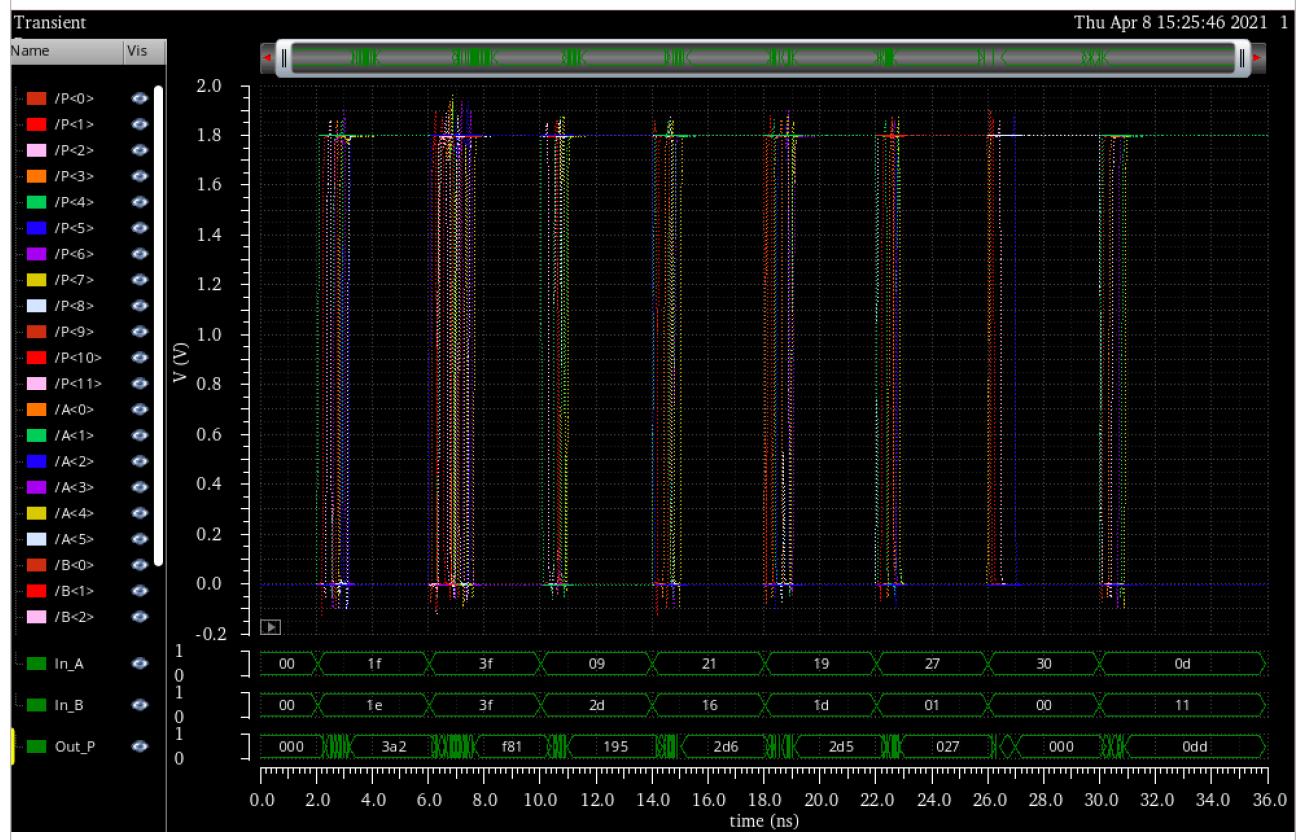
Multiplier Layout



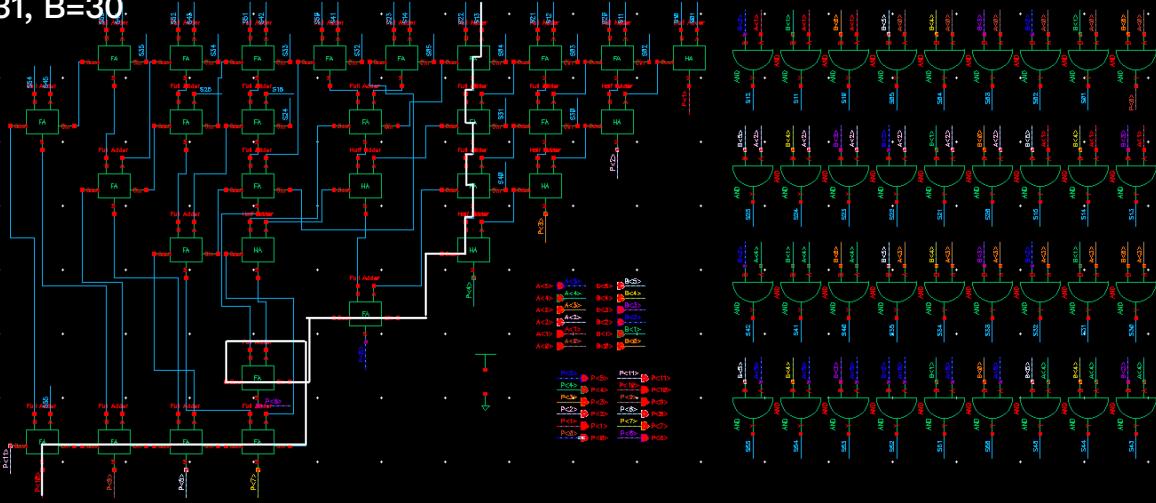
schematic delay

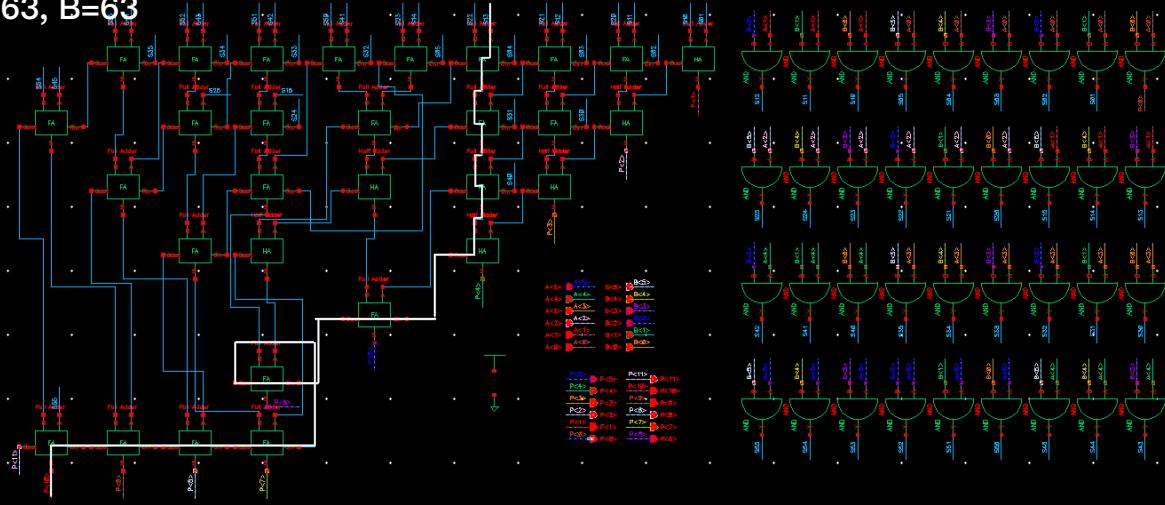
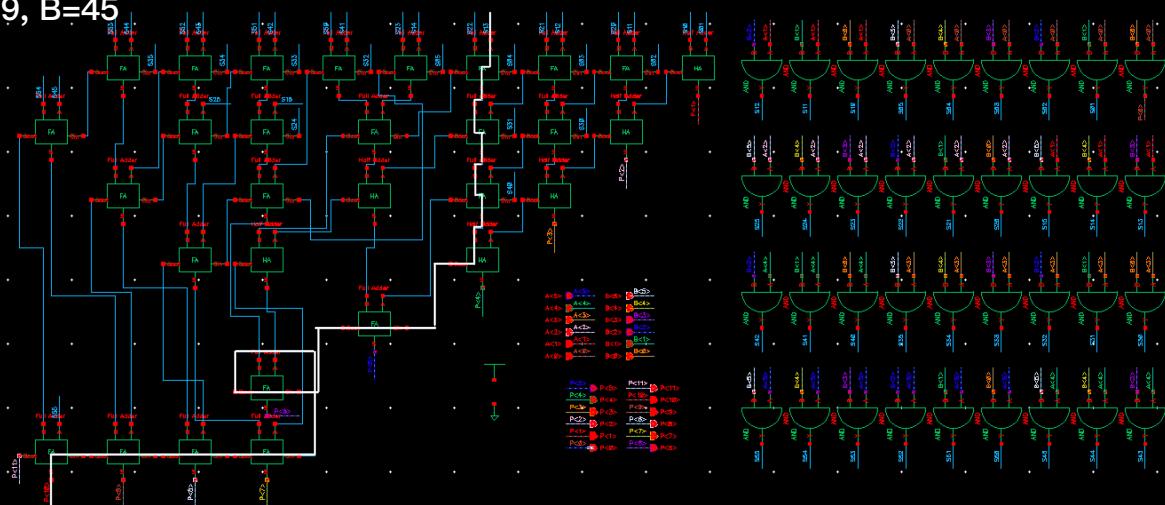
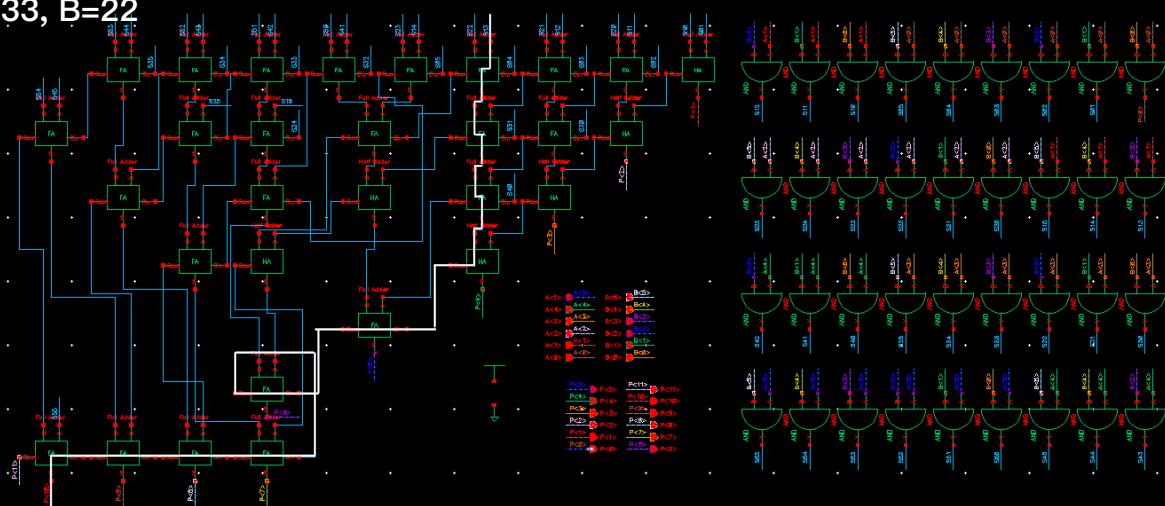


extracted view delay

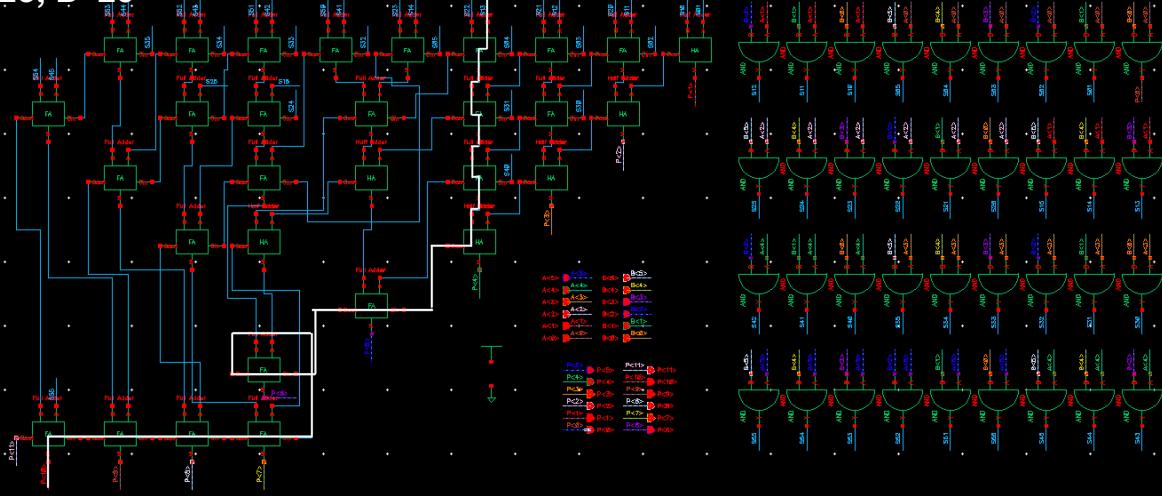


A=31, B=30

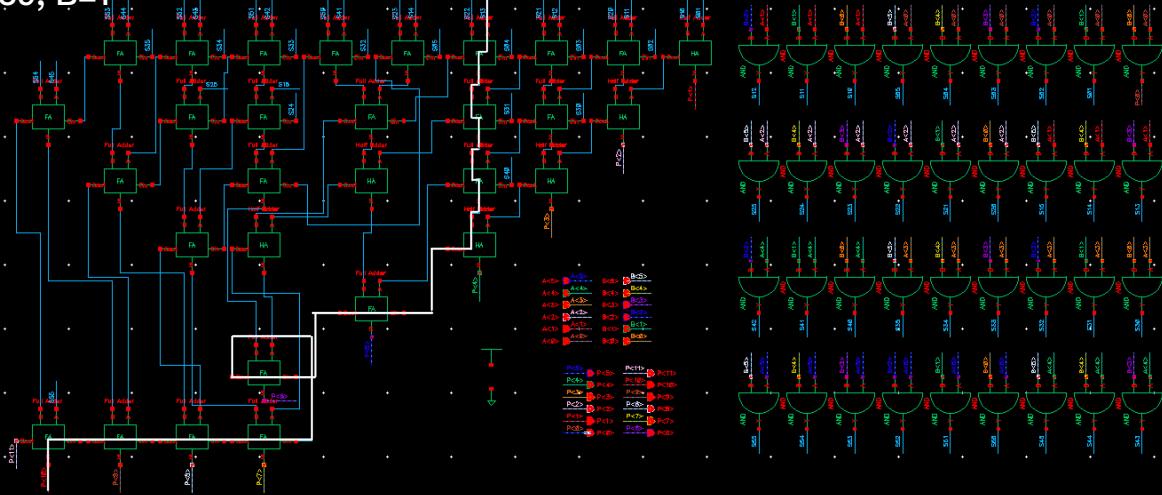


A=63, B=63**A=9, B=45****A=33, B=22**

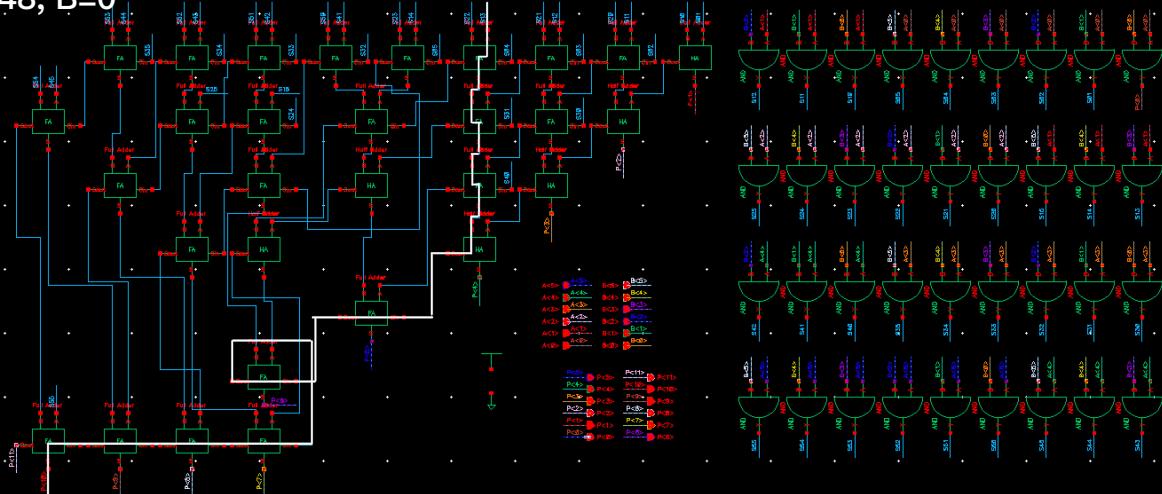
A=25, B=29



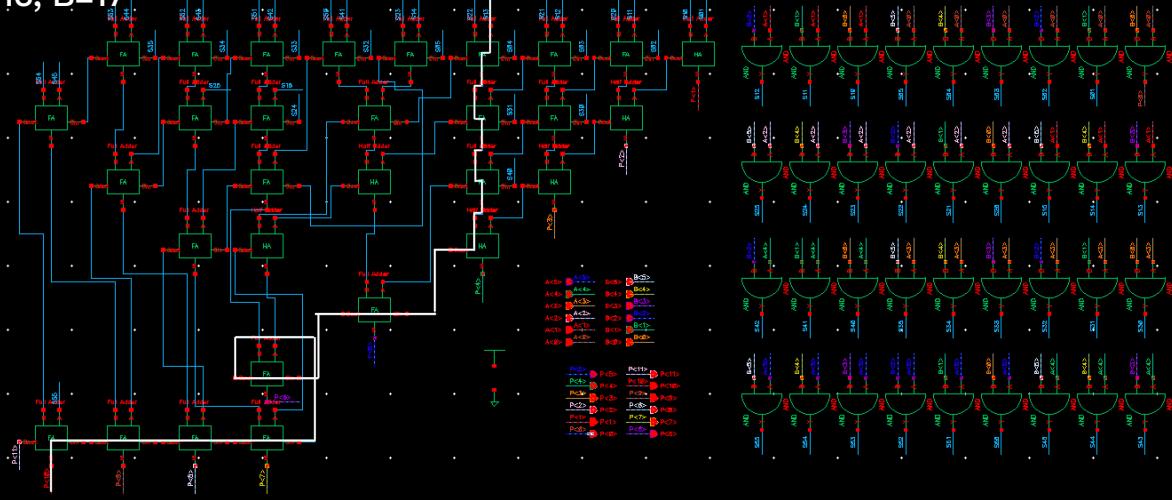
$$A=39, B=1$$

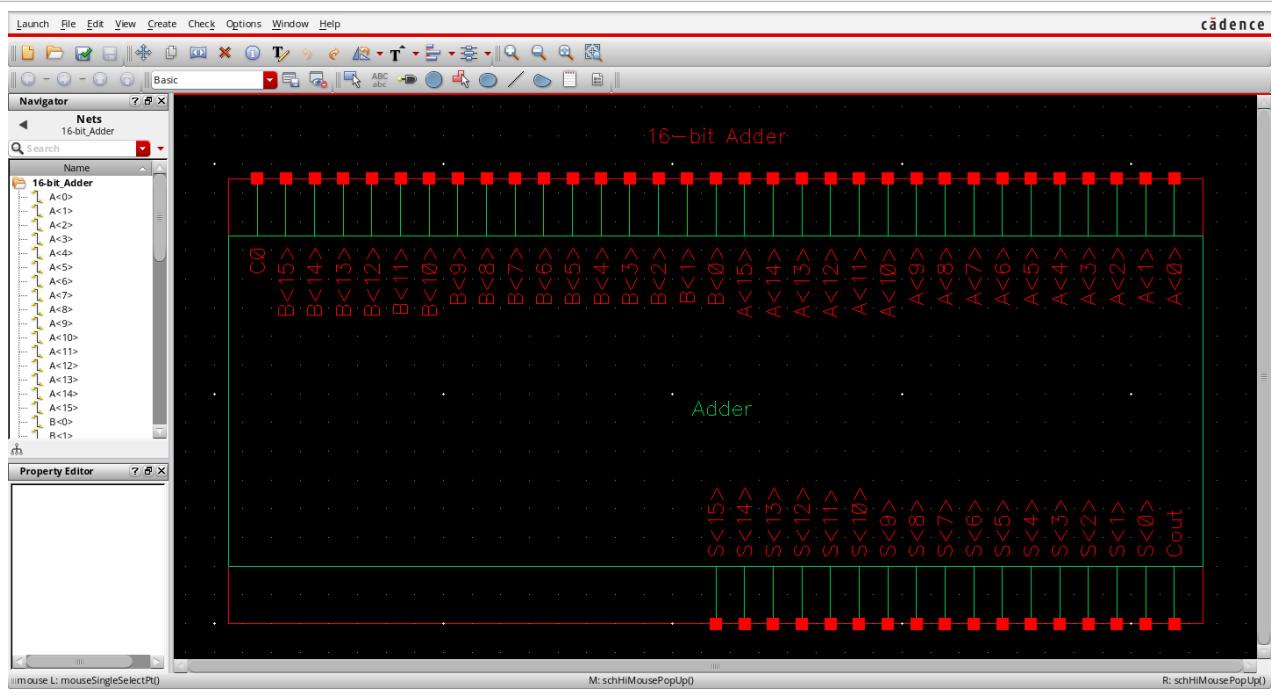
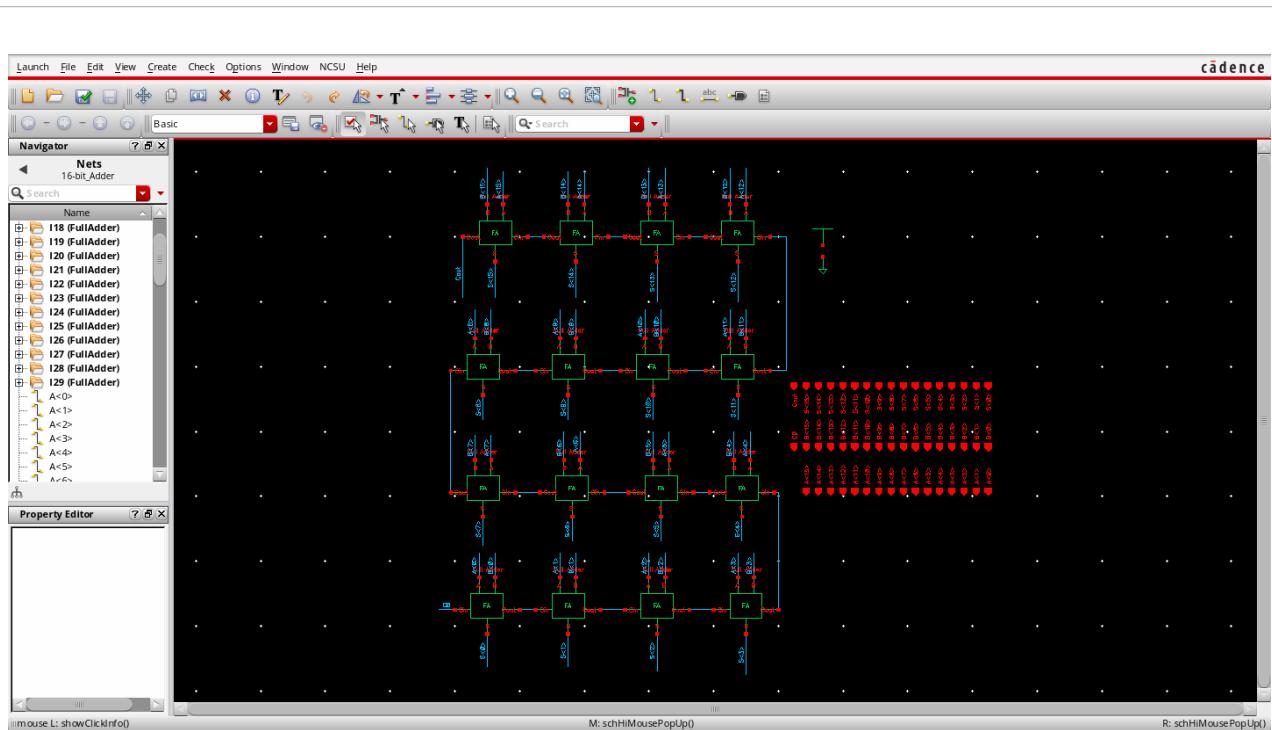


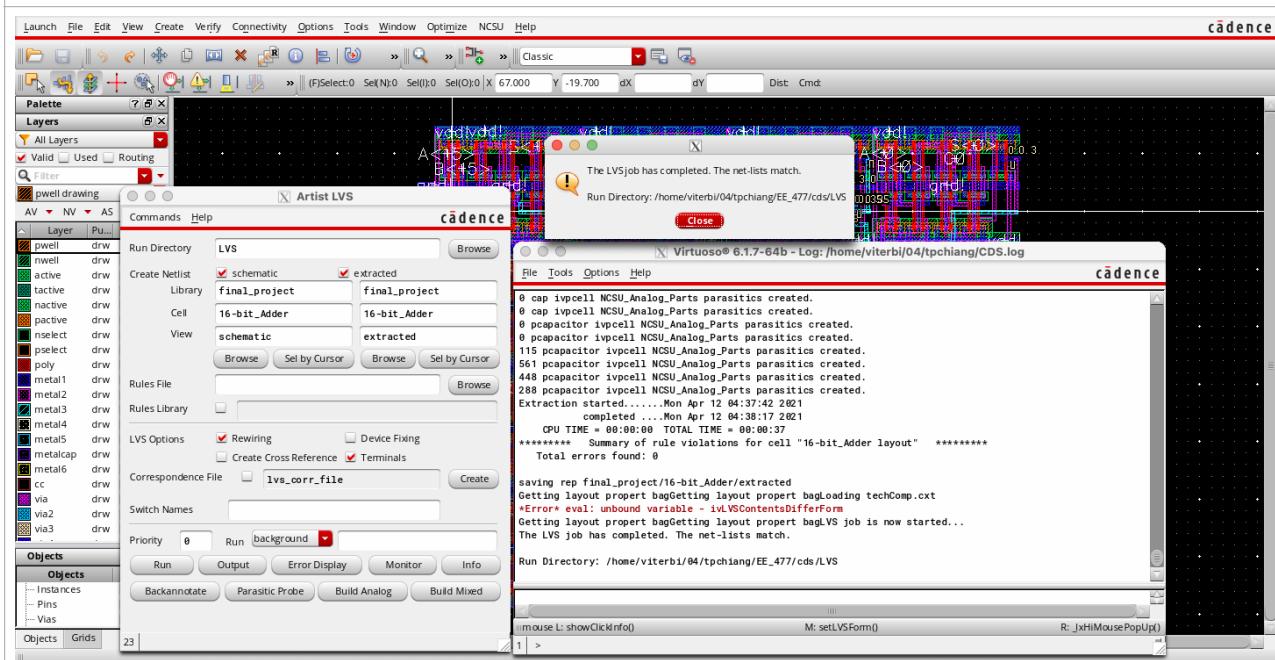
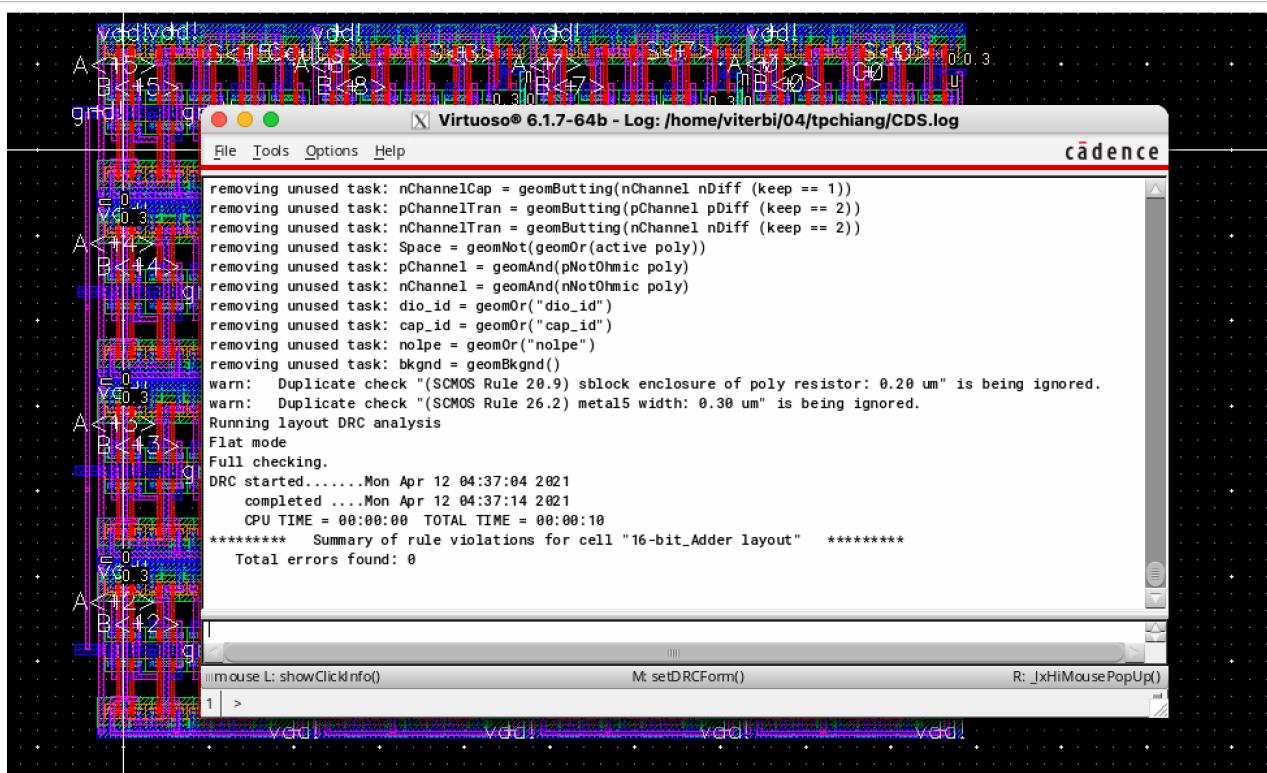
A=48, B=0

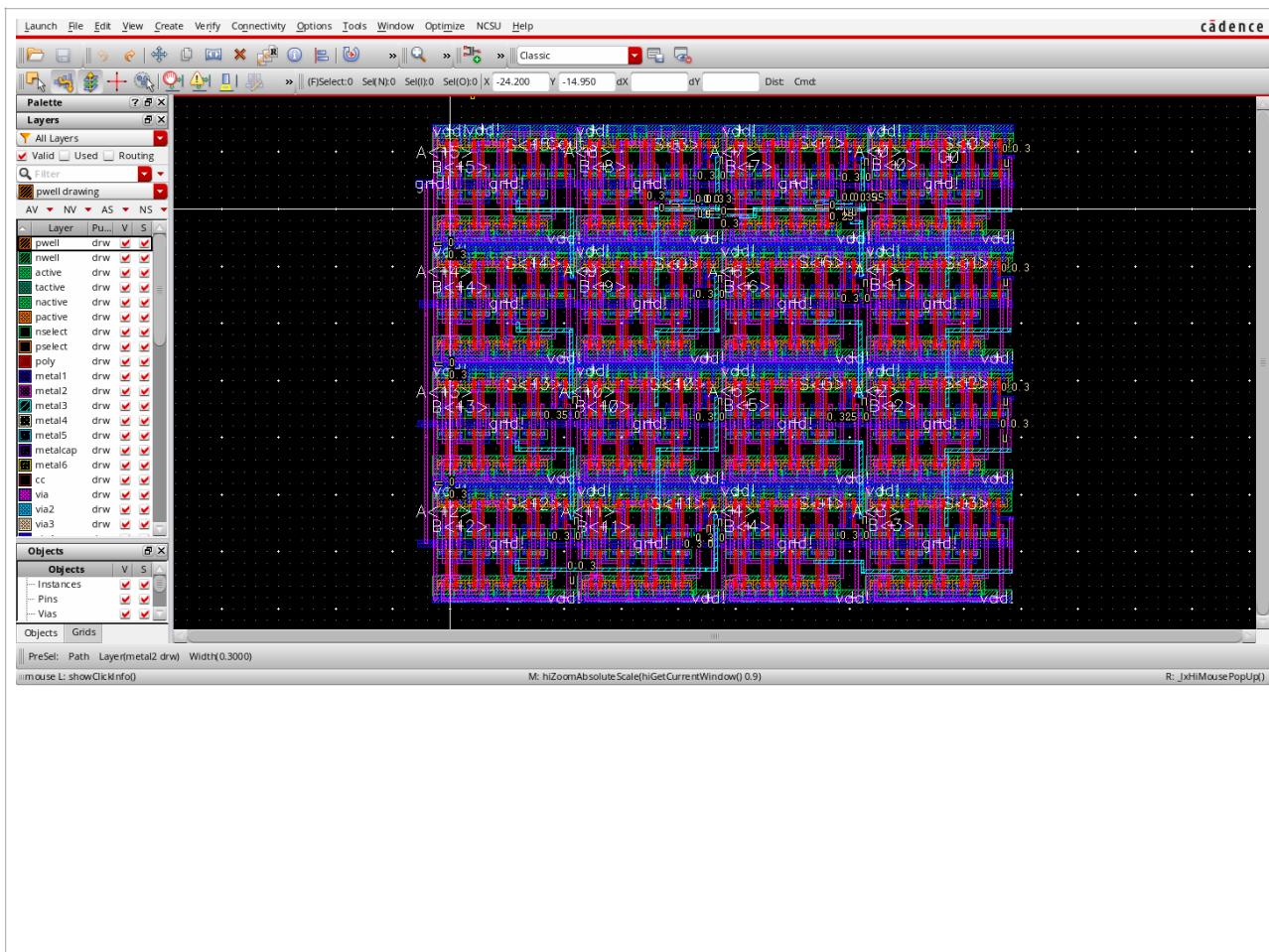


A=13, B=17









Schematic delay

