Branch Control Unit

Paria Pasehvarz , Mohsen Hassanzadeh

Instruction Type

B-Type

B-Type

B-Type

B-Type

B-Type

B-Type

default

Instruction

JAL

JALR

BEQ

BNE

BLT

BGE

PCSrcE

zero ? 2'b01

~zero ? 2'b01

neg ? 2'b01

2'b00

zero | ~neg ? 2'b01

jumpE == JAL ? 2'b01

jumpE == JALR ? 2'b10

Hazard Control Unit

XXX

XXX

XXX

XXX

Rs1D

XXX

XXX

XXX

XXX

XXX

XXX

Rs2D

XXX

XXX

XXX

XXX

RdM

XXX

XXX

XXX

XXX

XXX

XXX

XXX

RdE

XXX

XXX

RdW

XXX

XXX

XXX

XXX

XXX

RdW

XXXXX

XXXXX

XXXXX

XXXXX

XXXXX

XXXXX

XXXXX

XXXXX

RdM

RdW

XXXXX

XXXXX

XXXXX

XX

XX

XX

XX

XX

XX

00

		 •	

Rs1E == RdW && regWriteW

5 Rs2E == RdM && regWriteM

6 Rs2E == RdW && regWriteW

PCSrcE != 2'b00

4 Rs2E == 0

7 lwStall == 1

9 default

Pai	Paria Pasehvarz , Mohsen Hassanzadeh																			
#	Condition	Rs1D	Rs2D	RdE	RdM	RdW	Rs1E	Rs2E	PCSrcE	resultSrc0	regWriteM	regWriteW	luiM	forwardAE	forwardBE	stallF	stallD	flushD	flushE	
1	Rs1E == 0	XXX	XXX	XXX	XXX	XXX	XXXXX	XXXXX	XX	Х	Х	Х	Х	00	XX	0	0	0	0	
2	Rs1E == RdM && regWriteM	XXX	XXX	XXX	RdM	XXX	RdM	XXXXX	XX	X	1	Х	0	10 or 11	XX	0	0	0	0	

XXX

XXX

XXX

XXX

XXX

XXX

XXX

XXX

0

X

XXX

XXX

XXX

01

XX

XX

XX

XX

XX

00

XX

00

01

XX

XX

00

10 or 11

0

0

0

0

0

0

0

0

0

Main Control Unit Paria Pasehvarz , Mohsen Hassanzadeh Instruction_type Instruction **PCSrc** ResultSrc MemWrite **ALUControl ALUSrc ImmSrc** RegWrite ADD R-Type XX SUB R-Type XX SLTU XX R-Type R-Type SLT XX OR R-Type XX XX R-Type AND LW I-Type ADDI I-Type SLTUI I-Type I-Type SLTI **XORI** I-Type

XX

XXX

X

X

I-Type

S-Type

J-Type

J-Type

B-Type

B-Type

B-Type

B-Type

U-Type

ORI

SW

JALR

JAL

BEQ

BNE

BLT

BGE

LUI

zero ? 1: 2'b00

zero ? 0 : 2'b01

sign + ?1:2'b00

sign + or zero ? 1 : 2'b00