CS 331: Embedded Systems, Fall 2002 Midterm

Name:	
NetID:	

- Do **NOT** open the test until told to do so.
- Print your name and NetID in the boxes above. Also print your NetID on the upper right corner of every page.
- \bullet You may use one $8\frac{1}{2}''\times11''$ double-sided handwritten sheet of notes and a calculator.
- If anything is unclear, write down your assumptions.
- Show your work when appropriate. Clearly indicate your answers by circling them.
- You have exactly 1 hour and 15 minutes to complete the exam.
- Good luck!

#	Points	Score	Grader
1	10		
2	6		
3	6		
4	18		
5	12		
6	12		
7	12		
8	10		
9	14		
Total	100		

- 1. (10 points) Give short answers to the following questions.
 - (a) (3 points) Under POSIX, many different processes can use timers simultaneously. However, there is only one hardware timer interrupt. When the timer interrupt is raised, how can POSIX figure out which processes to send a SIGALRM signal? Draw a figure if it helps in illustrating your answer.

(b) (3 points) Should we use usleep(n microseconds) to implement a periodic task with a period of n microseconds? Explain why or why not.

(c) (4 points) In the Interrupt Model presented in class, the hardware does not automatically save all of the data registers when an interrupt is generated. Instead, the programmer must manually save them as part of the Interrupt Service Routine (ISR). Explain why this makes sense. Give an example if it helps in illustrating your answer.

- 2. (6 points) You are trying to observe a periodic signal with frequency components of 8Hz and 10Hz. But there is a high frequency noise spike at 80Hz.
 - (a) (3 points) What is a practical lower bound on the sampling frequency in Hz if Digital Signal Processing is used to filter the noise? Show your work.

(b) (3 points) What is a practical lower bound on sampling frequency in Hz if an analog anti-aliasing filter is used to remove the noise? Show your work.

3. (6 points) Is the following system stable, marginally stable, or unstable? Why? Show your work.

$$x'' - 9x' - 8x = 0$$

- 4. (18 points) An obscure A/D − D/A card uses 11-bit data registers consisting of a high byte and a low byte. The 11 bits are divided into the most significant 6 bits and the least significant 5 bits. The 6 bits are stored in the most significant bits of the high byte, and the 5 bits are stored in the most significant bits of the low byte. The port address of the low byte is 300H and the high byte is 301H. The card is configured to use a −10 ... +30 volt range for both input and output.
 - (a) (6 points) Write C or C++ code to read in the two bytes and assemble the 11-bit value in an unsigned int. Use the unsigned char hw_in(int port) function from the lab.

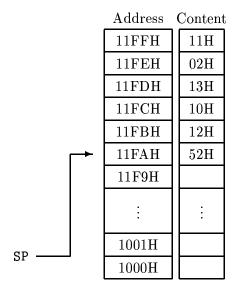
(b) (6 points) You just read in the 11-bit value "011 0010 1001" from an A/D channel. What voltage does it represent? Show your work.

(c) (6 points) What 11-bit value represents -2.75 volts? Show your work.

5. (12 points) Suppose that we have two new assembly commands named STAQADD and STAQSUB. STAQADD pops the 2 most recently pushed values, adds them, and pushes the result back onto the stack. STAQSUB pops the 2 most recently pushed values, subtracts them, and pushes the result back onto the stack. See the Intel assembly code below.

STAQADD	STAQSUB
POP AX	POP AX
POP BX	POP BX
ADD AX, BX $//$ AX := AX + BX	SUB AX, BX $//$ AX: = AX - BX
PUSH AX	PUSH AX

Suppose we have a stack that starts at segment 1000H (SS = 100H) and has a size of 200H (SP = 200H). Some values are pushed onto the stack and the result is shown here:



(a) (6 points) The STAQADD instruction is then executed. What will the stack look like afterwards? Annotate the picture below with your solution by filling in the stack contents and drawing an arrow to indicate where SP points.

	$\operatorname{Address}$	Content
	11FFH	
	11FEH	
	11FDH	
	11FCH	
	11FBH	
	11FAH	
	11F9H	
	÷	:
חי	1001H	
SP —	1000H	

S

(b) (6 points) Immediately after executing STAQADD in part (a), the STAQSUB instruction is executed. What will the stack look like afterwards? Annotate the picture below with your solution by filling in the stack contents and drawing an arrow to indicate where SP points.

$\operatorname{Address}$	Conten
11FFH	
11FEH	
11FDH	
11FCH	
11FBH	
11FAH	
11F9H	
:	:
1001H	
1000H	

- 6. (12 points) PID Control
 - (a) (4 points) If a system is moving to the set point too slowly, which gain should you change and how should you change it?

(b) (4 points) If a system has a steady state error (i.e., it cannot reach the set point), what should you do?

(c) (4 points) If a system's error tends to grow larger and larger, what can you say about the eigenvalues of the system?

- 7. (12 points) Square waves from Lab 2
 - (a) (2 points) What is the period in milliseconds of a 160 Hz square wave?
 - (b) (10 points) Suppose we tried to generate this 160 Hz square wave using our Lab 2 code on a new computer system that allows square waves to have a maximum frequency of 200 Hz. What is the period in milliseconds of the wave seen on the oscilloscope screen? Is your answer the same as that from part (a)? Why or why not?

8. (10 points) The function yowza(), which uses extended AT&T inline assembly, is defined below. What will be printed to the screen when "yowza(5, 10, 20)" is called? Hint: Note the positions of the colons and their meaning for read-only ("input") and write-only ("output") variables.

```
void yowza(int x, int y, int z)
1
2
           __asm__ ("movl %2, %1\n\t"
3
                      "addl %2, %1\nt"
                                            /\ast adds %2 to %1 and stores the result in %1 \ast/
4
                      "movl %1, %0\n\t"
5
                      : "=a" (x)
6
                      : "b" (y), "c" (z));
7
8
           cout << "x=" << x << endl;</pre>
9
           cout << "y=" << y << endl;</pre>
10
           cout << "z=" << z << endl;</pre>
11
12
```

9. (14 points) The Butterworth filter will decrease the magnitude of a signal according to the formula below; where ω is the original frequency, ω_{cutoff} is the cutoff frequency, n is the order of the filter, and θ is the attenuated signal (as a percentage of the original).

$$\frac{1}{\sqrt{\left(\frac{\omega}{\omega_{cutoff}}\right)^{2n} + 1}} = \theta \tag{1}$$

Suppose that we have a signal with a frequency of x and noise with a frequency of 5x. Compute the cutoff frequency and the minimal number of stages of the filter such that, after filtering, more than 90% of the signal is left and at most 10% of the noise is left? Note that your cutoff frequency will be in terms of x.