

TJARK PETERSEN

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EXPERIENCE

Student Project

Technical University of Denmark

📅 Sep 2021 – Dec 2021

📍 Copenhagen, Denmark

- Work on constrained random assembly program generation for processor verification purposes as an addition to the *ChiselVerify* hardware verification framework

Teaching Assistant

Technical University of Denmark

📅 Sept 2020 – Ongoing

📍 Copenhagen, Denmark

- Correcting assignments and helping students in lab sessions in the following courses:
 - 02138 - Digital Electronics 1
 - 02139 - Digital Electronics 2
 - 02155 - Computer Architecture
- Guiding students through a project concerning error correction using Hamming code in the course 31011 - Engineering Practices

Research Assistant

DTU Compute

📅 Sept 2020 – Dec 2020

📍 Copenhagen, Denmark

- Implement a priority queue in hardware as a use case to evaluate the *ChiselVerify* hardware verification framework for the hardware construction language *Chisel* [Dobis et al., 2021]

Internship

Rheinland Air Service GmbH

📅 Jan 2018 – Mar 2018

📍 Mönchengladbach, Germany

- Gained insight into all aspects of the maintenance process of aircrafts of the ATR family during an 8 week internship

STRENGTHS

Eager to learn Enduring Responsible

Computer Architecture Digital Design
Verification Embedded Systems

SKILLS

German	mother tongue
English	C1
Danish	C1
French	B2



EDUCATION

B.S. in Electrical Engineering

Technical University of Denmark

📅 Sept 2019 – Ongoing

PUBLICATIONS

Journal Articles

- Dobis, A., Petersen, T., Rasmussen, K., Tolotto, E., Damsgaard, H., Andersen, S., ... Schoeberl, M. (2021). Open-source verification with chisel and scala.

REFEREES

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