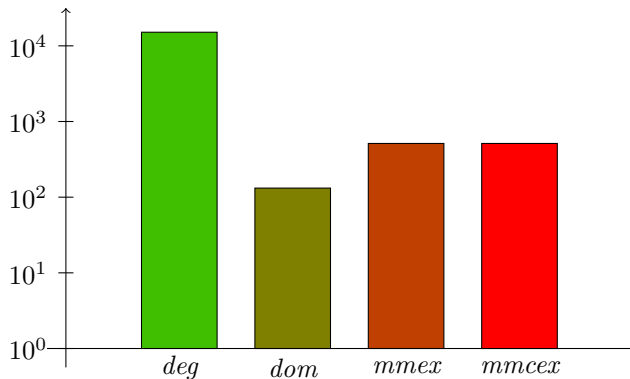
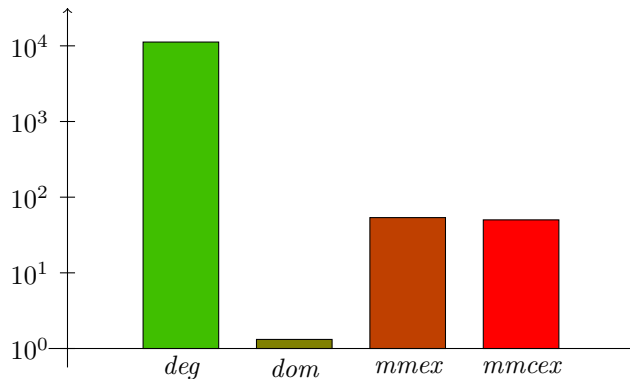


Parallelisation scheme 1



Parallelisation scheme 2



The average number of recursive calls of the INDUCTION procedure  
for the analyzed variable ordering schemes for the basic and inverse Tomita samples 3–7  
(GenuineIntel processor,  $p = 1, 8$  taken together)