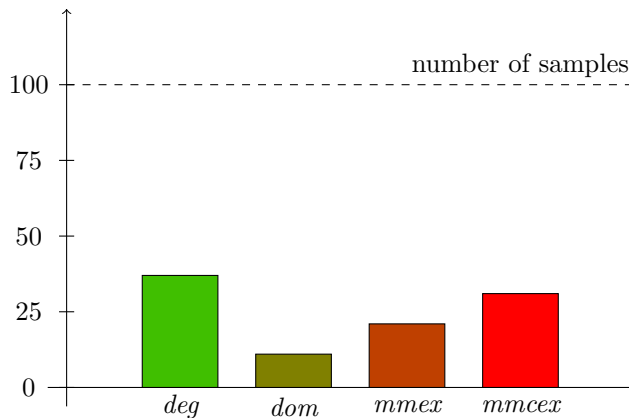
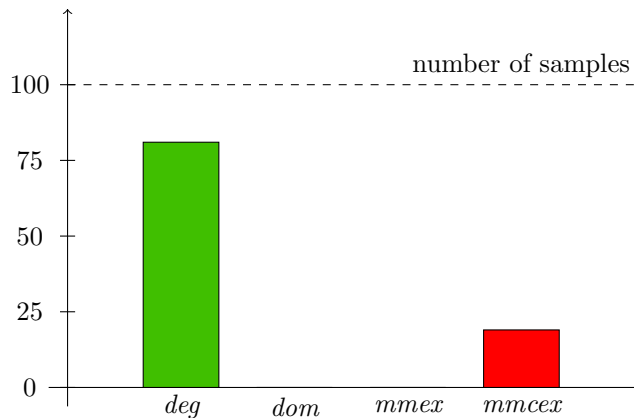


Parallelisation scheme 1



Parallelisation scheme 2



The number of times each variable ordering scheme found the NFA  
for the basic and inverse Tomita samples 3–7  
(GenuineIntel QEMU processor,  $p = 1, 8$  taken together)