Q: What are the three popular strategies for allocating free memory blocks to processes in dynamic memory partitioning? Explain briefly how each strategy works.

A: First-fit: chooses the first free block in the list that is large enough for the request. Best-fit: chooses the free block that is closest in size to the request. Next-fit: chooses the first free block that is large enough for request and comes after the "Last Allocated Block" in the list.

Q: True or False? Buddy Strategy always allocates memory in chunks of size power of two and uses a data structure based on a binary tree.

A: True

Q: Give an example drawing of a partially allocated memory and appropriate pointers in which each strategy ends up allocating a different free memory block to satisfy a memory request for 16 MB. Indicate clearly which strategy allocates which memory block

A: Refer to the example in Chapter 7 slides

Q: What interrupt is created when a desired page frame is not currently resident in RAM?

A: Page fault trap.

Q: How does the hardware 'know' that a desired page frame is not currently resident in RAM?

Q: What precisely does it mean if the 'dirty bit' is set for a page frame?

A: The page frame has been modified

Q: What is 'good' vs. 'bad' program locality? A: 'Good' locality means that the process executes in clustered

pages. 'Bad' locality means that the process executes in scattered pages.

O: Explain when/how internal fragmentation may occur A: When fixed-sized pages are used, the last page of a program may be partially filled. This is called internal fragmentation.

Q: Explain when/how external fragmentation may occur. A: Segmentation system breaks up the memory space into varia ble-sized pieces. After a sequence of allocation and deallocations, free memory may get fragmented into small pieces. Even if the total size of free memory is large enough to satisfy large memory requests, a large request may not be met due to the lack of continuity between small fragments. This is called external fragmentation. Compaction is needed to put free blocks into one large memory block

Q: What is a global allocation scheme?

A: Global replacement allows a process to select a replacement frame from the set of all frames, even if that frame is currently allocated to some other process; one process can take a frame from another

Q: What is a working set model?

A: The working set model assumes that processes execute in localities. The working set is the set of pages in the current locality Accordingly, each process should be allocated enough frames for its current working set.

O: Comparing global allocation vs. working set allocation, which would be more adversely affected by a program with 'bad' local ity? And WHY would that be true?

A: Working set allocation would be more adversely affected by a program with 'bad' locality. This is because the program with 'bad' locality has poorly defined working sets and therefore. many page faults are likely to occur.

Q: Which one of the following is not among the set of events that may take place between the time a page fault occurs and the time the faulting process resumes execution? (a) OS blocks the process and puts it into a wait queue. (b) One of the processes in the ready queue is selected to run. (c) A DMA is initi ated to load the page from disk into main memory. (d) A page replacement strategy is used to find a page frame to load the new page. (e) Page table is updated to reflect the change. (f) None of the above

Q: Which one of the following is not among the set of events that may take place between the time a page fault occurs and the time the faulting process resumes execution? (a) OS blocks the process and puts it into a wait queue (b) One of the processes in the ready queue is selected to run. (c) A DMA is initiated to load the page from disk into main memory. (d) The last page that the faulting process was executing is replaced with the newly loaded page. (e) Page table is updated to reflect the change. (f) None of the above

Q: True or False? While DMA (Direct Memory Access) is taking place, processor is free to do other things. The processor is only involved at the beginning and end of the DMA transfer A: True

Q: True or False? DMA uses "cycle stealing" to transfer data on the system bus. Each time cycle stealing is used, CPU is inter rupted.

A: False. The only interrupts that occur during DMA transfer is at the beginning and at the end of DMA (no interrupts in between) Q: What is the "largest" program that could execute on a machine with a 24-bit virtual address? A: 2^24 hyte

Q: What is the "largest" program that could execute on a machine with a 24-bit physical address?

A: Can't tell. Need to know the size of the virtual (logical) ad-

O: The address contained in a TLB entry <PTE> is (physical | log-

A: physical O: List at least 3 flags that are contained in a PTE

A: Valid bit; Reference bit; Dirty bit.

Q: Define hit-ratio in a memory management contex A: in a two-level memory (cache-RAM or RAM-Hard disk), the fraction of all memory accesses that are found in the master

memory (i.e. the cache) O: True or False? If a virtual page number X generates a miss in he TLB (Translation Lookaside Buffer), then the corresponding physical page number for X is guaranteed to be found in the Page Table Entry.

A: False. Not necessarily. X may not be resident in RAM (still in secondary memory)

Q: True or False? It is possible that page tables are stored in virtual (secondary) memory. A: True. When multi-level paging schemes are used.

O: True or False? In a virtual memory system with paging, page size must be large enough to offset the high cost of page faults.

O: True or False? The Least Recently Used (LRU) page replacement strategy is based on the principle of spatial locality (localit in space) as opposed to temporal locality (locality in time). A: False, LRU strategy is based on the temporal locality.

Q: True or False? Consider "clock policy" for page replacement, a ewly arrived page (i.e. just swapped in) will not get replaced before the clock pointer makes two full rotations in the circular buffer of candidate page frames.

A: True | Ise=1 at arrival use=0 after the first rotation of the clock pointer. If use=0 remains true after the second rotation, it may get replaced; otherwise, it stays on.

Q: True or False? In a virtual memory system with paging, you can run a program whose size is larger than the size of Main A. True

O: Draw a picture which shows the relationship between "Virtua Address (virtual page#, offset)", "Translation Lookaside Buffer". "Page Table", "Real Address (frame#, offset)", "Main Memory", and "Secondary Memory".

A: Slide #18 in CHAP8.ppt Q: Draw the flowchart for paging using TLB, Page Table, and

A: Slide #19 in CHAP8.ppt

Q: How does the kernel 'know' where on disk the desired information is for a non-resident frame?

A: If valid bit=0, Page Table Entry should contain the Disk ad-

Q: Describe what demand paging means.

The technique of only loading virtual pages into memory as they are accessed is known as demand paging. If the demand pages are not in memory, a page fault trap happens, and the oneration system swans them in

Q: Describe what prepaging means.

Prepaging brings in more consecutive pages than needed. If a virtual page X causes a page fault, then virtual page (X+1) is also brought in along with X. It is less overhead to bring in pages

that reside contiguously on the disk Q: If a desired page frame is not currently resident in RAM, _

A: A Page Fault

Q: Since paging system uses _____-sized pages, _____ fragmentation may occur

A: fixed size: internal

Q: If a memory management system uses dynamic partitioning, fragmentation may occur. A: External

Q: ____ is a form of I/O in which a special module controls the exchange of data between main memory and an I/O device. During this I/O transfer, CPU is free to do other computation A: DMA (Direct Memory Access)

Q: The least recently used (LRU) page replacement strategy is based on the principle of ____ as opposed to ____.

A: temporal locality: spatial locality Q: The top four levels in the memory hierarchy, starting with the fastest, are:

A: Registers; cache memory; RAM; Disk.

Q: Swapping out a piece of a process (i.e. pages of a process) just before that piece is needed is called _

O: True or False? A UNIX socket is used for communication between processes running on the same machine. On the other hand, an internet socket cannot be used for communication be tween processes running on the same machine.

A: False, Internet Socket can be used for processes running or the same machine as well as different machines

Q: True or False? If clients are connected to a server through connect()" and "accept()" calls and the server calls "listen(soc,2) before "accept()", then at most 2 clients can get connected to

the server at any time. A: False. "listen()" determines the size of the wait queue before the connections take place, not the max number of clients that

Q: True or False? In RAID (Redundant Array of Independent Disks) Level 1, every disk in the array has a mirror disk that con

A: True Q: True or False? In Client/Server architectures, OS and the platorms in the client and server machines must be the same

A: FALSE Q: True or False? In Client/Server applications, there is heavy en phasis on providing a user-friendly Graphical User Interface

(GUI) on the client side.

Δ· True

Q: True or False? In client/server applications, fat client models cannot take advantage of the desktop power and therefore can only serve a small number of clients.

O: True or False? First-Come-First-Served (FCFS) process schedling favors I/O-bound processes.

A- EALSE Q: True or False? Most antivirus software is based on program

A: True Q: True or False? RAID 2 (Redundant Array of Independent Disks with Level 2) is designed to provide error detection/correction.

A: True Q: True or False? User Datagram Protocol (UDP) provides unduplicated and reliable packet delivery. : FALSE

mulation and virus signature analysis.

O: True or False? Two periodic real-time processes A and B have periods T a=0.2 ms and T b=0.5 ms respectively Eurthermore. their execution times are C_a=10 micro sec. and C_b=40 micro sec. respectively. If Rate Monotonic scheduling is used A has higher priority than B.

O: True or False?

The following I/O devices are sorted correctly in decreasing or der according to the typical data rates that they can sustain: Gigabit Ethernet, firewire 800, laser printer, hard disk, keyboard, and modem.

A: FALSE O: True or False? DMA uses "cycle stealing" to transfer data on the system bus. Each time cycle stealing is used, CPU is interrupted.

A: FALSE O: Which of the following strategies is not used in a Dick Sched uling Algorithm? (a) First in first out (FIFO); (b) Last in first out (LIFO): (c) Shortest service time first (SSTF); (d) Longest service time first (LSTF); (e) Back and forth over disk (SCAN)

O: Explain what the following C calls do both when the call is successful and when it is unsuccessful.

(1) socket(AF_INET, SOCK_STREAM, 0);

(2) bind(sd. (struct sockaddr*)&server_addr.sizeof(server_addr)) (3) socket(AF_INET, SOCK_DGRAM, 0);

(4) accept(sd. (struct sockaddr*)&client addr. &client len) A: (1) creates an internet stream (TCP) socket and returns the

socket descriptor. If the call fails, it returns -1. (2) Binds the definition of a socket (socket descriptor) to a port

number. If the call fails, it returns -1 (3) Creates an internet datagram (LIDP) socket and returns the

socket descriptor. If the call fails, it returns -1. (4) Blocks execution until a client connection is received. When that happens, it returns a descriptor for the connection. If the call fails, it returns -1.

Q: Which one of the following is not among the 7-layers defined for ISO Open Systems Interconnect (OSI) model? (a) Application (b) Routing: (c) Transport: (d) Data Link: (e) Physical

O: Which of the following are among the direct goals of process scheduling algorithms: (a) improve response time; (b) minimize interrupts: (c) improve throughput: (d) minimize page faults: (e) improve turnaround time for jobs; (f) increase memory efficience A a c e

Q: When we compare clusters with SMP (Symmetric Multiprocessors), which of the following are true? (a) Clusters are easier to manage and configure: (b) Clusters take up less space and draw less power: (c) Clusters are better for incremental and absolute scalability: (d) Clusters are superior in terms of availability (e) Clusters have superior price/performance

A·c d e O: Which of the following malicious software need a host program to operate? (a) Logic Bomb; (b) Worm; (c) Zombie (bots); (d) Trojan Horse; (e) Virus

A: a, d, e Q: Which of the following scheduling policies may cause starvation for certain jobs? (a) First Come First Serve (ECES): (b) Feedback; (c) Round Robin; (d) Shortest Process Next (SPN); (e)

Shortest Remaining Time Next (SRT) A·h d e

Q: Which of the following features are specific to Real-Time OS? (a) Small size: (b) Fast context switch: (c) Less user control: (d) Nondeterministic delays: (e) Fail-safe operation

Q: In which one of the following OSI layers Transmission Contro rotocol (TCP) and User Datagram Protocol (UDP) are defined and implemented? (a) Application; (b) Physical; (c) Transport; (d)

Data Link; (e) Session

O: What does an Internet Protocol do? A: (1) Provides a naming scheme which uses a uniform forma for host addresses; (2) Provides a delivery mechanism by defin-

ing a standard packet format. Q: True or False? Sockets are bidirectional communication ports in UNIX. Once a socket is created, it can be bound to an interne port using "socket()" call.

A: False First statement is true but the 2nd statement is false Sockets can be bound to an internet port using "bind()" call. O: True or False? There is only one internet port in each net worked host.

A: False. There are many internet ports in each host; some are reserved by the OS O: True or False? The UNIX call "listen(soc N)" allows only N clients to be connected to a socket at any time.

A: False. N specifies the length of the wait gueue for the clients who are waiting to be connected

Q: True or False? The two lowest layers in the 7-layer ISO Open systems Interconnect (OSI) model are Physical and Data Link lay ers and their primary function is to implement the TCP/IP proto

A: False. First part of the statement is true but the second part is false, because Transport layer (which is the 4. layer from the bot om) implements TCP/IP.

Q: What are the possible goals that any scheduling policy might try to accomplish (list at least three)? A: To improve: response time, Turnaround time (TAT), Through

put, Processor Efficiency O: True or False? Long-Term scheduler controls the degree o multiprogramming

Or True or False? Among the three scheduling disciplines (long term, medium-term, and short-term), long-term scheduler exe utes most frequently. A: False Short-term scheduler (dispatcher) executes most fre

quently. Q: True or False? Among the short-term scheduling policies, feedback policy penalizes jobs that have been running longer

Q: Which decisions are made by Long-term, Medium-term, and Short-term scheduling? Be brief.

A: Long-term scheduling: Determines which programs are admitted to the system for processing. Controls the degree of mul tiprogramming.

Medium-term scheduling: Determines which programs will be resident. Part of the swapping function. Swapping-in decision i ased on the need to manage the degree of multiprogramming Short-term scheduling: Determines which program will be eye cuted on CPU next. Known as the dispatcher. Executes most fre quently.

O: Name 3 things that are essential to launch a "bots" attack: x (1) attack software, (2) a large number of vulnerable ma chines. (3) locating these machines (scanning or fingerprinting) Q: The two lowest layers in the 7-layer ISO Open Systems Interconnect (OSI) model are ____ and ____ layers and their primary

function is to provide and : Physical; Data Link; signaling technology; frame management Q: Two transport protocols, ____ and ____ are defined and handled at the Transport Laver.

A: Transmission Control Protocol (TCP); User Datagram Protocol (LIDP)

(1) ____ and ___ are generally credited with the invention of C/Univ?

A. Dennis Richie, Ken Thompson

(2) ____ and ____ started Microsoft in 19____ A Bill Gates Paul Allen 1975 (3) What corporation/laboratory may fairly take credit for invertions like the mouse, windows, pull-down menus etc.?

A. Xerox/PARC (4) and co-founded Apple. then started NeXT. and is now the CEO of Pixar?

A. Steve Jobs, Steve Wozniac, Steve Jobs (5) MS/DOS was 90% derived from a predecessor product named ____ which was written by ____ and owned by ___ which in turn had been cloned from ____ written by _ A: QDOS, Tim Patterson, CL Computer Products, CPM, Gary Kil-

(6) What did Steve Jobs see while visiting PARC that inspired him to build a different kind of computer?

What did he see that he completely ignored? A: object oriented programming and E-mail What was the 1st computer that he built based on this inspira tion (that flopped)?

What was the 2nd one that didn't flop?

A; Macintosh (7) What 'product' got Microsoft into the microcomputer soft-

ware husiness? A: BASIC language interpreter (8) What lucky event got Microsoft into the operating system

market? A: Gary Kildall didn't eagerly pursue IBM when they requested a new OS. His wife and attorney would not sign a nondisclosure agreement. Bill Gates of Microsoft saw this as an opportunity

nd jumped in. (9) What company purchased NeXT and their OS NExTStep? What year?

A Apple in 1996

(10) What is a 'killer application'?

A: Software that's so useful that people will buy computers just to run it (11) What was the killer app for the Apple II?

A· VisiCalc (12) What was the killer app for the IBM PC?

(13) What was the killer ann for the Apple Macintosh? : WYSIWYG - What you see is what you get ----> Desktop Publishing

(14) Why didn't IBM create their own OS for their 1st PC? wanted to manufacture and market it very fast; within oneyear "...Once IBM decided to do a personal computer and to do t in a year - they couldn't really design anything, they just had to slap it together, so that's what they did ...'

(15) Who 'should have' sold IBM their operating system for the

A: Gary Kildall of Digital Research

A- ROM-RIOS

(16) What was the one part of the 1st IBM PC that was proprietary (that Compag had to later reverse engineer)?

(17) Why did IBM decide to build the PC using 'open architec-

A: To save time, instead of building a computer from scratch, IBM initially decided to buy PC components off the shelf and as emble them -- in IBM terms, this was called an 'open architecture'. IBM made some changes to this initial decision.

What was the almost immediate result of IBM having made that decision?

A: IBM had to buy the OS and other software from other

(18) What was IBM's motivation for designing/building PS-A: IBM planned to steal the market from Gates with a brand new

OS called OS/2 (19) What person ____ what company ____ built the 1st com-

mercially available personal computer in 1975? A: Ed Roberts; MITS

(20) Gordon Moore is one of the founders. A: Intel

(21) World's first personal computer. was designed by and was introduced in 19

A: Altair 8800: Ed Roberts: 1975

(22) The first mass market PC company is A: Apple

Sase Address – an address that is used as the origin in the calculation of addresses i

the execution of a computer program

Dynamic Relocation – a process that assigns new absolute addresses to a computer tion so that the program may be executed from

If main storage dekexed Access – pertaining to the organization and accessing of the records of a orage structure through a separate index to the locations of the stored records dekexed Sequential Access – pertaining to the organization and accessing of the re-rds of a storage structure through an index of the keys that are stored in arbitrarily

partitioned sequential files state In First Out (LIFO) – a queuing technique in which the next item to be retri-is the item most recently placed in the queue Logical Address – a reference to a memory location independent of the current signment of data to memory. A translation must be made to a physical address to

Aemory Partitioning - the subdividing of storage into independent section

n virtual storage, a fixed length block that has a virtual address and that is red as a unit between main memory and Page Fault - occurs when the page containing a referenced word is not in main ory. This causes an interrupt and requires that the proper page be brought into

Paging – the transfer of pages between main memory and secondary memory Physical Address – the absolute location of a unit of data in memory (e.g., word or byte in main memory, block on secondary memory)

Real-Time System – an operating system that must schedule and manage real-time

entered

a file in which records are ordered according to the values of one of sequential File – alle in which records are ordered according to the values of one more key fields and processed in the same explanes from the beginning of the file Server – (1) a process that responds to request from clients via messages. (2) in a network, a data station that provides facilities to other station; for example, a file server, a print server, a mail server.

Specifies—the use of secondary memory as buffer storage to reduce processing of any when transferring data between perspikeral equipment and the processors of a server of the processors of the process

Irosan Horse – secret undocumented routine embedded within a useful program, ecution of the program results in execution of the secret routine.
Trusted System – a computer and operating system that can be verified to implement a given security policy

ment a given security policy

Virtual Address – the address of a storage location in virtual storage

Virus – secret undocumented routine embedded within a useful program. Execution

of the program results in execution of the secret routine.

Address Space – The range of addresses available to a computer program Address Translator – A functional unit that transforms virtual addresses to Application Programming Interface (API) – A standardized library of programs ools used by software developers to write applications that are compatible with

Aspectation Programming Interface (API) — A standardiscell bitary of programming to loss and by synthese developers to wise application that are compatible with a specific operating system or graphic user interface.

Applicational Synthesis — An operation that accountable on a program of the programming of the production of the programming of the standard of the standard of the programming of the standard of the standard of the programming of the standard of

Tache Memory – a memory that is smaller and faster than main memory and that is interposed between the processor and main memory. The cache acts as a buffer for

recently used memory locations.

Client – a process that requests services by sending messages to server processes

Cluster – a group of interconnected, whole computers working together as a unified omputing resource that can create the illusion of being one machine. The term nputer means a system that can run on its own, apart from the cluster Concurrent – pertaining to processes or threads that take place within a common in terval of time during which they may have to alternately share common resources. Critical Section – in an asynchronous procedure of a computer program a part that

annot be executed simultaneously with an associated critical sec

Deadlock — (1) as impasses that occurs when multiple processes are waiting for the availability of a revoure than will not because having the consequent than will not become available because it is being held by another process that is in a similar wast state. Device Drivers — not presenting system module (usually in the kemel) that deals directly with the control of the control

sitiotis between this memory and the less of main memory obstributed Operating System – a common operating system shared by a network of computers. The distributed operating system provides support for interprocess com-nunication, process migration, mutual exclusion, and the prevention or detection of Enabled Interrunt - a condition, usually created by the operating system, during which the processor will respond to interrupt request signals of a specified class **Encryption** – the conversion of plain text or data into unintelligible form by means of a reversible mathematical computation

Execution Context — same as process state File — a set of related records treated as a unit File Allocation Dable (FAT) — a table that indicates the physical location on secondar storage of the space allocated to a file. There is one file allocation table for each file.

File Management System - a set of system software that provides services to user ons in the use of files, including file access, directory maintenance, and

irst In First Out (FIFO) — a queuing technique in which the next item to be retrieved the item that has been in the queue for the longest time lash File – a file in which records are accessed according to the values of a key field.

thing is used to locate a record on the basis of its key value fashing – the selection of a storage location for an item of data by calculating the ddress as a function of the contents of the data. This technique complicates the

audress as a function of the contents of the data. In is technique complicates the toxing allication function but results in rapid random retrieval. Hit Ratio – in a two-level memory, the fraction of all memory accesses that are found in a the master memory (i.e. the cach) cache is a the master memory (i.e. the cache) Interrupt – a suspension of a process, such as the execution of a computer program, caused by an even external to that process and performed in such a way that the pro-

rupt Handler – a routine, generally part of the operating system. When an inter

rrupt Handler – a routine, generally part of the operating system. When an interest transferred to the corresponding interrupt handler, which take action in response to the condition that cause the interrupt. a set of computational steps packational steps packational steps packational steps packational steps packation of the p

Sernel – a portion of the operating system that includes the most heavily used por os of software. Generally the kernel is maintained permanently in main of The kernel runs in a privileged mode and responds to calls from processes and inte

Lightweight Process - a thread

nel – a large operating system core that provides a wide range of services Asin Memory - memory that is internal to the computer system, is program adressable, and can be loaded into registers for subsequent execution of processing Aessage - a block of information that may be exchanged between pro-

neans of communication ficrokernel – a small privileged operating system core that provides process schedling, memory management, and communication services and relies on other pro-esses to perform some of the functions traditionally associated with the operating

system keen!

Mode Switch – hardware operation that occurs that causes the processor to execute in a different mode (lexend or process). When the mode switches from proce to kernel, the prognation counter processor status word, and other registers are save When the mode switches from keend to process, this information is restored. Monolithic Kernel – large kernel containing virtually the complete operating system, including scheduling file system, device drivers, and memory management, the functional components of the kernel have cases to all of its internal dates structures and routines. Typically, am monolithic kernel is implemented as a single process with all elements sharing the arms address space.

Multiprocessing – a mode of operation that provides for parallel processing by two

e processors of a multiprocessor
rocessor – a computer that has two or more processors that have common ac

ess to a main storage Multiprogramming – a mode of operation that provides for the interleaved execu-ion of two or more computer programs by a single processor. The same as multitask ng, using different terminology. **Aultitasking** – a mode of operation that provides for the concurrent performance or other leaved execution of two or more computer tasks. The same as multiprogram-

ning, using different terminology.

Autual Exclusion – a condition in which there is a set of processes, only one of which able to access a given resource or perform a given function at any time. See critical

privilaged State – an execution context that does not allow sensitive hardware ructions to be executed, such as the 'interrupt disable' and I/O instructions rarting System – software that controls the execution of programs and that pro-services such as resource allocation, scheduling, input/output control, and data

tion - reclaiming a resource from a process before the process has finished

oed Instruction – an instruction that can be executed only in a specific mode usually by a supervisory program

Process - a program in execution. A process is controlled and scheduled by the oper-

sate structure containing information about the characteristics and state of the pro-

rocess Image - all of the ingredients of a process, including program, data, stack, rocess and that the processor needs to properly execute the process. The process

process and that the processor reads to properly execute the process. The process interest includes the contract of the various process registers, such as the program that the principle of the process and whether the process is waiting for the complex of a particular (over Same as execution content. process Sameth — an operation that waither the processor form one process to ar-forcess Sameth— and present on the same that the processor form one process to ar-ticle of the same that the processor forms of the processor form one process to ar-ticle of the same that the processor forms of the processor form one process to ar-ticle of the processor of the pr

ermination of a child process or an input/output operation) simple

econdary Memory – memory located outside the computer system itself, including disk and tape Shell – the portion of the operating system that interprets interactive user command: and job control language commands. It functions as an interface between the user nd the operating system.

and use operating system. Stack – a list that is constructed and maintained so that the next data item to be re-trieved is the most recently stored item in the list. This method is characterized as st-in-first-out tarvation – a condition in which a process in indefinitely delayed because other pro esses are always given preference Symmetric Multiprocessing (SMP) – a form of multiprocessing that allows the oper

amurtaneousry **Synchronization** – situation in which two or more processes coordinate their activitie

based on a consiston.

That - same as process.

The - same as process.

The state of the state o