Notes in ECEN 5623

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lecture

read chapter 13, lol.

Jetson TK-1 multi-core CPU and a GPU
Intel Mica (Many integrated core architecture)

i/o interfaces. 10/100 is ethernet.

why did we switch from parallel to serial
cost and signal integrity.

different types of i/o:

- word based for registers, and low i/o interfaces

- Block: for fifos, dual port ram, dMA high rate i/o

- system memory map: boot big busses

ethernet has a header and src
nagle algorithm is forceably used by ethernet.

need a driver to access memory. Need to tell MMU to reference driver which will present the device as a block of memory to write to.

ram at 0.

Test will be on