15-213: Introduction to Computer Systems Written Assignment #2

This written homework covers machine programming concepts, procedure control flow, and structs.

Directions

Complete the question(s) on the following pages with single paragraph answers. These questions are not meant to be particularly long! Once you are done, submit this assignment on Canvas.

Below is an example question and answer.

Q: Please describe the benefits of two's-complement signed integers versus other approaches.

A: Other representations of signed integers (ones-complement and sign-and-magnitude) have two representations of zero (+0 and -0), which makes testing for a zero result more difficult. Also, addition and subtraction of two's complement signed numbers are done exactly the same as addition and subtraction of unsigned numbers (with wraparound on overflow), which means a CPU can use the same hardware and machine instructions for both.

Grading

Assignments are graded via peer review:

- 1. Three other students will each provide short, constructive feedback on your assignment, and a score on a scale of 1 to 15. You will receive the maximum of the three scores.
- 1. You, in turn, will provide feedback and a score for three other students (not the same ones as in part 1). We will provide a *rubric*, a document describing what good answers to each question look like, to assist you. You receive five additional points for completing all of your peer reviews.

Due Date

This assignment is due on Wednesday, June 8 by 11:59pm Pittsburgh time (currently UTC-4). Remember to convert this time to the timezone you currently reside in.

Peer reviews will be assigned roughly 12 hours later, and are due a week after that.

- 1. Suppose %rdi holds the value 0xA0A0 and %rsi holds the value 0x1F0F. Provide the address calculation and the calculated address of the following:
 - a. 0x8(%rdi)b. (%rdi, %rsi)c. (%rdi, \$rsi, 2)d. 0x80(,%rdi, 2)
- 2. What does lea (%rsp, %rcx, 4), %rax put into %rax? How is this different from what mov (%rsp, %rcx, 4), %rax puts into %rax?

```
1a. ans = 0x8 + %rdi = 0xA0A8
1b. ans = %rdi + %rsi = 0xBFAF
1c. ans = %rdi + 2 * %rsi = 0xDEBE
1d. ans = 0x80 + 2 * %rdi = 0x141C0

2. %rax = %rsp + 4 * %rcx
lea only calculates the memory address and puts it inside %rax, mov puts the quad-byte located at that address into %rax
```

Consider the following x86-64 assembly function:

```
loopy:
     # a in %rdi, n in %esi
                $0, %ecx
                $0, %edx
     mov
                %esi, %esi
     test
                .L3
     jle
.L6
     movslq
                %edx, %rax
                (%rdi, %rax, 4), %eax
     mov
                %eax, %ecx
     cmp
                %eax, %ecx
     cmovl
     add
                $1, %edx,,
                %ecx, %esi
     cmp
                .L6
     jg
.L3
                %ecx, %eax
     mov
     ret
```

Fill in the blanks of the corresponding C code. Use the C variable names n, a, i, and x, not register names. Use array notation in showing accesses or updates to elements of a.

a. 0 b. 0 c. i < n d. ++i e. x < a[i] f. x = a[i]		

1. Wow Abi found a coolFunction! but unfortunately she can't find the code for the function mystery. All she has is this assembly snippet. What value should Abi call coolFunction with, in order to make it print out "left shark"? Please explain briefly why your answer works.

```
int coolFunction(int n) {
    if (mystery(n) == 1) {
        printf("left shark\n");
    } else {
        printf("boo\n");
    }
    return 0;
}
```

doSomething:

```
0x000000000400529:
                                   $0x10, %rsp
                        sub
0x000000000040052a:
                                   %rbx, 8(%rsp)
                       mov
                                   %edi, %ebx
0x000000000040052c:
                       mov
                                   %ebx, %ebx
0x000000000040052d:
                       test
0x000000000040052f:
                                   40053d <doSomething+0x14>
                       jle
0 \times 00000000000400531:
                                   %ebx, %edx
                       mov
                                   $0x1, %edx
0x0000000000400533:
                       sub
0x0000000000400536:
                                   400533 <doSomething+0xa>
                       jne
0x000000000400538:
                                   (%rbx, %rbx,1),%eax
                       lea
0x000000000040053a:
                       mov
                                   8(%rsp), %rbx
0x000000000040053b:
                                   $0x10, %rsp
                       add
0x000000000040053c:
                       ret
0x000000000040053d:
                                   $0x0, %eax
                       mov
0 \times 0000000000040053e:
                                   8(%rsp), %rbx
                       mov
0x000000000040053f:
                                   $0x10, %rsp
                       add
0x0000000000400541:
                       ret
mystery:
```

2. What does doSomething do in order to safely use the callee-save register, %rbx?

```
1.
0xD5

The C code of doSomething and mystery is:
int doSomething(int a) {
   if (a <= 0) return 0;
   for (int i = a; i > 0; --i);
   else return 2*a;
}

int mystery(int a) {
   if (doSomething(a) == 0x1AA) return 1;
   return 0;
}
```

So to print "left shark" the return value of mystery should be 1. Then the return value of doSomething should be 0x1AA. It's easy to see that the input of mystery cannot be 0. Then the input should be 0x1AA / 2 = 0xD5

2. doSomething pushes %rbx to the stack at the beginning, and pops the value back into %rbx at the end.

```
typedef struct babyShark_t{
    char s;
    int h[3];
    long a;
    char *r;
    short k;
} babyShark_t;

typedef struct mommaShark_t{
    char a[3];
    babyShark_t b;
    int c;
} mommaShark t;
```

1. We learned in lecture that a struct may need to have padding at the end. Why is this padding sometimes necessary?

Because sometimes some of the fields in the struct is not aligned correctly, and padding is used to skip some bytes so that the fields are aligned.

- 2. Imagine that the table below represents 48 bytes of memory and a mommaShark_t object begins at the beginning of the table (top left corner, addresses increase left to right and then top to bottom). In each cell, indicate the field of the mommaShark_t object that stretches across that byte. Be as specific as possible. Use x to indicate padding.
 - a. If a mommaShark_t object begins at address 0x4000, what is the address of b.h[2] within that object?

mommaShark t:

a[0]	a[1]	a[2]	X	X	X	X	X	b.s	X	X	X	b.h[0]	b.h[1]	b.h[2]	X
X	X	X	X	X	X	X	X	b.a	b.a	b.a	b.a	b.a	b.a	b.a	b.a
b.r	b.r	b.r	b.r	b.r	b.r	b.r	b.r	b.k	b.k	X	X	X	X	X	X
b.c	b.c	b.c	b.c	X	X	X	X								

2. Assuming sizeof(long) = 8, sizeof(int) = 4, sizeof(short) = 2, sizeof(char) = 1, sizeof(void*) = 8, then the alignment of babyShark_t is 8, that of mommaShark_t is also 8					
2a. The offset of b.h[2] relative to the beginning of mommaShark_t is 15. Therefore, &b.h[2] = 0x400F					