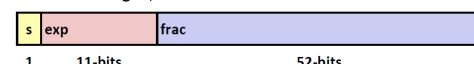


$a \mid b = \sim(\sim a \ \& \ \sim b)$
 $a \wedge b = (a \ \& \ \sim b) \mid (\sim a \ \& \ b)$
 $b = 1$ byte, $w = 2$ bytes, $l = 4$ bytes, $q = 8$ bytes
 ≈ 7 decimal digits, 10^{38}



Normalized value ($\text{exp} \neq 000\dots 0$ and $\text{exp} \neq 111\dots 1$)
 $E = \text{Exp} - \text{Bias}$, $\text{Bias} = 2^{k-1} - 1$
Denormalized Value ($\text{exp} = 000\dots 0$)
 Exponent value: $E = 1 - \text{Bias}$ (**equispaced**)
Infinity: $\text{exp} = 111\dots 1$, $\text{frac} = 000\dots 0$
NaN: $\text{exp} = 111\dots 1$, $\text{frac} \neq 000\dots 0$

≈ 16 decimal digits, 10^{308}



Rounding
 1. BBGRXXX
 Guard bit: LSB of result
 Round bit: 1st bit removed
 Sticky bit: OR of remaining bits

Round up conditions

- Round = 1, Sticky = 1 $\rightarrow > 0.5$
- Guard = 1, Round = 1, Sticky = 0 \rightarrow Round to even

Value	Fraction	GRS	Incr?	Rounded
128	1.0000000	000	N	1.000
15	1.1010000	100	N	1.101
17	1.0001000	010	N	1.000
19	1.0011000	110	Y	1.010
138	1.0001010	011	Y	1.001
63	1.1111100	111	Y	10.000

x86-64 linux calling convention:

Integer parameters:

`%rdi`, `%rsi`, `%rdx`, `%rcx`, `%r8` and `%r9`

Others are stored in stack, pushed in reversed (right-to-left) order

CF Carry Flag (for unsigned) **SF** Sign Flag (for signed)
ZF Zero Flag **OF** Overflow Flag (for signed)

Implicitly set by arithmetic operations (but **not set by `leaq` instruction**):

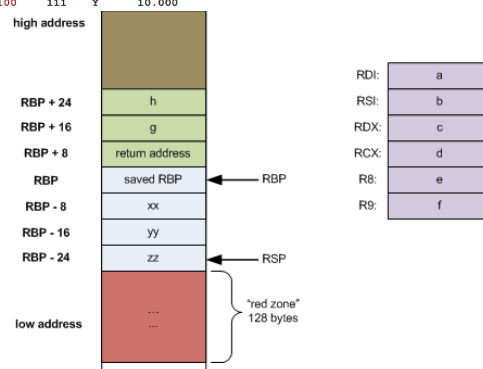
`addq Src DestDest` ($t = a + b$)

CF set if carry out from most significant bit (unsigned overflow)

ZF set if $t == 0$

SF set if $t < 0$ (as signed)

OF set if two's complement (signed) overflow



Rules for turning on the carry flag

1. The carry flag is set if the addition of two numbers causes a carry out of the most significant bits added.

$1111 + 0001 = 0000$ (carry flag is turned on)

2. The carry (borrow) flag is also set if the subtraction of two numbers requires a borrow into the most significant (leftmost) bits subtracted

$0000 - 0001 = 1111$ (carry flag is turned on)

Rules for turning on the overflow flag

1. If the sum of two numbers with the sign bits off yields a result number with the sign bit on

$0100 + 0100 = 1000$ (overflow flag is turned on)

2. If the sum of two numbers with the sign bits on yields a result number with the sign bit off

$1000 + 1000 = 0000$ (overflow flag is turned on)

Note that different from above ($1111 + 0001 = 0000$), the result is correct even though CF is set

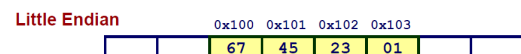
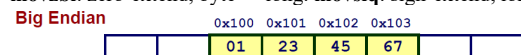
unsigned arithmetic \rightarrow CF | signed arithmetic \rightarrow OF

`cmp b, a` Computes $b - a$ (just like `sub`). Sets condition codes based on result, but **does not change b**

`test a, b` Computes $b \wedge a$ just like `and`. Sets condition codes (only SF and ZF) based on result, but **does not change b**

JX	Condition	Description	SetX	Condition	Description
<code>jmp</code>	1	Unconditional	<code>sete</code>	ZF	Equal / Zero
<code>jbe</code>	ZF	Equal / Zero	<code>setne</code>	\sim ZF	Not Equal / Not Zero
<code>jnb</code>	\sim ZF	Not Equal / Not Zero	<code>sets</code>	SF	Negative
<code>jns</code>	SF	Negative	<code>setns</code>	\sim SF	Nonnegative
<code>jg</code>	\sim (SF^OF) & \sim ZF	Greater (Signed)	<code>setg</code>	\sim (SF^OF) & \sim ZF	Greater (Signed)
<code>jge</code>	\sim (SF^OF)	Greater or Equal (Signed)	<code>setge</code>	\sim (SF^OF)	Greater or Equal (Signed)
<code>jl</code>	(SF^OF)	Less (Signed)	<code>setl</code>	(SF^OF)	Less (Signed)
<code>jle</code>	(SF^OF) ZF	Less or Equal (Signed)	<code>setle</code>	(SF^OF) ZF	Less or Equal (Signed)
<code>ja</code>	\sim CF & \sim ZF	Above (unsigned)	<code>seta</code>	\sim CF & \sim ZF	Above (unsigned)
<code>jb</code>	CF	Below (unsigned)	<code>setb</code>	CF	Below (unsigned)

`movzbl`: zero-extend, byte \rightarrow long. `movslq`: sign-extend, long \rightarrow quad. Etc.



Buffer overflow attacks

Stack Smashing Attacks: overwrite normal return address. Code Injection Attacks: overwrite normal return address and jump to exploit code

Measures

Avoid overflow vulnerabilities: `strcpy` \rightarrow `strncpy`. Employ system-level protections: randomized stack offsets, nonexecutable code segments. Have compiler use stack canaries

Return-Oriented Programming Attacks

Work around stack randomization and marking stack nonexecutable. Does not overcome stack canaries

Internal Fragmentation: For a given block, internal fragmentation occurs if payload is smaller than block size

Caused by: Overhead of maintaining heap data structures | Padding for alignment purposes | Explicit policy decisions

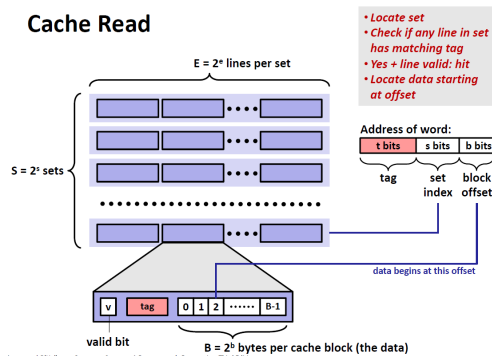
Depends only on the pattern of previous requests, easy to measure

External Fragmentation: Occurs when there is enough aggregate heap memory, but no single free block is large enough

Depends on the pattern of future requests, difficult to measure

Hex	Decimal	Binary
0	0	0000
1	1	0001
2	2	0010
3	3	0011
4	4	0100
5	5	0101
6	6	0110
7	7	0111
8	8	1000
9	9	1001
A	10	1010
B	11	1011
C	12	1100
D	13	1101
E	14	1110
F	15	1111

Cache Read



What about writes?

- **Multiple copies of data exist:**
 - L1, L2, L3, Main Memory, Disk
- **What to do on a write-hit?**
 - **Write-through** (write immediately to memory)
 - **Write-back** (defer write to memory until replacement of line)
 - Each cache line needs a dirty bit (set if data has been written to)
- **What to do on a write-miss?**
 - **Write-allocate** (load into cache, update line in cache)
 - Good if more writes to the location will follow
 - **No-write-allocate** (writes straight to memory, does not load into cache)
- **Typical**
 - Write-through + No-write-allocate
 - Write-back + Write-allocate