

setDcscRegisterBit

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graph LR; A[setDcscRegisterBit] --> B[readDcscRegister]; A --> C[writeDcscRegister]; B --> D[read_dcsc]; B --> E[seek_dcsc]; C --> E; C --> F[write_dcsc];
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The diagram is a flowchart with a single starting node on the left, 'setDcscRegisterBit', which is shaded gray. Two arrows originate from its right side, pointing to 'readDcscRegister' (top) and 'writeDcscRegister' (bottom). From 'readDcscRegister', two arrows point to 'read\_dcsc' and 'seek\_dcsc'. From 'writeDcscRegister', two arrows point to 'seek\_dcsc' and 'write\_dcsc'. All nodes are rectangular boxes with black outlines and text in a monospaced font. The arrows are dark blue.

readDcscRegister

read\_dcsc

seek\_dcsc

writeDcscRegister

write\_dcsc