Tanawat Khunlertkit

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Aim

Since I was a child, I dreamt about developing my own microprocessor with Nvidia as one of my benchmark. Moreover, During my 4 years of computer engineering degree, I found myself that I am seriously interested in microprocessor design, including Integrated Circuit design. With all the course work I was assigned, I am sure that I am capable of doing well in this field.

Courses Taken

Embedded System Design, Programming language and compiler, Operating System Concepts, Computer Architecture, Database Management system, Microprocessor course and lab.

Education

University of Wisconsin, Madison, Madison, WI
BE Computer Engineering
Expected Graduation date: December 2009
GPA: 2.97/4.00

Programming Experience

University of Wisconsin Madison, Madison, WI (Summer, 2007)
Provide advices on technical programming language to develop software for an Auto-SOVA generator model to facilitate costly trial-and-error fine-tuning of new-product assembly processes attributable to unforeseen dimensional errors in manufacturing.

Academic Experience

University of Wisconsin Madison, Madison, WI (Fall, 2007)
Course: ECE 552 Introduction to Computer Architecture
Instructor: Prof. Parameswaran (Parmesh) Ramanathan
Project: Design a 16-bit Microprocessor with 16 instructions set.

University of Wisconsin Madison, Madison, WI (Spring, 2008)
Course: CS 537 Introduction to Operating Systems
Instructor: Prof. Barton Miller
Project: Simulation for CPU Scheduling Algorithms.

University of Wisconsin Madison, Madison, WI (Fall, 2008)
Course: CS 536 Intro to Programming Languages and Compilers
Instructor: Prof. Thomas W. Reps
Project: Conversion from high level language(C) to Assembly language(MIPS Assembly Language)

University of Wisconsin Madison, Madison, WI (Spring 2009)
Course: CS 564 Intro to database Management System
Instructor: Prof. Jignesh Patel
Project: Design a Simple Database System

Skills

Microsoft: Microsoft Word, Microsoft Excel, Microsoft Powerpoint
Mathematical Model: MATLAB
Design: AutoCAD, Quartus, Verilog
High Level Languages: C, C++, Java, PHP, Postgresql
Low Level Languages: MIPS, ARM7TDMI
Operating System: Window XP, Mac OS X Leopard, Unix, Red Hat Linux
Language: Thai and English(Fluent)