|  |  |  |
| --- | --- | --- |
| **Tanawat Khunlertkit** | | |
| **tkhunlertkit@wisc.edu** |  | **Permanent Address** 4715 Sheboygan Ave. Apt. 217 Madison, WI 53705 (608) 886-7157 |

|  |  |
| --- | --- |
| **Goal** | I am seeking to work in an industry with computer both hardware and software. My expertise is in embedded system design field. For further information, please see below. |
| **Courses** | Embedded System Design, Programming language and compiler, Operating System Concepts, Computer Architecture, Database Management system, Microprocessor course and lab. |
| **Education** | **University of Wisconsin, Madison,** Madison, WI  **BE Computer Engineering**  Expected Graduation date: June 2009  GPA: 3.03/4.00 |
| **Employment Experience** | **University of Wisconsin Madison,** Madison, WI (Summer, 2007)  Provide advices on technical programming language to develop software for an Auto-SOVA generator model to facilitate costly trial-and-error fine-tuning of new-product assembly processes attributable to unforeseen dimensional errors in manufacturing. |
| **Academic Experience** | **University of Wisconsin Madison,** Madison, WI (Fall, 2007)  Course: ECE 552 Introduction to Computer Architecture  Instructor: Prof. Parameswaran (Parmesh) Ramanathan  Project: Design a 16-bit Microprocessor  **University of Wisconsin Madison,** Madison, WI (Spring, 2008)  Course: CS 537 Introduction to Operating Systems  Instructor: Prof. Barton Miller  Project: Simulation for CPU Scheduling Algorithms.  **University of Wisconsin Madison,** Madison, WI (Fall, 2008)  Course: CS 536 Intro to Programming Languages and Compilers  Instructor: Prof. Thomas W. Reps  Project: Conversion from high level language to Assembly language |
| **Skills** | Microsoft: Microsoft Word, Microsoft Excel, Microsoft Powerpoint  Mathematical Model: MATLAB Design: AutoCAD, Quartus, Verilog  High Level Languages: C, C++, Java  Low Level Languages: MIPS, ARM7TDMI  Operating System: Window XP, Mac OS X Leopard, Unix, Red Hat Linux  Language: Thai and English(Fluent) |