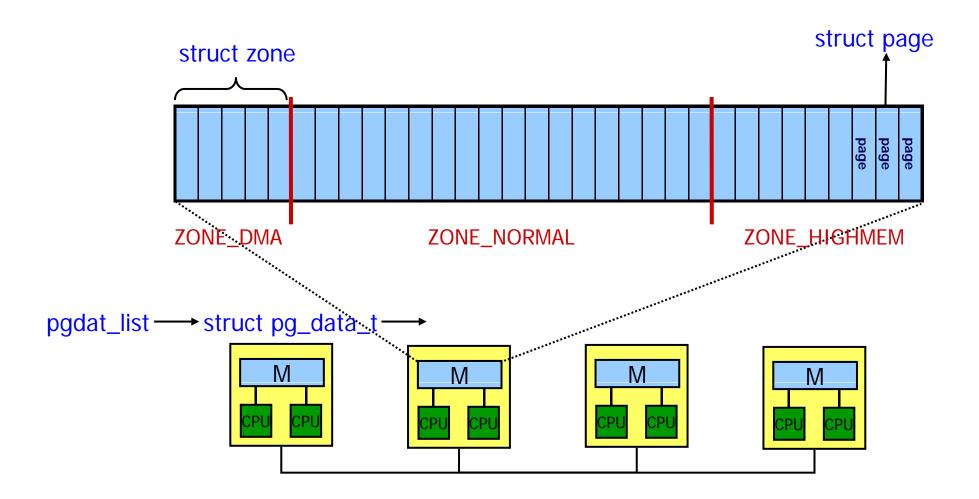


CS632/SEP564: Embedded Operating Systems (Fall 2008)

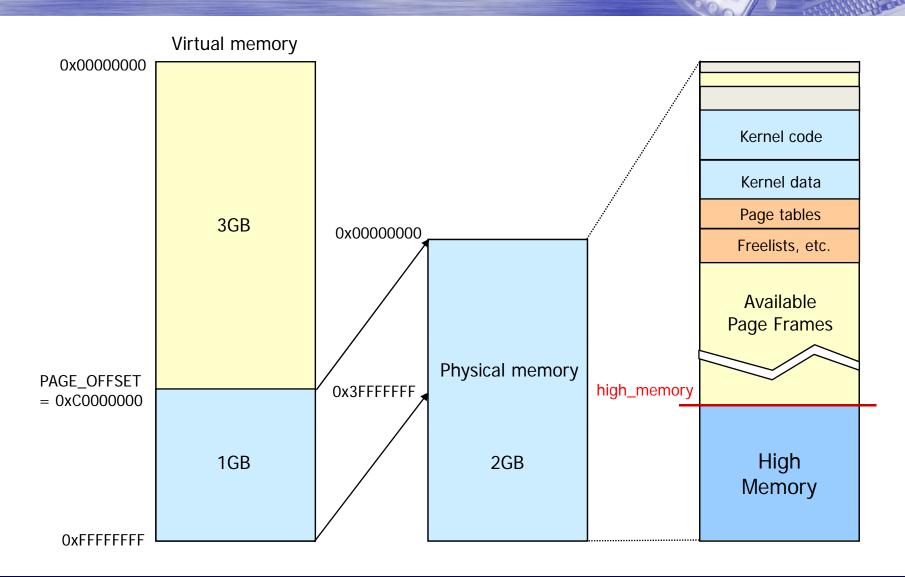
# Memory Management



# The Big Picture



# **Using Physical Memory**



## **Pages**

- struct page linux/mm.h>
  - All page descriptors are stored in the mem\_map array.
  - One entry for every physical page
    - About 1MB for 128MB physical memory with 4KB page size

page_flags_t	flags;
atomic_t	_count;
atomic_t	_mapcount;
unsigned long	private;
struct address_space *	mapping;
pgoff_t	index;
struct list_head	lru;

32 flags defined in linux/page-flags.h>

The number of references to this page Accessed via page\_count().

Count of PTEs mapped.

Private data

How the page is used?

Offset within the mapping

LRU list

# Zones (1)

### Why memory zones?

- Some hardware devices are capable of performing DMA to only certain memory addresses.
- Some architectures are capable of physically addressing larger amounts of memory than they can virtually address.

### Memory zones in Linux

ZONE_DMA	DMA-able pages	< 16MB
ZONE_NORMAL	Normally addressable pages	16MB – 896MB
ZONE_HIGHMEM	Dynamically addressable pages	> 896MB

# Zones (2)

- struct zone linux/mmzone.h>
  - Page frame reclamation is performed on a per zone basis.

spinlock_t	lock;
unsigned long	free_pages;
unsigned long	pages_min, pages_low, pages_high;
struct list_head	active_list;
struct list_head	inactive_list;
struct pglist_data *	zone_pgdat;
struct page *	zone_mem_map;
char *	name;
struct free_area	free_area[];

Spin lock protecting the descriptor Number of free pages in the zone

Parameters for page frame reclaiming

List of active pages in the zone
List of inactive pages in the zone
Pointer to the node descriptor
Pointer to the first page descriptor
Name of the zone
Buddy map

# Zones (3)

### Reserved page frames

- Memory allocation requests can be satisfied immediately without memory reclaiming.
- Reduces the chance of failure in case of atomic memory allocation requests (GFP\_ATOMIC).
- For ZONE\_DMA and ZONE\_NORMAL:
  - min\_free\_kbytes = sqrt(directly\_mapped\_memory \* 16) (KB)\* 128KB ~ 64MB (512KB for 16MB, 8MB for 4GB)
  - zone→pages\_min = the zone's contribution to min\_free\_kbytes / pagesize;
  - zone→pages\_low = zone→pages\_min \* 5 / 4;
  - zone→pages\_high = zone→pages\_min \* 3 / 2;

## **Nodes**

- pg\_data\_t linux/mmzone.h>
  - The physical memory is partitioned in several nodes.
  - All node descriptors are stored in a singly linked list, whose first element is pointed to by pgdat\_list.

zone_t	node_zones[];
zonelist_t	node_zonelists[];
int	nr_zones;
struct page *	node_mem_map;
unsigned long	node_start_pfn;
pg_data_t *	pgdat_next;
int	node_id;

Zones for this node.

The order of zones that allocations are preferred from.

The number of zones. Not all nodes will have the same number of zones.

The first page of the struct page array
Index of the first page frame in the node
Next item in the memory node list
Node ID

## **Zone Allocator (1)**

### Requesting page frames

- struct page \*alloc\_pages(gfp\_t gfp\_mask, unsigned int order);
- struct page \*alloc\_page(gfp\_t gfp\_mask);
- unsigned long <u>get\_free\_pages(gfp\_t gfp\_mask</u>, unsigned int order);
- unsigned long <u>get\_free\_page(gfp\_t gfp\_mask);</u>
- unsigned long get\_zeroed\_page(gfp\_t gfp\_mask);
- unsigned long <u>get\_dma\_pages(gfp\_t gfp\_mask</u>, int order);
  - = \_\_get\_free\_pages((gfp\_mask) | GFP\_DMA, (order));

## **Zone Allocator (2)**

### Releasing page frames

- void free\_pages(unsigned long addr, unsigned int order);
- void \_\_\_free\_pages(struct page \*page, unsigned int order);
- void free\_page(unsigned long addr);
- void <u>\_\_free\_page(struct page \*page);</u>

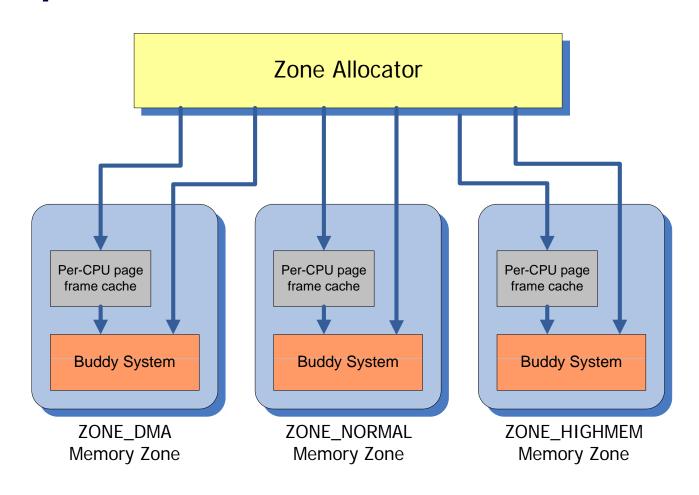
# Zone Allocator (3)

### Some flags used to request page frames

Flag	Description
GFP_DMA	The page frame must belong to the ZONE_DMA zone. Equivalent to GFP_DMA.
GFP_HIGHMEM	The page frame may belong to the ZONE_HIGHMEM zone.
GFP_WAIT	Allowed to block the current process waiting for free page frames.
GFP_HIGH	Allowed to access the pool of reserved page frames.
GFP_IO	Allowed to perform I/O transfers on low memory pages in order to free page frames.
GFP_FS	Allowed to perform filesystem-dependent operations
GFP_ZERO	The page frame returned, if any, must be filled with zeros.
GFP_ATOMIC	GFP_HIGH
GFP_KERNEL GPF_USER	GFP_WAIT  GFP_IO  GFP_FS
GFP_HIGHMEM	GFP_WAIT  GFP_IO  GFP_FS  GFP_HIGHMEM

# **Zone Allocator (4)**

Components of the zone allocator



# **Buddy System (1)**

### Buddy algorithm

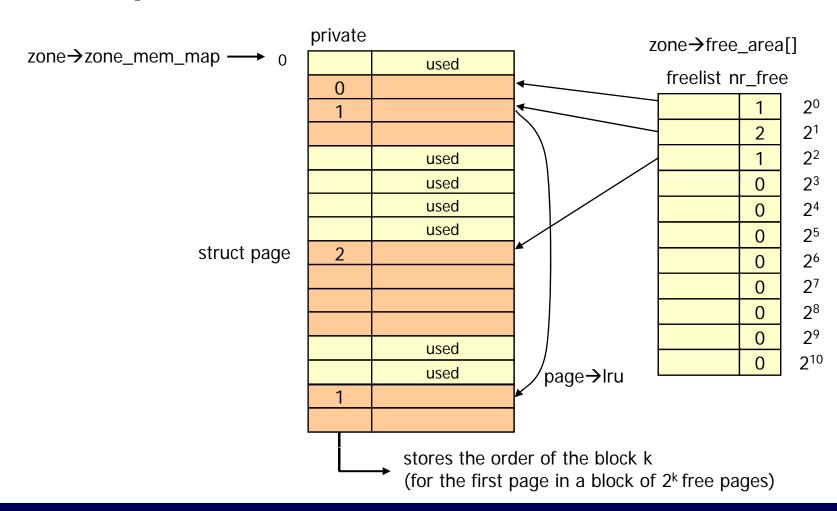
- Treats physical memory as a collection of 2<sup>n</sup>-page-sized blocks aligned on 2<sup>n</sup>-page boundaries.
- To allocate a block of a given order,
  - If a block is found at the specified order, it is allocated immediately.
  - If a block of higher order must be used, divide the larger block into two 2<sup>order-1</sup> blocks, add the lower half to the appropriate freelist, and allocate the memory from the upper half, executing this step recursively.
- When freeing a block,
  - If the block has a free buddy block, combine the two blocks into a single free block; and this process is performed recursively if necessary.

# **Buddy System (2)**

- Implementation < mm/page\_alloc.c>
  - A different buddy system for each zone.
  - All free page frames are grouped into 11 lists of blocks
    - Groups of 1, 2, 4, 8, 16, 32, 64, 128, 256, 512, and 1024
       contiguous page frames
  - struct page \*\_\_rmqueue(struct zone \*zone, unsigned int order);
    - Buddy allocation
  - void \_\_\_free\_one\_page(struct page \*page, struct zone \*zone, unsigned int order);
    - Buddy deallocation
  - void page\_is\_buddy(struct page \*page, int order);
    - Checks whether the block is free and it's a buddy.

# **Buddy System (3)**

### Implementation (cont'd)



# Per-CPU Page Frame Cache (1)

### Motivation

- The kernel often requests and releases single page frames.
- Prepare some pre-allocated page frames to be used for single memory requests issued by the local CPU.
- Hot cache
  - Page frames whose contents are likely to be included in the CPU's hardware cache.
  - Taking a page frame from the hot cache is beneficial for system performance.

#### Cold cache

Useful if the page frame is going to be filled with a DMA operation

# Per-CPU Page Frame Cache (2)

struct per\_cpu\_pages linux/mmzone.h>

int	count;
int	low;
int	high;
int	batch;
struct list_head	list;

Number of pages frame in the cache

Low watermark for cache replenishing

High watermark for cache depletion

Number of page frames to be added or subtracted from the cache

List of descriptors of the page frames included in the cache

- struct per\_cpu\_pageset linux/mmzone.h>
  - struct per\_cpu\_pageset {
     struct per\_cpu\_pages pcp[2];
    };
  - pcp[0]: hot cache, pcp[1]: cold cache

# Per-CPU Page Frame Cache (3)

### Managing per-CPU cache

- If (count < low), replenish the cache by allocating batch single page frames from the buddy system.
- If (count > high), release to the buddy system batch single frames in the cache.
- The kernel always assumes the freed page frame is hot.
- The cold cache is replenished when the low watermark has been reached.
- struct page \* buffered\_rmqueue (struct zonelist \*zonelist, struct zone \*zone, int order, gfp\_t gfp\_flags);
- void free\_hot\_cold\_page (struct page \*page, int cold);

# Slab Allocator (1)

#### Slab allocator

- Developed by Sun Microsystems for Solaris 2.4 in 1994.
- The kernel functions tend to request memory areas of the same type repeatedly.
- The slab allocator does not discard the objects that have been allocated and then released but saves them in memory.
- When a new object is then requested, it can be taken from memory without having to be reintialized.
- The slab allocator works on top of the buddy system.

## Slab Allocator (2)

#### Caches

- struct kmem\_cache <mm/slab.c>
- The slab allocator groups objects into caches.
- Each cache is a "store" of objects of the same type.
- General caches
  - kmem\_cache: objects are the cache descriptors of the remaining caches used by the kernel.
  - General-purpose caches for 13 geometrically distributed sizes
    - » 32, 64, 128, 256, 512, 1K, 2K, 4K, 8K, 16K, 32K, 64K, 128K
    - » Two caches for each size: one for normal, the other for DMA
  - Initialized in kmem\_cache\_init()

### Specific caches

- For specific objects used frequently by the kernel.
- Created by kmem\_cache\_create()

# Slab Allocator (3)

🚰 cafe.kaist.ac.kr	- PuTTY													
names_cache	7	7	4096	1	1	÷	tunables	24	12	8 : slabdata	7	7	9	
idr_layer_cache	299	319	136	29	- 1	:	tunables	120	60	8 : slabdata	11	11	9	
buffer_head	104419	146736	52	72	1	:	tunables	120	60	8 : slabdata	2038	2038	9	
mm_struct	164	180	448	9	1	:	tunables	54	27	8 : slabdata	20	20	9	
vm_area_struct	12173	12364	88	44	1	:	tunables	120	60	8 : slabdata	281	281	9	
Fs_cache	159	236	64	59	1	:	tunables	120	60	8 : slabdata	4	4	9	
Files_cache	149	189	448	9	1	:	tunables	54	27	8 : slabdata	21	21	9	
signal_cache	193	220	384	10	1	:	tunables	54	27	8 : slabdata	22	22	9	
sighand_cache	193	198	1344	3	1	:	tunables	24	12	8 : slabdata	66	66	9	
task_struct	221	228	1360	3	1	:	tunables	24	12	8 : slabdata	76	76	9	
anon_vma	5342	5588	12	254	1	:	tunables	120	60	8 : slabdata	22	22	9	
ogd _	145	145	4096	1	1	:	tunables	24	12	8 : slabdata	145	145	9	
size-131072(DMA)	9	9	131072	1	32	:	tunables	8	4	0 : slabdata	9	9	9	
size-131072	9	9	131072	1	32	:	tunables	8	4	0 : slabdata	9	9	9	
size-65536(DMA)	9	6	65536	1	16	:	tunables	8	4	0 : slabdata	9	9	9	
size-65536	1	1	65536	1	16	:	tunables	8	4	0 : slabdata	1	1	9	
size-32768(DMA)	9	6	32768	1	8	:	tunables	8	4	0 : slabdata	9	9	9	
size-32768	9	6	32768	1	8	:	tunables	8	4	0 : slabdata	9	9	9	
size-16384(DMA)	9	9	16384	1	4	:	tunables	8	4	0 : slabdata	9	9	9	
size-16384	8	16	16384	1	4	:	tunables	8	4	0 : slabdata	8	16	9	
size-8192(DMA)	6	6	8192	1	2	:	tunables	8	4	0 : slabdata	9	9	9	
size-8192	230	230	8192	1	2	:	tunables	8	4	0 : slabdata	230	230	9	
size-4096(DMA)	9	9	4096	1	1	:	tunables	24	12	8 : slabdata	9	9	9	
size-4096	163	163	4096	1	1	:	tunables	24	12	8 : slabdata	163	163	9	
size-2048(DMA)	9	9	2048	2	1	:	tunables	24	12	8 : slabdata	9	9	9	
size-2048	634	642	2048	2	1	:	tunables	24	12	8 : slabdata	321	321	9	
size-1024(DMA)	9	9	1024	4	1	:	tunables	54	27	8 : slabdata	9	9	9	
size-1024	340	340	1024	4	1	:	tunables	54	27	8 : slabdata	85	85	9	
size-512(DMA)	9	9	512	8	- 1	:	tunables	54	27	8 : slabdata	9	9	9	
size-512	600	656	512	8	1	:	tunables	54	27	8 : slabdata	82	82	9	
size-256(DMA)	9	6	256	15	1	:	tunables	120	60	8 : slabdata	9	9	9	
size-256	844	930	256	15	1	:	tunables	120	60	8 : slabdata	62	62	9	
size-128(DMA)	9	9	128	30	1	•	tunables	120	60	8 : slabdata	9	9	9	
size-128	2018	2250	128	30	1	•	tunables	120	60	8 : slabdata	75	75	9	
size-64(DMA)	9	9	64	59	1	÷	tunables	120	69	8 : slabdata	9	9	9	
size-32(DMA)	9	9	32	113	1	:	tunables	120	69	8 : slabdata	9	9	9	
size-64	5069	5251	64	59	1		tunables	120	60	8 : slabdata	89	89	9	
size-32	3273	3616	32	113	1	÷	tunables	120	60	8 : slabdata	32	32	9	
kmem cache	150	150	128	30	1	:	tunables	120	60	8 : slabdata	5	5	9	
dev1:/home/jinso											_			

## Slab Allocator (4)

#### Slabs

- struct slab <mm/slab.c>
- Each slab consists of one or more contiguous page frames.
- Each slab contains both allocated and freed objects.
- External slab descriptor
  - Slab descriptor is stored outside the slab. (in general cache)
- Internal slab descriptor
  - Slab descriptor is stored inside the slab.
  - When the object size is smaller than 512B, or when internal fragmentation leaves enough space.

## Slab Allocator (5)

### Objects

- Objects consist of a set of data structures and methods called the constructor and the destructor.
- Each object has a short descriptor of type kmem\_bufctl\_t <mm/slab.c>
- An object descriptor is meaningful only when the object is free.
  - It contains the index of the next free object in the slab.
  - The last element is marked by BUFCTL\_END (0xffff).
- External object descriptors vs. internal object descriptors

# Slab Allocator (6)

### Slab coloring

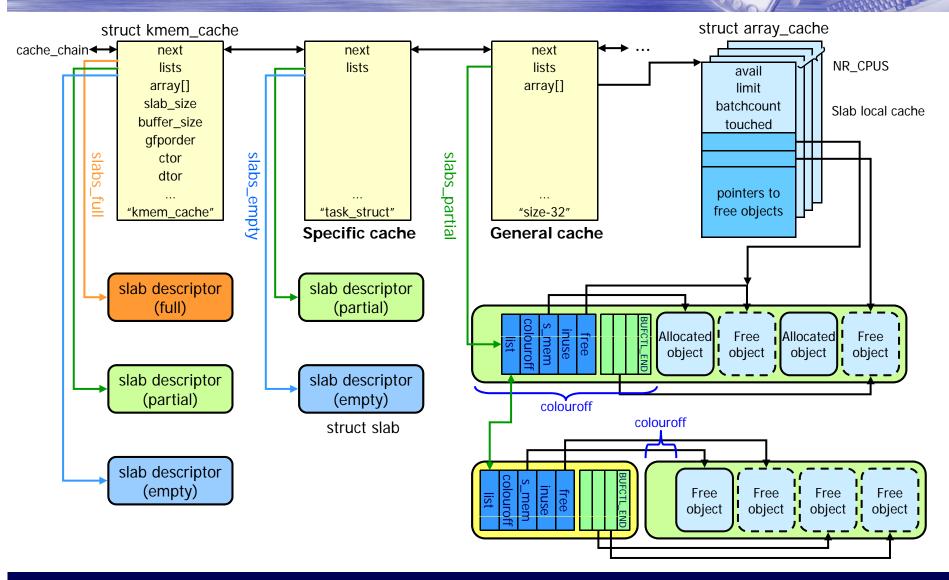
- Objects that have the same offset within different slabs will, with a relatively high probability, end up mapped in the same cache line.
- The slab allocator assigns different arbitrary values called colors to the slabs.
- Slabs having different colors store the first object in different memory locations, while satisfying the alignment constraint.
- Coloring works only when there is enough space inside the slab.

## Slab Allocator (7)

#### Slab local cache

- struct array\_cache <mm/slab.c>
- Per-CPU data structure consisting of a small array of pointers to freed objects.
- Reduce spin lock contention among processors and make better use of the hardware caches.
- The cache size depends on the size of the objects
   1 ~ 120 pointers
- Local caches are refilled or emptied from the slab if needed.

# Slab Allocator (8)



## Slab Allocator (9)

### Using specific caches

- struct kmem\_cache \*kmem\_cache\_create(const char \*name, size\_t size, size\_t align, unsigned long flags, void (\*ctor)(void \*, struct kmem\_cache \*, unsigned long), void (\*dtor)(void \*, struct kmem\_cache \*, unsigned long));
- void \*kmem\_cache\_alloc(struct kmem\_cache \*cachep, int flags);
- void kmem\_cache\_free(struct kmem\_cache \*cachep, void \*objp);

### Using general caches

- void \*kmalloc(size\_t size, int flags);
- void kfree(const void \*objp);

# **Noncontiguous Areas**

### Noncontiguous memory areas

- Virtually contiguous, but physically noncontiguous
- Need to modify kernel page tables
- Can make use of high memory page frames

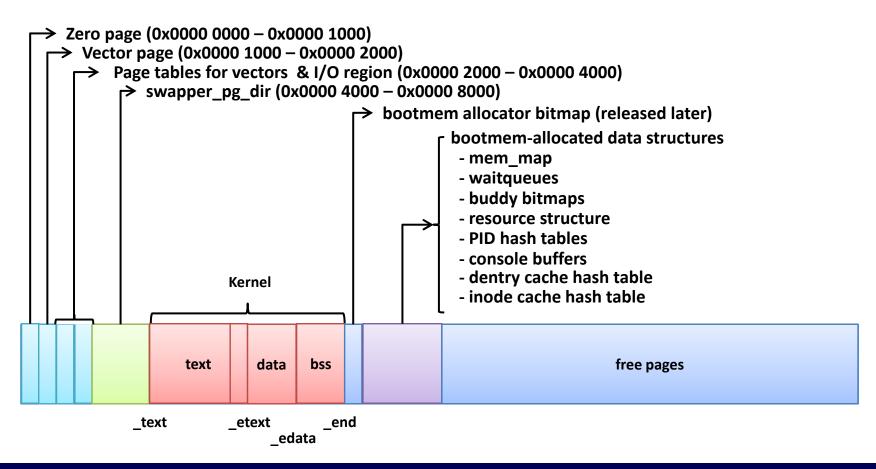
### Using a noncontiguous memory area

- void \*vmalloc(unsigned long size);
- void vfree(void \*addr);

# Where Are My Pages? (1)

### • After mem\_init();

Linux kernel 2.6.10 on ARM9



# Where Are My Pages? (2)

### Free pages

- Buddy
- PCP (Per-CPU Page frame cache)

#### Slab

- For kernel data structures
- Specific & general
- Vmalloc
- Kernel stacks
- Page tables
  - Kernel page tables
  - User page tables

# Where Are My Pages? (3)

### Anonymous pages

Private vs. shared

### File-mapped pages

- Page caches
- Buffer caches
- Mapped pages (private vs. shared)

#### Modules

#### Device drivers

DMA areas, buffers, etc.

### Other page-level data structures

PID bitmap, pipes, etc.

## Summary

- Memory allocation in the kernel
  - Page-aligned
    - alloc\_pages()
  - Specific caches from slab
    - kmem\_cache\_alloc()
  - General caches from slab
    - kmalloc()
  - From non-contiguous memory area
    - vmalloc()