# A Mechanized Theory of Communication Analysis in CML

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#### Concurrent ML

- extension of Standard ML
- concurrency and synchronization
- synchronized communication over channels: send event, receive event
- composition of events: choose event, wrap event ...

#### Concurrent ML

```
type thread_id
val spawn : (unit -> unit) -> thread_id

type 'a chan
val channel : unit -> 'a chan

type 'a event
val sync: 'a event -> 'a
val recvEvt: 'a chan -> 'a event
val sendEvt: 'a chan -> 'a event
val sendEvt: 'a chan * 'a -> unit event

val send: 'a chan * 'a -> unit
fun send (ch, v) = sync (sendEvt (ch, v))

val recv: 'a chan -> 'a
fun recv ch = sync (recvEvt ch)
```

#### Concurrent ML

```
structure Serv : SERV =
struct
 datatype serv = S of (int * int chan)
     chan
 fun make () =
 let
   val regCh = channel ()
   fun loop state =
   let
     val (v, replCh) = recv reqCh
     val () = send (replCh, state)
    in
     loop v
   end
   val() = spawn(fn() => loop 0)
 in
   S reqCh
 end
```

```
fun call (server, v) =
let
   val S reqCh = server
   val replCh = channel ()
   val () = send (reqCh, (v, replCh))
in
   recv replCh
   end
end
signature SERV =
sig
   type serv
   val make : unit -> serv
   val call : serv * int -> int
end
```

- interactive theorem proving assistant; proof assistant
- unification and rewriting
- simply typed terms
- propositions as boolean typed terms
- higher order terms
- computable functions
- inductive data
- inductive reasoning
- tactics and composition

```
\vdash P1 \lor P2 \rightarrow 0
proof
  assume P1 V P2:
     case P1:
       have \vdash P1 \rightarrow 0 by A
       have \vdash Q by modus ponens
     case P2:
       have \vdash P2 \rightarrow 0 by B
       have \vdash 0 by modus ponens
     have P1 \vdash 0. P2 \vdash 0
     have ⊢ Q by disjunction elimination
  have P1 \vee P2 \vdash 0
  have \vdash P1 \lor P2 \rightarrow 0
     by implication introduction
aed
```

```
\vdash P1 \lor P2 \rightarrow 0
apply (rule impI)
  P1 \lor P2 \vdash 0
apply (erule disjE)
 apply (insert A)
 P1. P1 \rightarrow 0 \vdash 0
apply (erule mp)
 apply assumption
  apply (insert B)
  P2. P2 \rightarrow 0 \vdash 0
apply (erule mp)
  P2 ⊢ P2
apply assumption
done
```

dana

```
├ sorted lte (Cons (Z) (Cons (S Z) (Cons (S Z) (Cons (S (S (S Z))) Nil))))
apply (rule cons)
 ⊢ lte Z (S Z)
* Forted lte (Cons (S Z) (Cons (S Z) (Cons (S (S (S Z))) Nil)))
apply (rule lt)
 ⊢ 1te 7.7
* - sorted lte (Cons (S Z) (Cons (S Z) (Cons (S (S (S Z))) Nil)))
apply (rule eq)
 ├ sorted lte (Cons (S Z) (Cons (S Z) (Cons (S (S (S Z))) Nil)))
apply (rule cons)
 ⊢ lte (S Z) (S Z)
* - sorted lte (Cons (S Z) (Cons (S (S (S Z))) Nil))
apply (rule eq)
 ⊢ sorted lte (Cons (S Z) (Cons (S (S (S Z))) Nil))
apply (rule cons)
 ⊢ lte (S Z) (S (S (S Z)))
* ⊢ sorted lte (Cons (S (S (S Z))) Nil)
apply (rule lt)
 ⊢ lte (S Z) (S (S Z))
* - sorted lte (Cons (S (S (S Z))) Nil)
apply (rule lt)
 ⊢ lte (S Z) (S Z)
* ├ sorted lte (Cons (S (S (S Z))) Nil)
apply (rule eq)
 ⊢ sorted lte (Cons (S (S (S Z))) Nil)
apply (rule uni)
```

# **Analysis**

- communication classification: one-shot, one-to-many, many-to-one, many-to-many
- control flow analysis
- channel liveness
- algorithm vs constraints
- structural recursion vs fixpoint accumulation
- performance improvements
- safety

# Synchronization

- uniprocessor; dispatch scheduling
- multiprocessor; mutex and compare-and-swap
- synchronization state
- sender and receiver thread containers
- message containers