

Summary

The goal of this master's thesis is to develop formal and mechanically verified proofs of useful properties about communication in Concurrent ML[8, 9]. This work will build on Reppy and Xiao's static analysis for computing sound approximations of communication topologies[11, 16]. I will define a small-step operational semantics for Concurrent ML and a constraint-based static analysis[5] that describes all possible communications with varying precision. I will prove that the analysis is sound with respect to the semantics. The semantics, analysis, propositions, proofs, and theorems will rely on Isabelle/HOL[6, 7, 14, 15] as the formal language of reasoning. The proofs will be mechanically checked by Isabelle[17].

Overview

Concurrent programming languages provide features to specify a range of evaluation orders between steps of distinct expressions. The freedom to choose from a number of possible evaluation orders has certain advantages. Conceptually distinct tasks may need to overlap in time, but are easier to understand if they are written as distinct expressions. Concurrent languages may also allow the evaluation order between steps of expressions to be nondeterministic or unrestricted. If it's not necessary for tasks to be ordered in a precise way, then it may be better that the program allow arbitrary ordering and let a scheduler find an execution order based on runtime conditions and policies of fairness. A common use case for concurrent languages is GUI programming, in which a program has to process various requests while remaining responsive to subsequent user inputs and continually providing the user with the latest information it has processed.

Concurrent ML is a concurrent programming language. It offers a process abstraction, which is a piece of code allowed to have a wide range of evaluation orders relative to code encapsulated in other processes. The language provides a synchronization mechanism that can specify the execution order between parts of expressions in separate processes. It is often the case that synchronization is necessary when data is shared. Thus, in Concurrent ML, synchronization and data sharing mechanisms are actually subsumed by a uniform communication mechanism. Additional process abstractions can be used for sharing data asynchronously, which can provide better usability or performance in some instances. A process, also known as a thread in Concurrent ML, is created using the `spawn` primitive.

```
type thread_id
val spawn: (unit -> unit) -> thread_id
```

Processes communicate by having shared access to a common channel. A channel can be used to either send data or receive data. When a process sends on a channel, another process must receive on the

same channel before the sending process can continue. Likewise, when a process receives on a channel, another process must send on the same channel before the receiving process can continue.

```
type 'a chan
val channel : unit -> 'a chan
val recv : 'a chan -> 'a
val send : ('a chan * 'a) -> unit
```

A given channel can have any arbitrary number of processes sending or receiving data on it over the course of the program's execution. Listing 1 and Listing 2 give a simple example derived from Reppy's book *Concurrent Programming in ML*[8] that illustrates these essential features of Concurrent ML.

```
signature SERV = sig
  type serv
  val make : unit -> serv
  val call : serv * int -> int
end
```

Listing 1: Server signature

```
structure Serv :> SERV = struct
  datatype serv = S of (int * int chan) chan

  fun make () = let
    val reqCh = channel ()
    fun loop state = let
      val (v, replCh) = recv reqCh
    in
      send (replCh, state);
      loop v
    end
  in
    spawn (fn () => loop 0);
    S reqCh
  end

  fun call (server, v) = let
    val S reqCh = server
    val replCh = channel ()
  in
    send (reqCh, (v, replCh));
    recv replCh
  end
end
```

Listing 2: Implementation of servers

The server implementation, given in Listing 2, defines a server that holds a number `state`. When a client gives the server a number `v`, the server gives back `state`, and holds onto `v` to as its new `state`, which it gives to the next client and so on. A request and reply is equivalent to reading and writing a mutable cell in isolation. The function `make` makes a new server. It creates a new channel `reqCh`, from which the server will receive requests. The sever behavior is defined by the infinite loop `loop`, which takes a number as the state of each iteration. Each iteration, the server tries to receive requests on `reqCh`. It expects the request to be composed of a number `v` and a channel `replCh`, through which to reply. Once a request has been received, it sends the current state back to the client through `replCh` by calling `send (replCh, state)`. It initiates the next iteration of the loop by calling `loop` with a new state from the client. The server is created with a new process by calling `spawn (fn () => loop 0)`. A handle to the new server is returned as `reqCh` wrapped in the constructor `S`. The function `call` makes a request to a server `server` with a number `v` and returns the number from the server's reply. It extracts the request channel `reqCh` from the server handle and creates a new channel `replCh`, from which the client will receive replies. It makes a request to the server with the number `v` and the reply channel `replCh` by calling `send (reqCh, (v, replCh))`. Then it receives the reply with the new number by calling `recv replCh`.

Reppy's original design of Concurrent ML allows for events other than sending and receiving to be triggered by synchronization. One such event chooses between one of many events to synchronize on. Only one of the events is chosen for synchronization, but all choices must be represented. Thus, event synchronization must be separated from event values, similar to the way function application is separated from function abstraction. `send` and `recv` are just shorthand for synchronization on send and receive events, respectively.

```

type 'a event
val sync : 'a event -> 'a
val recvEvt : 'a chan -> 'a event
val sendEvt : 'a chan * 'a -> unit event
val choose: 'a event * 'a event -> 'a event

fun send (ch, v) = sync (sendEvt (ch, v))
fun recv v = sync (recvEvt v)

```

`choose` is an example of an event combinator; a way to construct an event from other events. Reppy's book on Concurrent ML offers explanations of many other useful combinators, such as the `wrap` and `guard` combinators[8]. Donnelly and Fluet extended Concurrent ML with the transactional event combinator `thenEvt`[1]. Transactional events provide a technique for describing tasks that sometimes execute in isolation and sometimes don't. Achieving similar results without transactional events would require duplication of code in multiple processes, resulting in code that is brittle under

modification.

```
val thenEvt: 'a event * ('a -> 'b event) -> 'b event
```

When `thenEvt` is synchronized on, either all of its constituent events and abstractions evaluate in isolation, or none evaluate.

A uniprocessor implementation of synchronous communication is inexpensive. Using a fairly course-grain interleaving, the communication on a channel can proceed by checking if the channel is in one of two possible states: either a corresponding process is waiting or there's nothing waiting. The implementation doesn't need to consider states where competing processes are also trying to communicate on the same channel, since the course-grain interleaving ensures that competing processes have made no partial communication progress. In a multiprocessor setting, processes can run in parallel and multiple processes can simultaneously make partial progress on the same channel. The multiprocessor implementation of communication is more expensive than that of the uniprocessor, since it must consider additional states related to competing processes making partial communication progress.[10]

Channels known to have only one sender or one receiver can have lower communication costs than those with arbitrary number of senders and arbitrary number of receivers, since some of the cost of handling competing processes can be avoided. Concurrent ML does not provide language features for multiple types of channels distinguished by their communication topologies, or the number of processes that may end up sending or receiving on it. However, channels can be classified into various topologies based on their potential communication. A many-to-many channel has any number of senders and receivers; a fan-out channel has one sender and any number of receivers; a fan-in channel has any number of senders and exactly one receiver; a one-to-one channel has exactly one of each; a one-shot channel has exactly one sender, one receiver, and sends data only once. The server implementation in Listing 2 with the following calling code exhibits these topologies.

```
val server = Serv.make ()  
val _ = spawn (fn () => Serv.call (server, 35))  
val _ = spawn (fn () =>  
  Serv.call (server, 12);  
  Serv.call (server, 13)  
)  
val _ = spawn (fn () => Serv.call (server, 81))  
val _ = spawn (fn () => Serv.call (server, 44))
```

Since there are four processes that make calls to the server, the server's particular `reqCh` has four senders. Servers are created with only one process listening for requests, so the `reqCh` of this server

has just one receiver. So the server's `reqCh` is classified as fan-in. Each use of `call` creates a distinct new channel `replCh` for receiving data. The function `call` receives on the channel once and the server sends on the channel once, so each instance of `replCh` is one-shot.

A program analysis that describes communication topologies of channels has practical benefits in at least two ways. It can highlight which channels are candidates for optimized implementations of communication; or in a language extension allowing the specification of restricted channels, it can conservatively verify the correct usage of restricted channels. Listing 2 demonstrates the language extension based on an example from Reppy and Xiao[11].

```

structure Serv :> SERV = struct
  datatype serv = S of (int * int chan) chan

  fun make () = let
    val reqCh = FanIn.channel()
    fun loop state = let
      val (v, replCh) = FanIn.recv reqCh
    in
      OneShot.send (replCh, state);
      loop v
    end
  in
    spawn (fn () => loop 0);
    S reqCh
  end

  fun call (server, v) = let
    val S reqCh = server
    val replCh = OneShot.channel ()
  in
    FanIn.send (reqCh, (v, replCh));
    OneShot.recv replCh
  end
end

```

Without a static analysis to check the usage of the special channels, one could inadvertently use a one-shot channel for a channel that has multiple senders, resulting in runtime behavior inconsistent with the general semantics of channel synchronization.

The utility of the program analysis additionally depends on it being informative, sound, and computable. The analysis is informative iff there exist programs about which the analysis describes information that is not directly observable. The analysis is sound iff the information it describes about a program is the same or less precise than the operational semantics of the program. The analysis is

computable iff there exists an algorithm that determines all the values described by the analysis on any input program.

Program analyses, like operational semantics, describe information about the execution or behavior of programs. Yet, while an operational semantics may be viewed as ground truth, the correctness of an analysis is derived from its relation to an operational semantics. In practice, program analyses often describe computable information with respect to operational semantics that are universal and capable of describing uncomputable information. To allow for computability, program analyses often describe approximate information.

There are a large number of program analyses with a variety of practical uses. Some constructions of programs might be considered bad, by describing operations that don't make sense, like `True * 5 / "hello"`, or accessing the 7th element of an array with 6 elements. A type systems, or static semantics, is an analysis that can help ensure programs are well constructed. It describes how programs and expressions can be composed, such that the programs won't get stuck or result in certain kinds of undesired behavior. Type systems can improve debugging by pointing out errors that may be infrequently executed. They can also improve execution speeds of safe languages by rendering some runtime checks unnecessary.

Other analyses are useful for describing opportunities for program optimizations. Many analyses used for optimizations describe how data flows with information related to every point in the program. Each point refers to a term, from which the small-step semantics may take a step. Some programs may mention the same expression multiple times, possibly resulting in redundant computations. These redundant computations can be detected by available expressions analysis, one of many data flow analyses. An available expressions analysis describes which expressions must have been computed by each program point.

```
1. let
2.   val w = 4
3.   val x = ref 1
4.   val y = ref 2
5.   val z = (!x + 1) + (!y + 2) + (w - 3);
6.   val w = 1
7. in
8.   y := 0;
9.   (!y + 2) - (!x + 1) * (w - 3)
10.  end
```

The expression `(!x + 1)` is available by line 9 but `(!y + 2)` and `(w - 3)` are not, because `y` was modified in line 8 and `w` was rebound in line 6.

Another inefficiency is that programs may perform computations, but then ignore their results.

Such dead code can be detected by a liveness analysis. The analysis describes for each program point, the set of variables and references whose values might be used in the remainder of the program.

```
1. let
2.   val x = 1
3.   val y = 2
4.   val z = ref (4 * 73)
5.   val x = 4
6. in
7.   z := 1;
8.   x * !z
9. end
```

Since the variables `x` and `z` and the dereference `!z` are used in line 8, they are live at line 7. Since `z` is reassigned at line 7, `!z` is no longer live at line 6. Since `x` is bound at line 5 and not used above, it is not live at line 4 and above. Since `z` is bound at 4 and not used above, it is not live at line 3 and above. The liveness information demonstrates that the expression `(4 * 73)` doesn't need to be computed, and lines 2 and 3 can simply be removed.

The information at each program point is derived from control structures in the program that dictate how information may flow between program points. Some uses of control structures are represented as literals in the syntax, while other uses are expressions that may evaluate to control structures, or function parameters that may bind to control structures. Function abstraction is a control structure allowing multiple parts of a program to flow into a section of code via a binding. In ML, function abstractions are higher order, and may be unknown without some form of evaluation. These control structures may be revealed by an abstract value flow analysis, which associates each program point with a set of abstract values that the point's expression may evaluate to.

```
1. let
2.   val f = fn x => x 1
3.   val g = fn y => y + 2
4.   val h = fn z => z + 3
5. in
6.   (f g) + (f h)
7. end
```

The abstract values of `f`, `g`, `h` are simply their let bound expressions $\{\text{fn } x \Rightarrow x\ 1\}$, $\{\text{fn } y \Rightarrow y + 2\}$, $\{\text{fn } z \Rightarrow z + 3\}$, respectively. `x` has the abstract values of $\{\text{fn } y \Rightarrow y + 2, \text{fn } z \Rightarrow z + 3\}$, so `x 1` has the abstract values of $\{3, 4\}$; `(f g)` has abstract values of $\{3, 4\}$. Since the abstract values depend on the flow of information, which depends on the abstract values, the description of abstract values is inductive or recursive. The historical motivation for describing the

abstract value information was really for its the control information, so the original approaches to these analyses are known as control flow analyses or CFAs. With the control flow information, other data flow analyses like available expression analysis and liveness analysis can provide greater coverage.

Analyses can be described in a variety of ways. An algorithm that take programs as input and produce behavior information as output are necessary for automation in compilers. A specification that states a proposition in terms of programs and execution information may be more suitable for showing clarity of meaning and correctness with respect to the operational semantics. The specification can be translated into an algorithm involving two parts. The first part generates a comprehensive set of data structures representing constraints of all program points, mirroring the specification's description, and the second part solves the constraints.

For a subset of Concurrent ML without event combinators, Reppy and Xiao developed an efficient algorithmic analysis that determines for each channel all abstract processes that send and receive on it. The algorithm depends on each primitive operation in the program being labeled with a program point. A sequence of program points ordered in a valid execution sequence forms a control path. Distinction between processes in a program can be inferred from whether or not their control paths diverge.

The algorithm proceeds in multiple steps that produce intermediate data structures, used for efficient lookup in the subsequent steps. It starts with a control-flow analysis[12, 13] that results in multiple mappings. One mapping is from variables to abstract values that may bind to the variables. Another mapping is from channel-bound variables to abstract values that are sent on the respective channels. Another is from function-bound variables to abstract values that are the result of respective function applications. It constructs a control-flow graph with possible paths for pattern matching and process spawning determined directly from the primitives used in the program. Relying on information from the mappings to abstract values, it constructs the possible paths of execution via function application and channel communication. It uses the graph for live variable analysis of channels, which limits the scope for the remaining analysis. Using the spawn and application edges of the control-flow graph, the algorithm then performs a data-flow analysis to determine a mapping from program points to all possible control paths leading into the respective program points. Using the CFA's mappings to abstract values, the algorithm determines the program points for sends and receives per channel variable. Then it uses the mapping to control paths to determine all control paths that send or receive on each channel, from which it classifies channels as one-shot, one-to-one, fan-in, fan-out, or many-to-many.

Reppy and Xiao informally prove soundness of their analysis by showing that their analysis claims that more than one process sends (or receives) on a channel if the execution allows more than one to send (or receive) on a that channel. The proof of soundness depends on the ability to relate the execution of a program to the static analysis of a program. The static analysis describes processes in

terms of control paths, since it can only describe processes in terms of statically available information. Thus, in order to describe the relationship between the processes of the static analysis and the operational semantics, the operational semantics is defined as stepping between sets of control paths paired with terms. Divergent control paths are added whenever a new process is spawned.

The syntax, semantics, and analysis need to describe many details. Proving propositions relating all of these definitions requires manipulation of all those details. To ensure the correctness of proofs, it is necessary to check that there are no subtle errors in the definitions or proofs. Proofs in general require many subtle manipulations of symbols. The difference between a false statement and a true statement can often be difficult to spot, since the two may be very similar lexically. However, a mechanical proof checker, such as the one in Isabelle, has no difficulty discerning between valid and invalid derivations of statements. Mechanical checking of proofs can notify us of errors in the proofs or definitions far better and faster than manual checking. I have already benefitted from Isabelle's proof checker in order to correctly define the language semantics and abstract value flow analysis for this work. While trying to prove soundness of the analysis, the proof assistant would not accept my proof unless I provided derivation of facts that I believed to be false. I determined that my intuition was correct but my definitions had errors. After correcting the errors, I was able to complete the proof, such that the proof checker was satisfied.

Although Isabelle is described as a proof assistant[17], it is really a generic system for processing any kind of code. The code could be proofs, propositions, programs, or types. The processing could be checking proofs, interpreting programs, or translating code. Code and logics for processing code are defined by users using its meta-language Standard ML, and other user-defined languages. Isabelle/HOL is a higher-order logic built from Isabelle's primitives and other logics. It is useful for both programming and proving. Its ability to check that proofs satisfy propositions is simply one instance of its verification capabilities. It can also check that program terms satisfy types, similar to other programming systems for ML. Proofs and propositions are analogous to terms and types, respectively, yet Isabelle/HOL treats the two concepts distinctly. The practical uses for terms are quite different from that of proofs. If a *term* satisfies a *type*, then the term has utility for the data or computation it represents. The type is only valuable for confirming or denying the usage of a term. In contrast, once a *proof* satisfies a *proposition*, the proof becomes irrelevant, while the proposition is elevated to a theorem. The theorem is useful on its own without regard to any particular proof.

Similar to other programming languages, type `bool` can be satisfied by values `True` or `False`. In contrast to other programming languages, additional syntax, or data constructors, can be defined to satisfy the type `bool`. A constructor can take any number of terms of any types as input in order to create a boolean term. Although these new terms could be used in programs, just as `True` and `False` are, their main utility is in theorem proving. In Isabelle/HOL, propositions are isomorphic to terms of type `bool`. The constructors are defined with a set of inference rules, where each inference rule

defines the conditions sufficient for a construction to be valid, and at least one of the enumerated conditions is necessary for a valid construction. In other words, the constructor is equivalent to the boolean sum of all the conditions. The proposition definitions in terms of inference rules, or inductive definitions, are analogous to datatype definitions, just as propositions are analogous to types.

```
datatype 'a list = Nil | Cons 'a "'a list"

inductive sorted :: "('a  $\Rightarrow$  'a  $\Rightarrow$  bool)  $\Rightarrow$  'a list  $\Rightarrow$  bool" where
  Nil : "sorted P Nil" |
  Single : "sorted P (Cons x Nil)" |
  Cons : "P x y  $\wedge$  sorted P (Cons y ys)  $\wedge$  sorted P (Cons x (Cons y ys))"
```

The definitions of `list` and `sorted` can be combined with definitions of natural numbers to form propositions. Note that Isabelle/HOL's list is defined with syntactic sugar. `hd # tl` can be used instead of `Cons hd tl`, and `[a, b, c]` is `a # b # c # Nil`. In Isabelle/HOL, propositions may be proved by applying the inference rules. The method `rule` is used to work backwards from the goal until no further conditions need to be satisfied. Theorems may also be proved forwards from axioms, theorems or assumptions to the goal, using other methods like `drule` or `erule`.

```
datatype nat = Z | S nat

inductive lte :: "nat  $\Rightarrow$  nat  $\Rightarrow$  bool" where
  Eq : "lte n n" |
  Lt : "lte n1 n2  $\implies$  lte n1 (S n2)"

theorem "
  sorted lte (Cons (Z) (Cons (S Z) (Cons (S Z) (Cons (S (S (S Z))) Nil))))
"
apply (rule Cons)
apply (rule Lt)
apply (rule Eq)
apply (rule Cons)
apply (rule Eq)
apply (rule Cons)
apply (rule Lt)
apply (rule Lt)
apply (rule Eq)
apply (rule Single)
done
```

In truth, `True` and `False` are not primitive values, but actually just named instances of other propositions converted to boolean terms. `False` is defined to be the absurd statement that all

propositions are valid.

```
definition True :: bool where
  "True ≡ ((λx::bool. x) = (λx. x))"

definition False :: bool where
  "False ≡ (¬P. P)"
```

Hypothesis

I will derive a static analysis from Reppy and Xiao's algorithm, describing for each channel in a program, all processes that possibly send or receive on the channel. Additionally, it will classify channels as one-shot, one-to-one, fan-out, fan-in, or many-to-many. Instead of Serrano's algorithm[18] for the CFA used in Reppy and Xiao's algorithm, I will define a constraint-based specification and algorithm for the CFA. The method of determining topologies will be fairly similar to Reppy and Xiao's. The analysis of this work will also consider event combinators, which are not considered in Reppy and Xiao's work. I will show that the static analysis is informative by demonstrating programs for which the static analysis classifies some channels as fan-in, fan-out, and so on. I will show that the static analysis is sound by showing that for any program, the execution of the program results in the same sends and receives or fewer compared to the possible sends and receives described by the analysis. I will show that the static analysis is computable by demonstrating the existence of a computable function that takes any program as input and generates all sends and receives described by the analysis.

Evaluation

The main contributions of this work will be formal and mechanically verified proofs of communication properties of Concurrent ML, including an analysis derived from Reppy and Xiao's analysis. This work extends that of Reppy and Xiao by demonstrating formal proofs of soundness and extending the analysis to encompass event combinators for choice and transactions.

Architecture

To enable mechanical verification of the correctness of the proofs, I will construct the semantics, analysis and theorems in the formal language of Isabelle/HOL. To aid the development of formal proofs, I will design the analysis as a declarative specification as opposed to an algorithm. However, the declarative analysis will make the proof of computability less direct. To aid the scrutiny of the theorems' adequacy, I will express the definitions and propositions with the fewest number of structures, judgements, inferences rules, and axioms necessary. Efficiency of computation will be ignored in favor of verification. I will not rely on intermediate map or graph data structures, which Reppy and Xiao used for efficient computation. In order to relate the analysis to the operational

semantics, I will borrow Reppy and Xiao's strategy of stepping between sets of control paths tied to terms.

In this thesis work, I'm interested in communication topology soundness, rather than flow soundness. Nevertheless, I will need to prove additional flow soundness theorems en route to proving communication topology soundness. Restricting the grammar to a form that requires every abstraction and application to be bound to a variable would allow the operational semantics to maintain static term information necessary for proofs of flow soundness[3, 5]. The semantics would be defined as an environment based operational semantics, rather than a substitution based operational semantics. By avoiding simplification of terms in the operational semantics, it will be possible to relate the abstract values of the analysis to the values produced by the operational semantics, which in turn is relied on to prove flow soundness.

I will incorporate the restricted grammar and the environment based semantics into this work. The restricted grammar is impractical for a programmer to write, yet it is still practical for a language under automated analysis since there is a straight forward procedure to transform more flexible grammars into the restricted form as demonstrated by Flanagan et al [2]. Additionally, the restricted grammar melds nicely with the control path semantics. Instead of defining additional meta-syntax for program points of primitive operations, I can simply use the required variables of the restricted grammar to identify program points, and control paths will simply be sequences of let bound variables. A modification of Listing 2 illustrates the restrictive grammar applied to Concurrent ML.

```
let make = fun f () =>
  let reqCh = channel () in
  let loop = fun loop state =>
    let req = recv reqCh in
    let v = fst req in
    let replCh = snd req in
    let p1 = (reply, state) in
    let u1 = send p1 in
    let lres = loop v in
    lres
  let s = spawn (
    let z = 0 in
    let sres = loop z in
    sres
  ) in
  let mres = S reqCh in
  mres

let call = fun f (server, v) =>
  let reqCh = case server of (S x) => x in
  let replCh = channel () in
```

```

let p2 = (v, replCh) in
let p3 = (reqCh, p2) in
let u2 = send p3 in
let cres = recv replCh in
cres

```

Deliverables

- Technical paper
- Isabelle code

References

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Schedule

- Define the syntax of Concurrent ML using Isabelle/HOL (**completed**)
- Define the operational semantics (**completed**)
- Define communication topologies in terms of operational semantics (**completed**)
- Define the abstract value flow analysis (**completed**)
- Define the communication topology analysis (**completed**)
- Prove that the abstract value flow analysis is sound (**completed**)
- Prove that the communication topology analysis is sound (**in progress**)
- Prove that simple programs adhere to communication topology definitions
- Prove that the analysis is informative (not vacuous) with respect to example programs
- Extend the definitions and proofs with `choose_evt`
- Extend the definitions and proofs with `then_evt`
- Derive a constraint-based algorithm from specification
- Prove the analysis is computable using the algorithm (time permitting)
- Write technical paper
- Defense (May 2018)