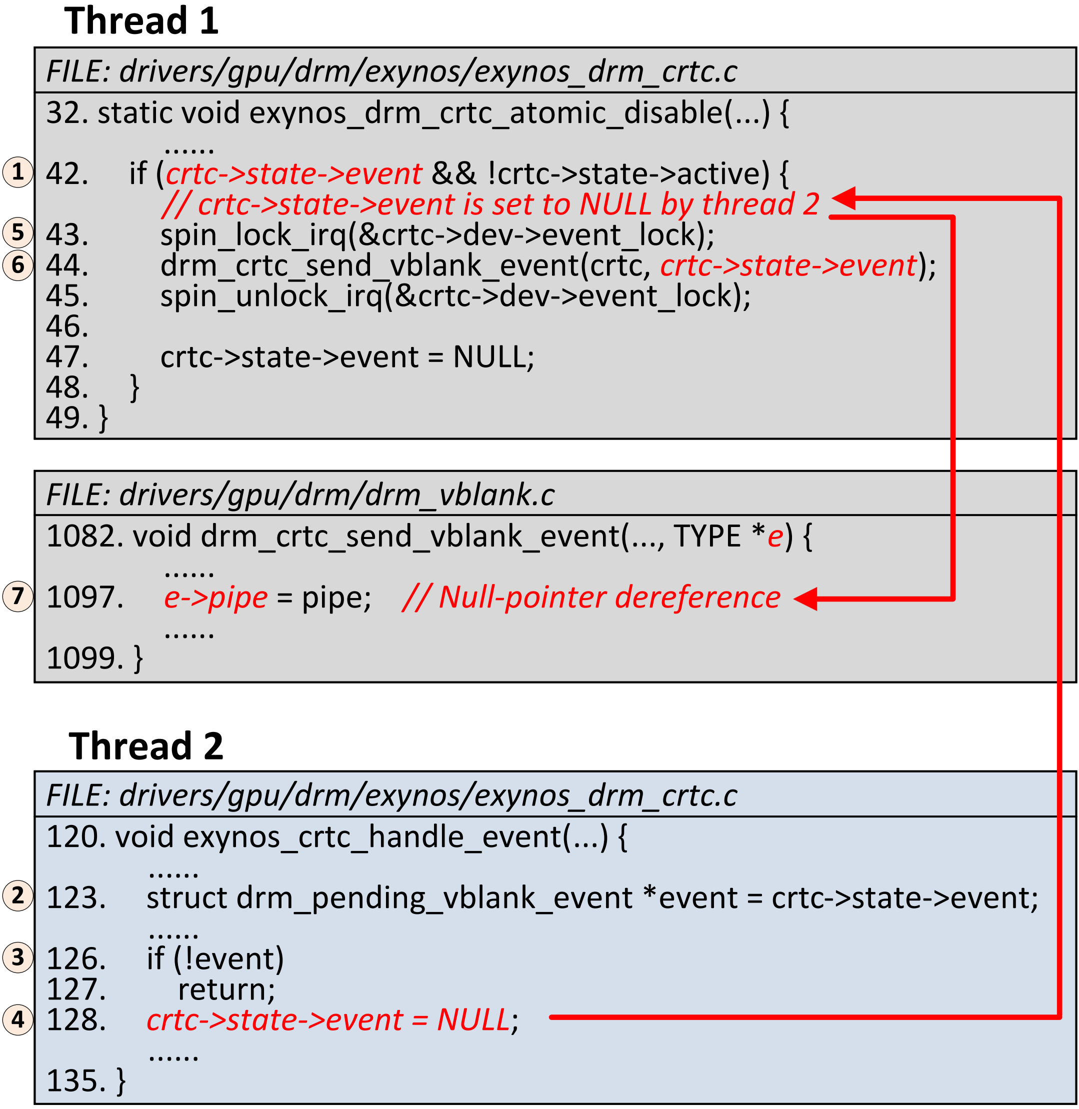

Thread 1

FILE: drivers/gpu/drm/exynos/exynos_drm_crtc.c

```
32. static void exynos_drm_crtc_atomic_disable(...) {
          ......
42.     if (crtc->state->event && !crtc->state->active) {
          // crtc->state->event is set to NULL by thread 2
43.         spin_lock_irq(&crtc->dev->event_lock);
44.         drm_crtc_send_vblank_event(crtc, crtc->state->event);
45.         spin_unlock_irq(&crtc->dev->event_lock);
46.
47.         crtc->state->event = NULL;
48.     }
49. }
```

FILE: drivers/gpu/drm/drm_vblank.c

```
1082. void drm_crtc_send_vblank_event(..., TYPE *e) {
          ......
1097.     e->pipe = pipe;    // Null-pointer dereference
          ......
1099. }
```

Thread 2

FILE: drivers/gpu/drm/exynos/exynos_drm_crtc.c

```
120. void exynos_crtc_handle_event(...) {
         ......
123.     struct drm_pending_vblank_event *event = crtc->state->event;
         ......
126.     if (!event)
127.         return;
128.     crtc->state->event = NULL;
         ......
135. }
```