

Electrical Rules Check Report

| Class | Document | Message |
|---------|----------------------|--|
| Warning | CW-Oscillator.SchDoc | Net NetC5_1 has no driving source (Pin C5-1, Pin U1-2, Pin U1-6, Pin |
| | | VR2-3) |

Design Rules Verification Report

Filename: E:\Xenon Labs\CW Oscillator\PCB.PcbDoc

Warnings 0 Rule Violations 4

| Warnings | |
|----------|---|
| Total | 0 |

| Rule Violations | |
|---|---|
| Clearance Constraint (Gap=10mil) (All),(All) | 0 |
| Short-Circuit Constraint (Allowed=No) (All),(All) | 0 |
| Un-Routed Net Constraint ((All)) | 0 |
| Modified Polygon (Allow modified: No), (Allow shelved: No) | 0 |
| Width Constraint (Min=10mil) (Max=20mil) (Preferred=18mil) (All) | 0 |
| Power Plane Connect Rule(Relief Connect)(Expansion=20mil) (Conductor Width=10mil) (Air | 0 |
| Hole Size Constraint (Min=1mil) (Max=100mil) (All) | 4 |
| Hole To Hole Clearance (Gap=10mil) (All),(All) | 0 |
| Minimum Solder Mask Sliver (Gap=10mil) (Disabled)(All),(All) | 0 |
| Silk To Solder Mask (Clearance=2mil) (Disabled)(IsPad),(All) | 0 |
| Silk to Silk (Clearance=1mil) (Disabled)(All),(All) | 0 |
| Net Antennae (Tolerance=0mil) (All) | 0 |
| Room CW-Oscillator (Bounding Region = (3260mil, 1005mil, 5060mil, 3160mil) | 0 |
| Height Constraint (Min=0mil) (Max=1000mil) (Prefered=500mil) (All) | 0 |
| Total | 4 |

Hole Size Constraint (Min=1mil) (Max=100mil) (All)
Hole Size Constraint: (118.11mil > 100mil) Pad Free-96(3390mil,1110mil) on Multi-Layer Actual Hole Size = 118.11m Hole Size Constraint: (118.11mil > 100mil) Pad Free-96(3390mil,3050mil) on Multi-Layer Actual Hole Size = 118.11mi Hole Size Constraint: (118.11mil > 100mil) Pad Free-96(4950mil,1110mil) on Multi-Layer Actual Hole Size = 118.11m Hole Size Constraint: (118.11mil > 100mil) Pad Free-96(4950mil,3050mil) on Multi-Layer Actual Hole Size = 118.11mi





