DISHA SRIVASTAVA

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OBJECTIVE

Seeking an internship using my background in Embedded Systems to gain more experience that can lead to a career.

EDUCATION

UNIVERSITY OF NORTH CAROLINA AT CHARLOTTE Charlotte, North Carolina, USA
Masters of Science in Electrical & Computer Engineering May 2016
Major: Embedded Systems GPA: 3.0/4.0

SYMBIOSIS INSTITUTE OF TECHNOLOGY Pune, Maharashtra, India
Bachelor of Technology May 2012
Major: Electronics & Telecommunications GPA: 3.377/4.0
Diploma in Business Management GPA: 3.027/4.0

TECHNICAL SKILLS

Programming Languages: C, C++, LaTeX, Embedded C
Development Tools: MATLAB, Mathcad, NI Multisim, Texas Instruments Code composer studio (CCS), PSpice, High performance embedded workshop IDE, GEM 5 Simulator, Eclipse, NetBeans, Microsoft Office suite, Adobe Photoshop
Architectures: ARM, x86, MIPS
Operating Systems: Unix/Linux (Ubuntu), Windows
Microprocessors & Microcontrollers: Renesas RX63N, MSP430, 8086, 8051
Protocols: TCP/IP, SPI, SCI, UART, Zigbee

WORK EXPERIENCE

- **Intern**, Adani Power Ltd., Gujarat, India July 2010
  Got familiarized with the major components and working of a Power Plant. Understanding of the current technology that is being used in the erection of a power plant. Trained in the most efficient Enterprise Resource Planning (ERP) software system, Systems Analysis & Products in Data Processing (SAP). Handled the management of the application of SAP that integrates a wide range of business functions including human resources, pay roll, finance and budget.
- **Intern**, Bharat Electronics Ltd., Pune, India July 2011
  Study and hands on experience of Hand held Laser Range Finder, LH-30 designed for use by infantry personnel. Examined the basic modules of the device, namely, Sighting Optics, Laser Transmitter, Laser Receiver and Range Processing electronics. Developed and fabricated a prototype PCB used for Range processing.
- **Intern**, Infosys Ltd., Mysore, India November 2012- March 2014
  First-hand training in Java and Oracle. Worked on communication network routing protocols based on OSI model. Extensive study of OSI model protocol suite.

ACADEMIC PROJECTS

- **Boolean Algebra Calculator** Fall 2014
  Implemented a Boolean expression simplification program using a Renesas RX63N Development board. The simplification was done using Quine-McCluskey algorithm.
- **Programming with RX63N Development Board** Fall 2014-Spring 2015
  - Developed various interactive application using RX63N board, including a Jet fighter game on the LCD (A/D converter), a digital oscilloscope (Timers, A/D converters and interrupts), a virtual pet (Timers, A/D converts), a digital watch prototype (RTC, Timers), WiFi Datalogger (XBee WiFi, UART, Timers)
  - Developed a Digital Level (Accelerometer) using MSP430 development board.

    - Established a wireless communication between two RX63N development boards using an XBee communication module. Also used the RSSI values returned from remote modules to determine their location (Easter Egg Hunt).
- **Simulation of ARM Processor using GEM5** Spring 2015
  Performed matrix multiplication on ARM processor with L1 data cache. Evaluated the performance by measuring total number of cycles elapsed to run the program for various data cache sizes.
- **Processing of Digital Signals using Netbeans** Spring 2015
    - Performed Sampling, Convolution, Circular Convolution, Discrete-Time Fourier Transform (DTFT), Discrete Fourier Transform (DFT), Fast Fourier Transform (FFT) of digital signals using Mathcad and Netbeans Interface.
- **High Gain 741 Operational Amplifier** Fall 2014
  Designed and simulated a 741 operational amplifier using PSpice AD and made the necessary modifications to obtain a theoretical gain of 1 million.
- **Two Stage CMOS Opamp with Active Load & High Frequency CMOS Opamp** Fall 2014
  Designed and simulation a two stage Complementary Metal Oxide Semiconductor operational amplifier with active load. From the results, a gain of greater than 4000 was observed using the PSpice simulator. The high speed/ frequency CMOS operational amplifier was designed to obtain a gain of more than 50 dB.
- **Accelerometer Based Mouse** January 2012
  Designed a hand held mouse to demonstrate a practical application of accelerometers. It describes the design of a system which enables comprehensive control of a PC, via a wireless, hand-held device without the need for additional driver software.

- Won multiple awards in Intra College Badminton tournaments. Maharashtra, India
- Won multiple awards in Inter School Dance Competitions Madhya Pradesh, India

RELEVANT COURSEWORK

Embedded Systems | Advanced Embedded Systems | Computer Architecture | Research Tools & Techniques in Computer Engineering | Linear Integrated Electronics | Digital Signal Processing