

Figure 1: Picture of Actual Circuit and System Set-Up

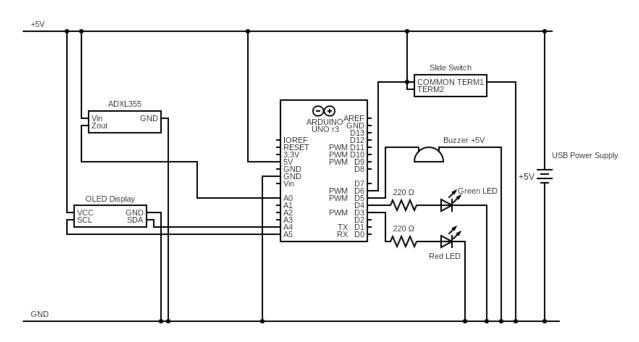


Figure 2: Complete Circuit Diagram