

**ENGIN 346: Microcontrollers**  
Engineering Department  
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**Instructor:** Dr. Honggang Zhang

Project Report for:

**Project #5: ADC and DAC**

By

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Signature: \_\_\_\_\_

A handwritten signature in black ink, appearing to read "Tyler McKean", written over a horizontal line.

## TABLE OF CONTENTS

Introduction .....	3
Background and Design .....	3
Experiment and Results .....	5
Observations and Conclusions .....	6
Bibliography .....	7

NOTE: To automatically update ToC, right click above and select "Update Field"

## INTRODUCTION

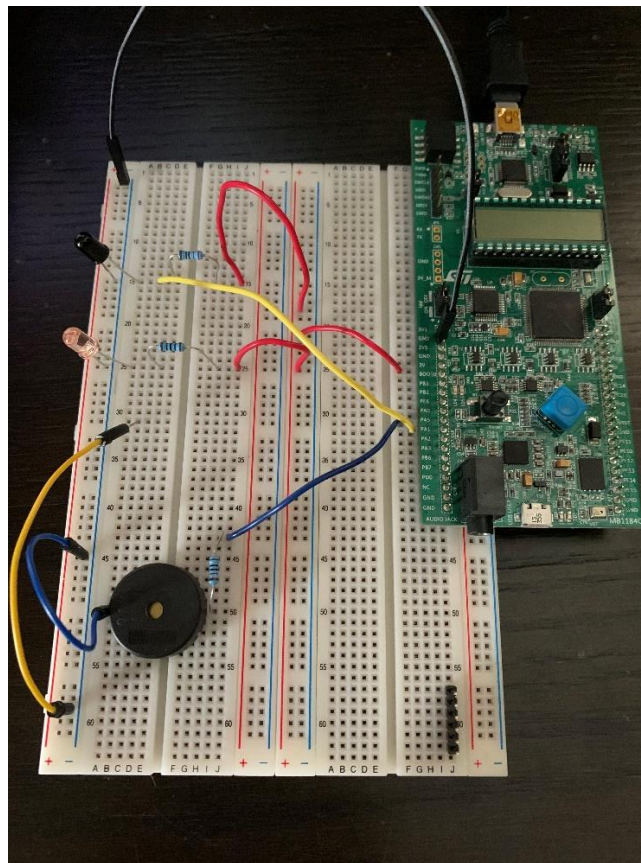
For Project 5, the main goal was:

1. Understanding the basic concepts of ADC and DAC.

The main objectives of the project are to use the onboard ADC to capture an analog voltage from an Infrared transmitter diode and Infrared phototransistor that varies the voltage when an obstacle moves closer to the sensors. The transmitter diode's infrared signals are captured more from the receiver when an obstacle is closer because more of the infrared signal bounces back towards the receiver. This causes the voltage to dip down by several millivolts, so by using the ADC we can capture a particular distance away from both the infrared diodes. After choosing a particular voltage, which is proportional to some distance, the Green LED of the Discovery Board should turn on, and trigger the DAC to omit a tone of 440Hz to a Piezoelectric buzzer. This can be achieved by counting the number of interrupts that occur in  $(1/440\text{Hz})$  secs and programming the DAC to output a square wave with a 50% duty cycle and frequency of 440Hz, which is a musical A note. Both the ADC and DAC need to be initialized and triggered by the trigger output event (TRGO) of TIM4 as well.

## BACKGROUND AND DESIGN

The design of this project required the initialization of GPIO pins PA.1 for the ADC, pin PA.5 for the DAC, and TIM4 to be configured to trigger both the ADC and DAC process. The physical connections for the circuit can be seen in Figure 1 below.



**Figure 1 - Physical Connections for Project 5 to the Discovery Board**

GPIO Pin PA.1 make use of ADC1 for the project and captures the voltage drop across the 5mm phototransistor. The phototransistor's anode was tied to a 2.2k $\Omega$  resistor which was tied to the 3V pin of the Discovery board, with the cathode plugged right into the GND rails of the breadboard. The Infrared LED had its cathode also plugged into the GND rails of the breadboard with its anode tied to a 100 $\Omega$  resistor and the 3V pin of the Discovery board. The forward current of the Infrared LED needed to be close to 20mA in order to have a radiation intensity of about 20mW/sr to trigger the phototransistor. When enough of the infrared signal was captured by the phototransistor a base current in the transistor would turn it on and produce a low voltage level that PA.1 could capture. The voltage across the phototransistor drops more when an obstacle reflects more of the infrared signal from the infrared LED. The ADC output can be estimated by the formula:

$$\text{ADC Output} = \text{round}((2^N - 1) * V_{\text{in}}/V_{\text{ref}}) \quad (\text{Eq 1-1})$$

where N is the bit resolution,  $V_{\text{ref}}$  is the reference voltage, and  $V_{\text{in}}$  is the voltage drop across the phototransistor. For this circuit, we are using the 3V pin on the Discovery board as our  $V_{\text{ref}}$  and the bit resolution was set as 12-bits in the ADC CFGR. So, when the ADC reads an analog voltage that's dropping across the phototransistor, that value will be converted into a digital value ranging from 0 to 4095 based on the bit resolution of the ADC. After the ADC converts the analog value into a digital value, the End of Conversion flag will be triggered, which will stop the main program and redirect its attention to the ADC1\_2\_IRQHandler. Within the handler, I was able to save the current value in the Data Register of the ADC to a variable called **result**. An if statement could then be written to check when that result variable was proportional to an analog voltage at some distance away from the phototransistor. If that statement was true, the handler would turn on the Green LED from the Green\_LED\_On() function from the included LED.c files added to project window of the program. If the statement was false, the Green LED would then be turned off. These statements made it possible to move an obstacle close to the IR sensor and turn the Green LED on and would turn it off when the obstacle backs away from the sensor.

Timer 4 was used as the output trigger event that would trigger both the ADC and DAC for the project. The timer was initialized using the TIM4\_Init() in the main program which came from the included TIM.c file added to the project window of the project. The timer was initialized as PWM Mode 1 with a PSC value of 18, ARR value of 18, and CCR1 value of 9. These values scaled the system clock down from 80MHz to about 221kHz. The CCR1 value also made it so that the OC1REF had a duty cycle of about 50%. The trigger interrupt and update interrupt were also enabled in order to use the TIM4\_IRQHandler. Afterwards, timer 4 was then enabled in the NVIC IRQ so that we could use the timer's handler to trigger the DAC output.

DAC channel 2 is tied to GPIO pin PA.5 on the Discovery board and was used to send a 440Hz square wave to the Piezoelectric buzzer when the ADC handler read a specific value in its DR. Pin Pa.5 was tied to a 1k $\Omega$  resistor that was tied to the buzzer that was then tied to the GND rails of breadboard. The initialization for DAC channel 2 was done in the DAC\_Init() function provided from the DAC.c file added to the project window. TIM4\_TRGO was selected as the DAC trigger for channel 2 in the DAC CR. I was able to output a 440Hz square wave pulse to the buzzer was utilizing the TIM4 handler and counting the number of interrupts that occurred within 1/440Hz seconds. Since I was using the 80MHz system clock, I was able to count about 503 times the interrupt occurred within that time frame. Since a square wave is essentially a high voltage and then low voltage for a 50% duty cycle. I wrote some if statements that included a temporary variable that was set to equal 1 whenever the ADC read a particular voltage with an obstacle at a close distance from the IR sensor. When this temp variable was 1, I set the DH12R2 output to be 4095, which is a digitized high voltage of 3V, for high the time frame of 1/440Hz

and then set the DAC to output a zero voltage for the second half of that timeframe. This is how I was able to output a square wave at a frequency of 440Hz, which produced a musical A note from the buzzer.

## EXPERIMENT AND RESULTS

This section of the report will discuss what pins and registers of the Discovery board needed to be initialized to achieve the project goals.

To start with the ADC, I included both an ADC.c and ADC.h files into my project, so I could simply call a function called ADC\_Init() that would provide all the proper configurations in order to initialize the ADC for PA.1. The function starts by enabling the clock of the ADC in the AHB2ENR and then resetting and initializing the ADC interface in the AHB2RSTR. A function called ADC\_Pin\_Init() then enables the clock for GPIO Port A and configures pin PA.1 as Analog (11) in the MODER, sets the pin as no pull-up/pull-down in the PUPDR, and connects the analog switch to the ADC input by enabling bit GPIO\_ASCR\_EN\_1 in the Analog Switch Control Register. The ADC\_Init() function then executes and sets the ADC to have 12-bit resolution and the data alignment to be right aligned. In order for the ADC to be triggered by TIM4\_TRGO event the ADC1 CFGR needs to set the EXTSEL bits to be 1100 and this is precisely what is done. The external trigger enable bits are then set to 01 for hardware trigger with detection on the rising edge and ADC1 is then set to single conversion in the ADC1 CFGR. Since we are only performing one channel conversion, the ADC regular sequency register is then programmed for only one conversion and specified to channel 6 by altering the bits in the SQR1 because PA.1 is assigned to ADC12\_IN6. The channel is a single-ended input so the DIFSEL bit for channel 6 is cleared. The ADC sample time is then initialized for 24.5 ADC clock cycles at the system clock of 80MHz by clearing the SMPR1\_SMP6 bits and setting them as 11. This would then mean that the conversion time for the ADC would be  $(24.5\text{cycles}/80\text{MHz}) = 0.3\mu\text{secs}$ . The End of Regular Conversion interrupt is then set and enabled in the NVIC for the ADC1\_2\_IRQn. The handler will be programmed to check when the ADC reads a particular voltage level from the IR sensor to turn the Green LED on. The ADC\_Init() function then finishes by enabling the ADC in the ADC control register and then checks if the ADC is ready in the ISR.

For the Timer, the clock was set first by enabling the APB1ENR1 from the RCC for Timer 4. The MMS bits in the TIM4 CR2 was then set to 100 for OC1REF as TRGO. The trigger interrupt and update interrupt bits were set in the DIER next. To initialize the timer as PWM mode 1, the OC1M bits were set as 0110 in the CCMR1. As mentioned before, the value of 18 was set for the PSC and ARR values, with the CCR1 set to 9 to achieve a 50% duty cycle. The timer was then enabled by the CEN bit in the CR1 and then shortly after the TIM4\_IRQ was set in the NVIC.

For the DAC, GPIO pin PA.5 first needed to be initialized. The clock for GPIO Port A was enabled by the AHB2ENR from the RCC. To configure PA.5 as DAC1\_OUT2 the pin needed to set as Analog (11) in the MODER and no pull-up/pull-down in the PUPDR, which was done by clearing bits 11 and 10. The DAC was then configured by enabling the DAC Clock by the APB1ENR1 to the DAC1ENR bit in the RCC. The function DAC\_Calibration\_Channel() function then calibrated DAC Channel 2. The DAC mode control register was then set for DAC Channel 2 to be connected to an external pin with Buffer enabled by cleared the MODE2 bits in the MCR. Afterwards, the TEN2 bit was set in the DAC CR to enable the trigger. The TSEL2 bits were then configured as 101 for TIM4\_TRGO as the trigger for Channel 2 of the DAC. Lastly, the EN2 bit in the DAC CR was set to enable DAC Channel 2.

In the ADC Handler, the interrupt request checks when the EOC flag was set which indicates when the ADC finishes converting an analog voltage to a digital value. The value in the ADC DR was

then stored in the variable **result**, which clears the EOC flag. As mentioned before, two if statements checked when the result value was a particular digital value between 0 and 4095. I had the statement check when the analog output was less than or equal to 0xE74, which an analog value of about 2.71V across the phototransistor. This distance is about 2-3cms away from both the infrared LED and phototransistor. If this statement was true, I called the `Green_LED_On()` function and set the temp variable to 1, else the `Green_LED_Off()` function was called, and the temp variable was set to zero. The temp variable was then used a flag to tell the `TIM4_Handler` when to output a square wave to the buzzer.

In the `TIM4_Handler`, the SR checks whether the CC1IF is not equal to zero, indicating an interrupt request has occurred. The CC1IF is then reset back to zero. Another variable called **result\_DAC** is then assigned as an integer `i` that continuously increments. A nested if statement checks if the variable **temp** is equal to 1 and then outputs the square wave if its true. The square wave is created by using the **result\_DAC** with the modulus operation with the value of 503. This is because about 503 timer interrupts occur within 1/440Hz seconds, which is the period of a musical A note. If the result of this modular operation is less than or equal to the value 251, which is about half the period of the A note, the DHR12R2 of the DAC is set to a digital value of 4095, which is a voltage value of 3V, else the DHR12R2 is equal to zero. This creates a pulse with a duty cycle of 50% and a period of 1/440Hz, which satisfies the requirements to synthesize a musical A note to the buzzer only when an obstacle is about 2-3cm away from the phototransistor. Lastly, the TIF and UIF flags are cleared to end the timer interrupt handler before continuing to resume the main program.

The last part of the main program consists of a while loop that uses the LCD.c functions to display the digitized output value from the ADC1\_IN6. The loop then runs infinitely for as long as the Discovery board has power.

## OBSERVATIONS AND CONCLUSIONS

For voltage measurements and analysis of the Project 5 circuit, I used the oscilloscope of the Analog Discovery 2. I started my first observations by reading the voltage across the phototransistor when my hand was hovered over the IR from about 5cm away. Screenshots from the oscilloscope can be seen below.

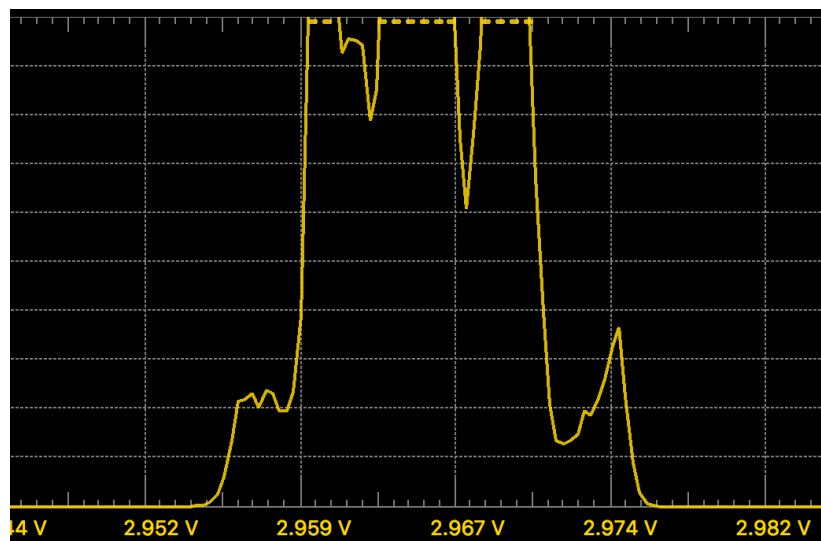


Figure 2 - Voltage drop across the Phototransistor at 1 cm away

