Wishbone Slave wbs2: GPIO & GBTx testing

The registers of wishbone slave 2 are used for general purpose I/O and the GBTx interface module. This slave provides 80 bits of general purpose I/O and 16 bits of self-resetting bits (“pulser” register). These registers are currently attached as follows (wishbone slave is at address 0x1xx):

Register	Bit	
0	0	NOT GBTX_RST_n
	1	NOT SCA_RST_n
	2	NOT PU_RESET_n
	3	NOT QADC_RESET_n
	4	NOT QADC_CONVST_n
	15:5	
1	2:0	sBitSlipRx
	3	
	6:4	sBitSlipTX
	7	
	9:8	sTestPatternSel
	14:10	
	15	GBTX_TX_DATAVALID
2	5:0	SCA_GPIO_o
	7:6	
	8	SCA_GPIO_T (1=in, 0=out)
	15:9	