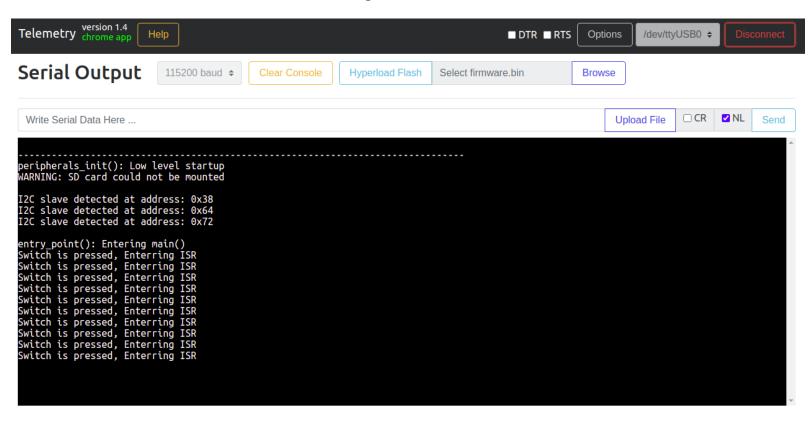
This file has the screenshots corresponding to all 3 parts of Interrupt assignment.

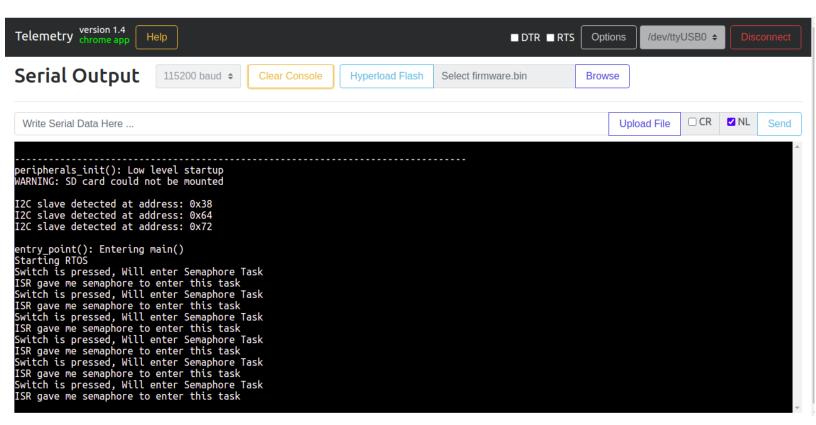
## Part 0:

- Initially because of switch bouncing, for single press code was entering ISR multiple times. To add Switch debounce logic I added the logic to disable Interrupt at the beginning of ISR and enabling back while exiting the ISR.
- This logic helped to enter ISR only one time for single button press.
- Below is the result with switch debounce logic.



## Part 1:

- The fprintf statements are added to reflect all implemented steps:
- Below screenshot is showing the prints for entering ISR and then for entering wait\_on\_semaphore task.



## Part 2:

- Below screenshot is showing prints for:
- 1) attaching callback for particular configured GPIO ISR
- 2) dispatching to corresponding ISR
- 3) entering ISR
- 4) entering wait\_on\_semaphore task

