

P2.1 FUNCTIONAL SPECIFICATION

Jean-Pierre Deschamps

University Rovira i Virgili, Tarragona, Spain

1 INSTRUCTION TYPES

- 8 *instruction types* are defined;
- to each instruction => *code*, *list of parameters*:

Code and list of parameters	Operation
<i>(ASSIGN_VALUE, k, A)</i>	$X_k := A;$
<i>(DATA_INPUT, k, j)</i>	$X_k := IN_j;$
<i>(DATA_OUTPUT, i, j)</i>	$OUT_i := X_j;$
<i>(OUTPUT_VALUE, i, A)</i>	$OUT_i := A,$
<i>(OPERATION, i, j, k, f)</i>	$X_k := f(X_i, X_j);$
<i>(JUMP, N)</i>	go to N ;
<i>(JUMP_POS, i, N)</i>	if $X_i > 0$ go to N ;
<i>(JUMP_NEG, i, N)</i>	if $X_i < 0$ go to N ;

(OPERATION, i, j, k, f): f is an identifier of an operation (*add*, *subtract*, ...) that one of the processing resources can execute (to be defined later)

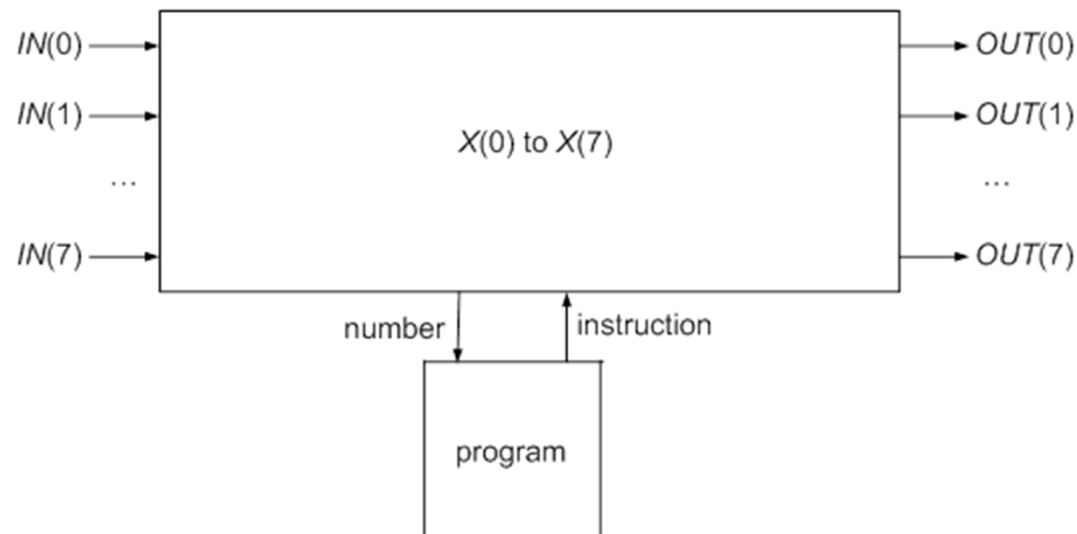
Example: temperature control system.

0: <i>ASSIGN_VALUE</i> , 5, 10	(X5 := 10)
1: <i>DATA_INPUT</i> , 0, 0	(X0 := IN0)
2: <i>DATA_INPUT</i> , 1, 1	(X1 := IN1)
3: <i>OPERATION</i> , 4, 0, 1, subtract	(X4 := X0 - X1)
4: <i>JUMP_NEG</i> , 4, 7	(if X4 < 0 then go to 7)
5: <i>JUMP_POS</i> , 4, 9	(if X4 > 0 then go to 9)
6: <i>JUMP</i> , 10	(go to 10)
7: <i>OUTPUT_VALUE</i> , 0, 1	(OUT0 := 1)
8: <i>JUMP</i> , 10	(go to 10)
9: <i>OUTPUT_VALUE</i> , 0, 0	(OUT0 := 0)
10: <i>DATA_INPUT</i> , 3, 2	(X3 := IN2)
11: <i>DATA_INPUT</i> , 2, 2	(X2 := IN2)
12: <i>OPERATION</i> , 4, 2, 3, subtract	(X4 := X2 - X3)
13: <i>OPERATION</i> , 4, 4, 5, subtract	(X4 := X4 - X5)
14: <i>JUMP_NEG</i> , 4, 11	(if X4 < 0 then go to 11)
15: <i>JUMP</i> , 1	(go to 1)

2 FUNCTIONAL SPECIFICATION

Digital system to be implemented:

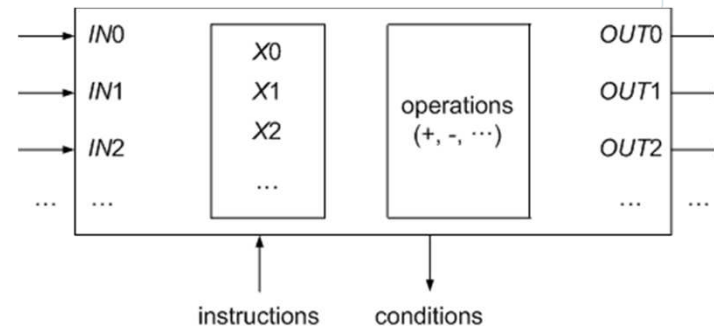
- eight 8-bit data inputs $IN(0)$, $IN(1)$, \dots , $IN(7)$;
- eight 8-bit data outputs $OUT(0)$, $OUT(1)$, \dots , $OUT(7)$;
- an input *instruction* whose value corresponds to one of the eight types ($ASSIGN_VALUE$, i , A), ($DATA_INPUT$, i , j), \dots ;
- an output *number* that selects the instruction to be executed.



Comment:

Initial specification (week 1):

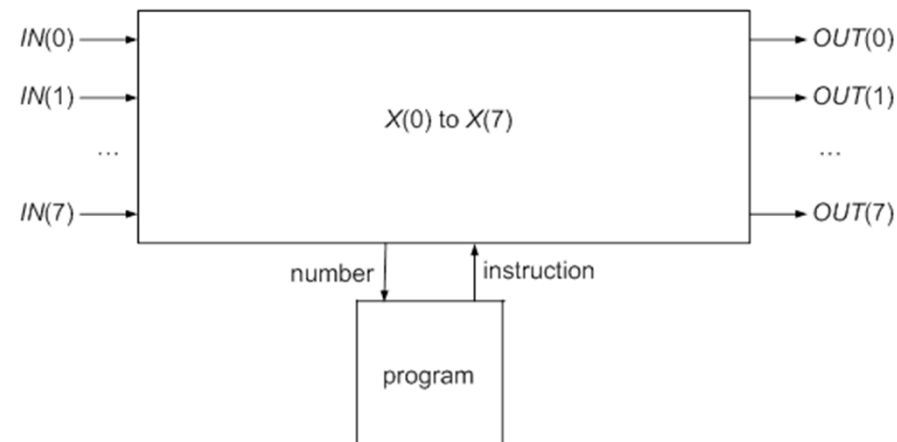
- **next instruction** defined by an **external** circuit / programmer in function of some internal *conditions*



Modified specification:

- **next instruction number internally** computed by the processor;
- program stored within a **memory**.

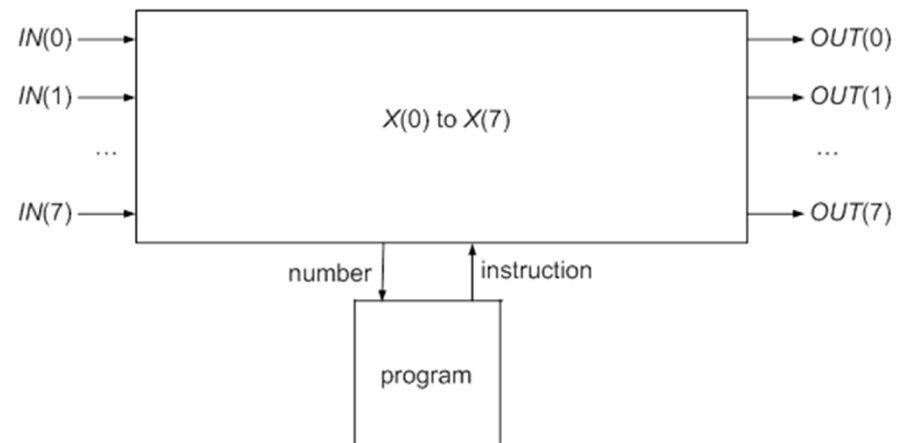
(= **von Neumann architecture**)



Algorithm: functional specification

```

number := 0;
loop
  case program(number) is
    when (ASSIGN_VALUE, k, A) =>
      X(k) := A; number := number + 1;
    when (DATA_INPUT, k, j) =>
      X(k) := IN(j); number := number + 1;
    when (DATA_OUTPUT, i, j) =>
      OUT(i) := X(j); number := number + 1;
    when (OUTPUT_VALUE, i, A) =>
      OUT(i) := A; number := number + 1;
    when (OPERATION, i, j, k, f) =>
      X(k) := f(X(i), X(j)); number := number + 1;
    when (JUMP, N) =>
      number := N;
    when (JUMP_POS, i, N) =>
      if X(i) > 0 then number := N; else number := number + 1; end if;
    when (JUMP_NEG, i, N) =>
      if X(i) < 0 then number := N; else number := number + 1; end if;
  end case;
end loop;
  
```



X: previously declared vector;
 number of components: to be defined later;
 components: 8-bit numbers.

(Exercise)

Assume that

- X is a 16-component vector and each component is an 8-bit number,
- the maximum number of instructions of a program is 256,
- there are only 2 different operations f .

How many different instructions can be defined?

(Solution)

P2.1

<i>(ASSIGN_VALUE, k, A)</i>	$Xk := A;$	$16 \cdot 256 = 4096$
<i>(DATA_INPUT, k, j)</i>	$Xk := INj;$	$16 \cdot 8 = 128$
<i>(DATA_OUTPUT, i, j)</i>	$OUTi := Xj;$	$8 \cdot 16 = 128$
<i>(OUTPUT_VALUE, i, A)</i>	$OUTi := A,$	$8 \cdot 256 = 2048$
<i>(OPERATION, i, j, k, f)</i>	$Xk := f(Xi, Xj);$	$16 \cdot 16 \cdot 16 \cdot 2 = 8192$
<i>(JUMP, N)</i>	go to $N;$	256
<i>(JUMP_POS, i, N)</i>	if $Xi > 0$ go to $N;$	$16 \cdot 256 = 4096$
<i>(JUMP_NEG, i, N)</i>	if $Xi < 0$ go to $N;$	$16 \cdot 256 = 4096$

$4096 + 128 + 128 + 2048 + 8192 + 256 + 4096 + 4096 = \mathbf{23,040}$ different instructions.

$$(2^{14} < 23,040 < 2^{15})$$

SUMMARY

- definition of the **instruction set**;
- **von Neumann** architecture;
- **functional** specification.

P2.1

P2.2 STRUCTURAL SPECIFICATION

Jean-Pierre Deschamps

University Rovira i Virgili, Tarragona, Spain

1 BLOCK DIAGRAM

```
number := 0;
loop
  case program(number) is
    when (ASSIGN_VALUE, k, A) =>
      X(k) := A; number := number + 1;
    when (DATA_INPUT, k, j) =>
      X(k) := IN(j); number := number + 1;
    when (DATA_OUTPUT, i, j) =>
      OUT(i) := X(j); number := number + 1;
    when (OUTPUT_VALUE, i, A) =>
      OUT(i) := A; number := number + 1;
    when (OPERATION, i, j, k, f) =>
      X(k) := f(X(i), X(j)); number := number + 1;
    when (JUMP, N) =>
      number := N;
    when (JUMP_POS, i, N) =>
      if X(i) > 0 then number := N; else number := number + 1; end if;
    when (JUMP_NEG, i, N) =>
      if X(i) < 0 then number := N; else number := number + 1; end if;
  end case;
end loop;
```

P2.2

external inputs:

IN0, IN1, ..., IN7;
instruction;

external outputs:

OUT0, OUT1, ..., OUT7;
number;

internal data: *X*;

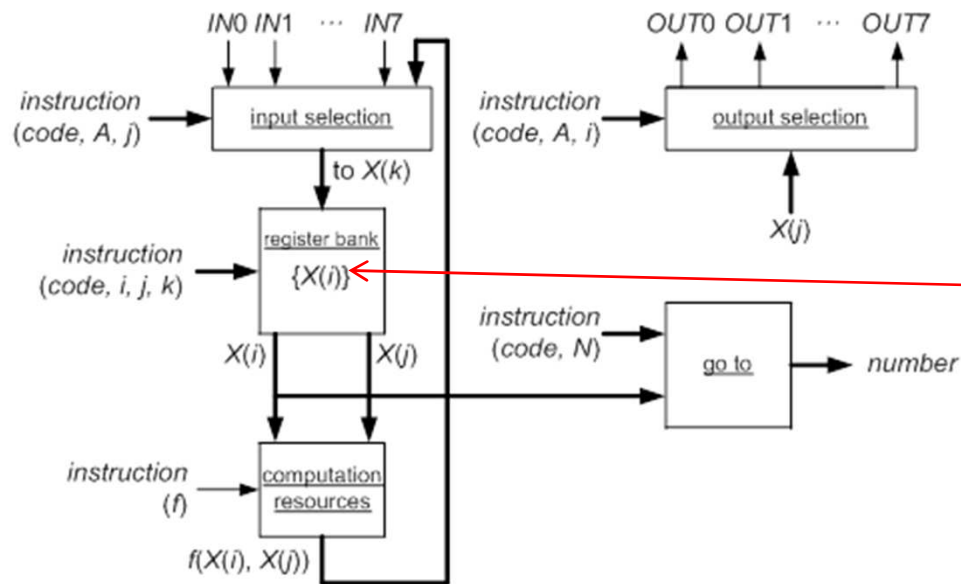
data transfers:

OUT(i) <= X(j) or A;
number <= (number + 1) or N;
X(k) <= A or IN(j) or f;

operations: *f(X(i), X(j)).*

INTERNAL DATA

P2.2



external inputs:

$IN0, IN1, \dots, IN7$;
 $instruction$;

external outputs:

$OUT0, OUT1, \dots, OUT7$;
 $number$;

internal data: X ;

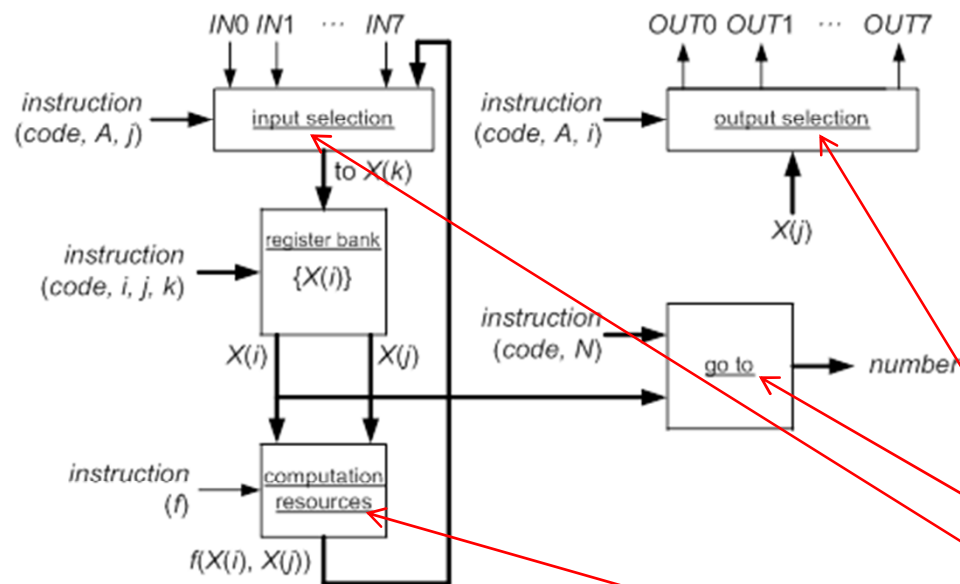
data transfers:

$OUT(i) \leq X(j)$ or A ;
 $number \leq (number + 1)$ or N ;
 $X(k) \leq A$ or $IN(j)$ or f ;

operations: $f(X(i), X(j))$.

DATA TRANSFERS

P2.2



external inputs:

$IN0, IN1, \dots, IN7$;

instruction;

external outputs:

$OUT0, OUT1, \dots, OUT7$;

number;

internal data: X ;

data transfers:

$OUT(i) \leq X(j)$ or A ;

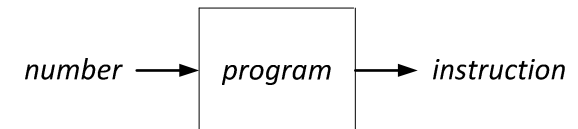
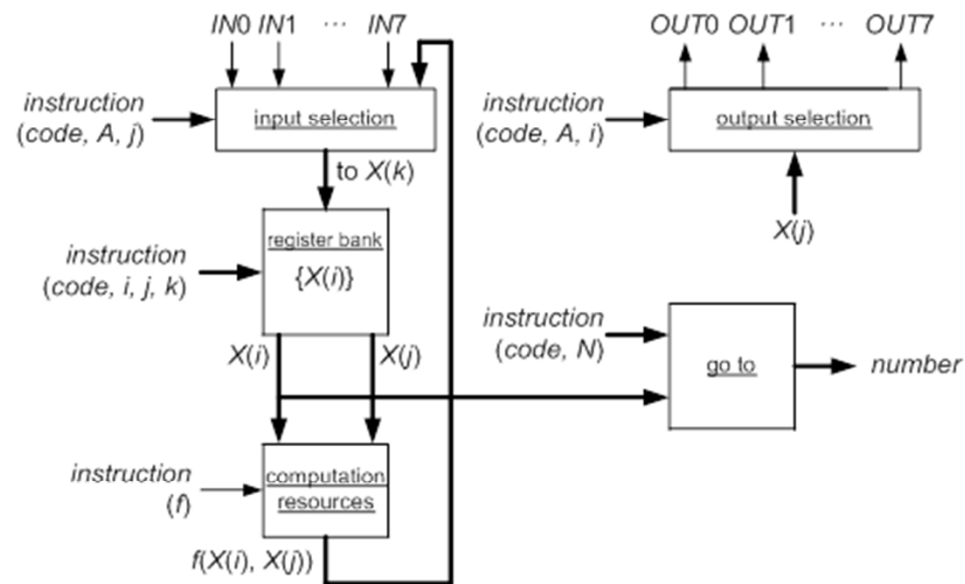
$number \leq (number + 1)$ or N ;

$X(k) \leq A$ or $IN(j)$ or f ;

operations: $f(X(i), X(j))$.

BLOCK DIAGRAM

P2.2



INSTRUCTIONS (additional specification)

P2.2

Code and list of parameters	Operation
<i>(ASSIGN_VALUE, k, A)</i>	$X_k := A;$
<i>(DATA_INPUT, k, j)</i>	$X_k := IN_j;$
<i>(DATA_OUTPUT, i, j)</i>	$OUT_i := X_j;$
<i>(OUTPUT_VALUE, i, A)</i>	$OUT_i := A,$
<i>(OPERATION, i, j, k, f)</i>	$X_k := f(X_i, X_j);$
<i>(JUMP, N)</i>	go to N ;
<i>(JUMP_POS, i, N)</i>	if $X_i > 0$ go to N ;
<i>(JUMP_NEG, i, N)</i>	if $X_i < 0$ go to N ;

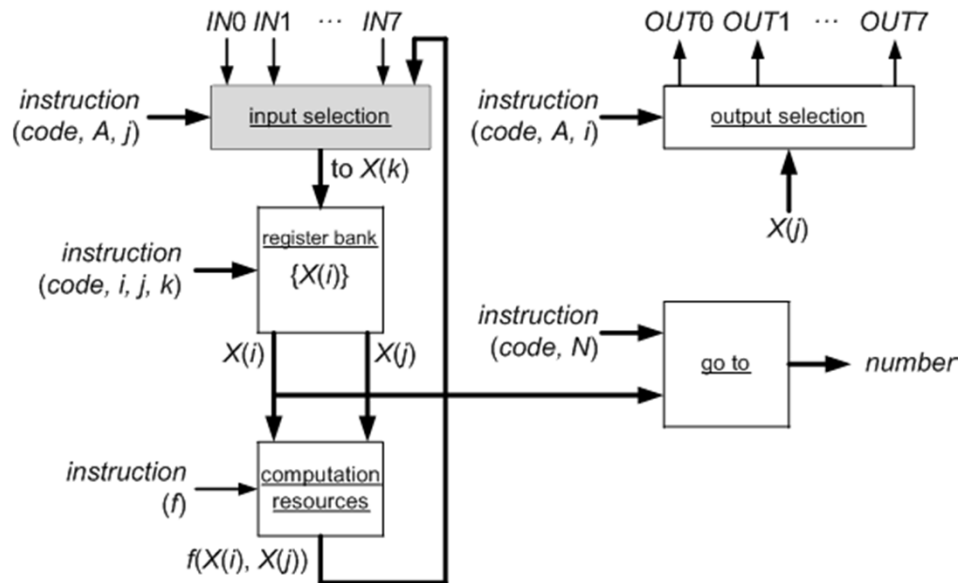
- only **two operations** f : + and - ;
- register bank: **16 registers** (i, j and k are 4-bit numbers);
- maximum **number of instructions: 256** (N is an 8-bit number).

Instruction encoding will be defined later.

2 BLOCK DESCRIPTION

2.1 INPUT SELECTION

P2.2



number := 0;

loop

case program(number) is

when (ASSIGN_VALUE, k, A) =>

$X(k) := A$; number := number + 1;

when (DATA_INPUT, k, j) =>

$X(k) := IN(j)$; number := number + 1;

when (DATA_OUTPUT, i, j) =>

$OUT(i) := X(j)$; number := number + 1;

when (OUTPUT_VALUE, i, A) =>

$OUT(i) := A$; number := number + 1;

when (OPERATION, i, j, k, f) =>

$X(k) := f(X(i), X(j))$; number := number + 1;

when (JUMP, N) =>

number := N;

when (JUMP_POS, i, N) =>

if $X(i) > 0$ then number := N; else number := number + 1; end if;

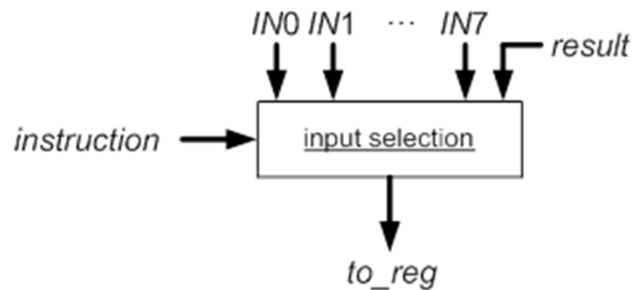
when (JUMP_NEG, i, N) =>

if $X(i) < 0$ then number := N; else number := number + 1; end if;

end case;

end loop;

(input selection)



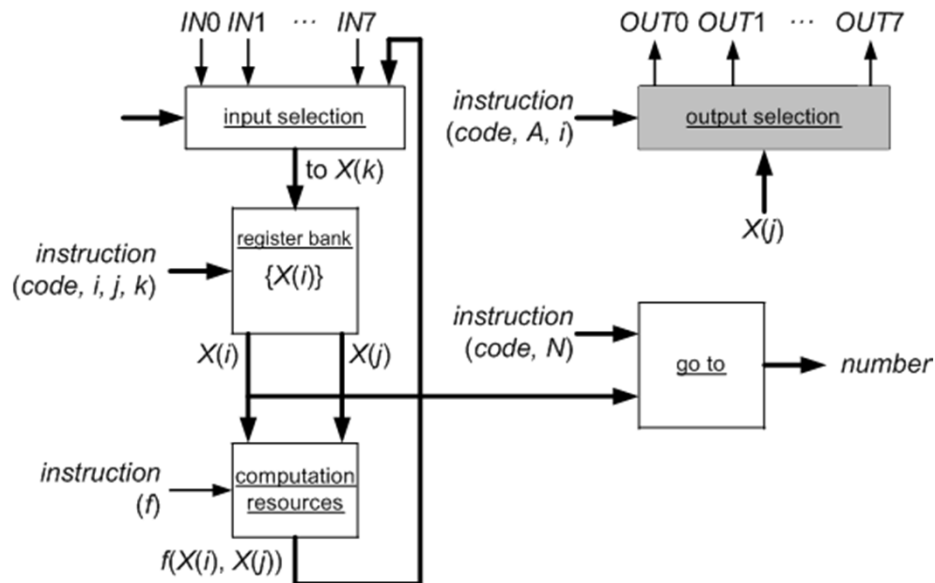
Functional specification

```
loop
  case instruction is
    when (ASSIGN_VALUE, k, A) =>
      to_reg := A;
    when (DATA_INPUT, k, j) =>
      to_reg := IN(j);
    when (OPERATION, i, j, k, f) =>
      to_reg := result;
    when others =>
      to_reg := don't care;
  end case;
end loop;
```

```
number := 0;
loop
  case program(number) is
    when (ASSIGN_VALUE, k, A) =>
      X(k) := A; number := number + 1;
    when (DATA_INPUT, k, j) =>
      X(k) := IN(j); number := number + 1;
    when (DATA_OUTPUT, i, j) =>
      OUT(i) := X(j); number := number + 1;
    when (OUTPUT_VALUE, i, A) =>
      OUT(i) := A; number := number + 1;
    when (OPERATION, i, j, k, f) =>
      X(k) := f(X(i), X(j)); number := number + 1;
    when (JUMP, N) =>
      number := N;
    when (JUMP_POS, i, N) =>
      if X(i) > 0 then number := N; else number := number + 1; end if;
    when (JUMP_NEG, i, N) =>
      if X(i) < 0 then number := N; else number := number + 1; end if;
  end case;
end loop;
```

P2.2

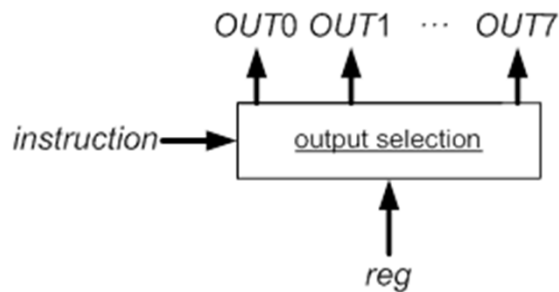
2.2 OUTPUT SELECTION



```

number := 0;
loop
  case program(number) is
    when (ASSIGN_VALUE, k, A) =>
      X(k) := A; number := number + 1;
    when (DATA_INPUT, k, j) =>
      X(k) := IN(j); number := number + 1;
    when (DATA_OUTPUT, i, j) =>
      OUT(i) := X(j); number := number + 1;
    when (OUTPUT_VALUE, i, A) =>
      OUT(i) := A; number := number + 1;
    when (OPERATION, i, j, k, f) =>
      X(k) := f(X(i), X(j)); number := number + 1;
    when (JUMP, N) =>
      number := N;
    when (JUMP_POS, i, N) =>
      if X(i) > 0 then number := N; else number := number + 1; end if;
    when (JUMP_NEG, i, N) =>
      if X(i) < 0 then number := N; else number := number + 1; end if;
  end case;
end loop;
  
```

(output selection)



Functional specification

loop

case program(number) is

when (DATA_OUTPUT, i, j) =>

OUT(i) := reg;

when (OUTPUT_VALUE, i, A) =>

OUT(i) := A;

when others =>

OUT(i) := OUT(i);

end case;

end loop;

registered output
(like a memory element)

P2.2

UAB

Universitat Autònoma
de Barcelona

number := 0;

loop

case program(number) is

when (ASSIGN_VALUE, k, A) =>

X(k) := A; number := number + 1;

when (DATA_INPUT, k, j) =>

X(k) := IN(j); number := number + 1;

when (DATA_OUTPUT, i, j) =>

OUT(i) := X(j); number := number + 1;

when (OUTPUT_VALUE, i, A) =>

OUT(i) := A; number := number + 1;

when (OPERATION, i, j, k, f) =>

X(k) := f(X(i), X(j)); number := number + 1;

when (JUMP, N) =>

number := N;

when (JUMP_POS, i, N) =>

if X(i) > 0 then number := N; else number := number + 1; end if;

when (JUMP_NEG, i, N) =>

if X(i) < 0 then number := N; else number := number + 1; end if;

end case;

end loop;

SUMMARY

- **Structural description = block diagram**, based on
 - ✓ internal data,
 - ✓ data transfers,
 - ✓ operations.

- **Functional description of blocks:**
 - ✓ input selection,
 - ✓ output selection.