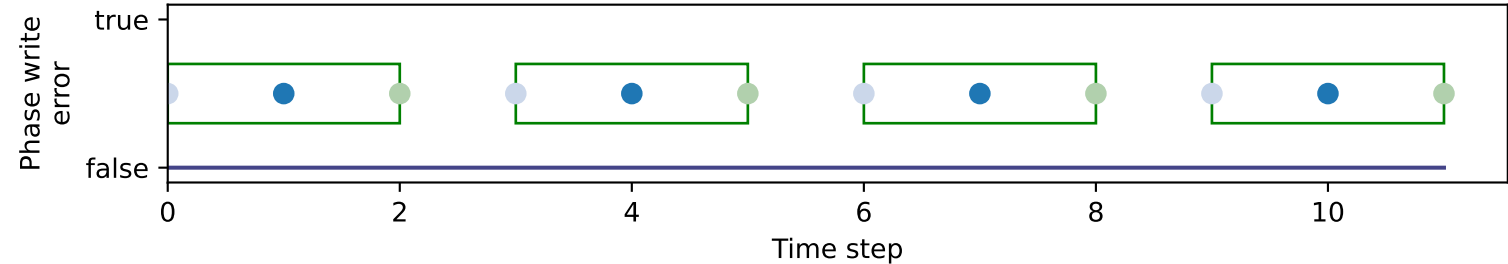


Phase write — Legitimate



start write_isLegit end phase duration