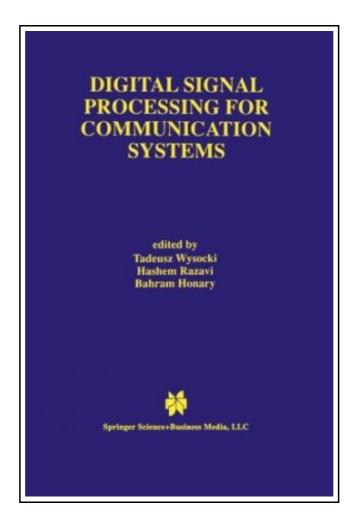
Digital Signal Processing for Communication Systems



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Reviews

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DIGITAL SIGNAL PROCESSING FOR COMMUNICATION SYSTEMS



Book Condition: New. Publisher/Verlag: Springer, Berlin | Digital Signal Processing for Communication Systems examines the plans for the future and the progress that has already been made, in the field of DSP and its applications to communication systems. The book pursues the progression from communication and information theory through to the implementation, evaluation and performance enhancing of practical communication systems using DSP technology. Digital Signal Processing for Communication Systems looks at various types of coding and modulation techniques, describing different applications of Turbo-Codes, BCH codes and general block codes, pulse modulations, and combined modulation and coding in order to improve the overall system performance. The book examines DSP applications in measurements performed for channel characterisation, pursues the use of DSP for design of effective channel simulators, and discusses equalization and detection of various signal formats for different channels. A number of system design issues are presented where digital signal processing is involved, reporting on the successful implementation of the system components using DSP technology, and including the problems involved with implementation of some DSP algorithms. Digital Signal Processing for Communication Systems serves as an excellent resource for professionals and researchers who deal with digital signal processing for communication systems, and may serve as a text for advanced courses on the subject. | Preface. Part I: Modulation and Coding. 1. Levinson-Durbin Algorithms Used for Fast BCH Decoding; M. Schmidt, G.P. Fettweis. 2. On the AMP Algorithm, Iterative Decoding and Turbo Codes; L. Zhang. 3. A Turbo-Coded, Low-Rate HF Radio Modem; L. Zhang, et al. 4. Evaluation of the Undetected Error Probability Interleaved Block Codes; H.J. Zepernick. 5. Effective Trellis Decoding Techniques for Block Codes; L.E. Aguado Bayón, P.G. Farrell. 6. On Header Forward Error Control in ATM Systems; A. Rieke, H.J. Zepernick, 7. An Alternative Multi Stage Sigma-Delta Modulator Circuit Suitable...

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