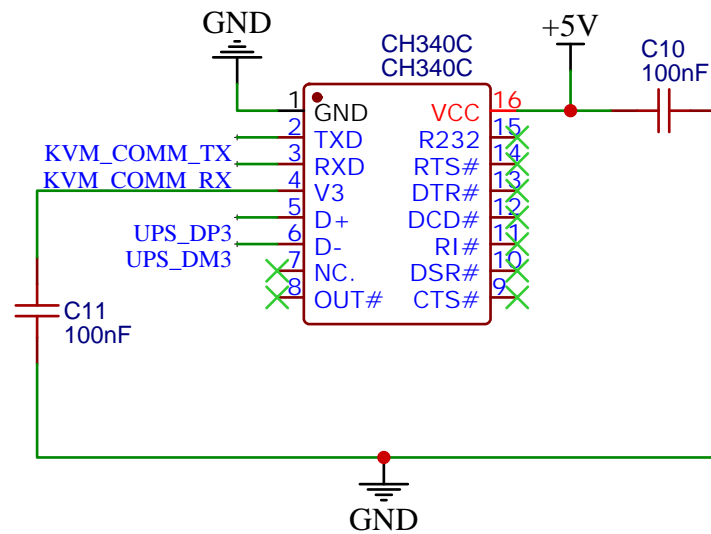



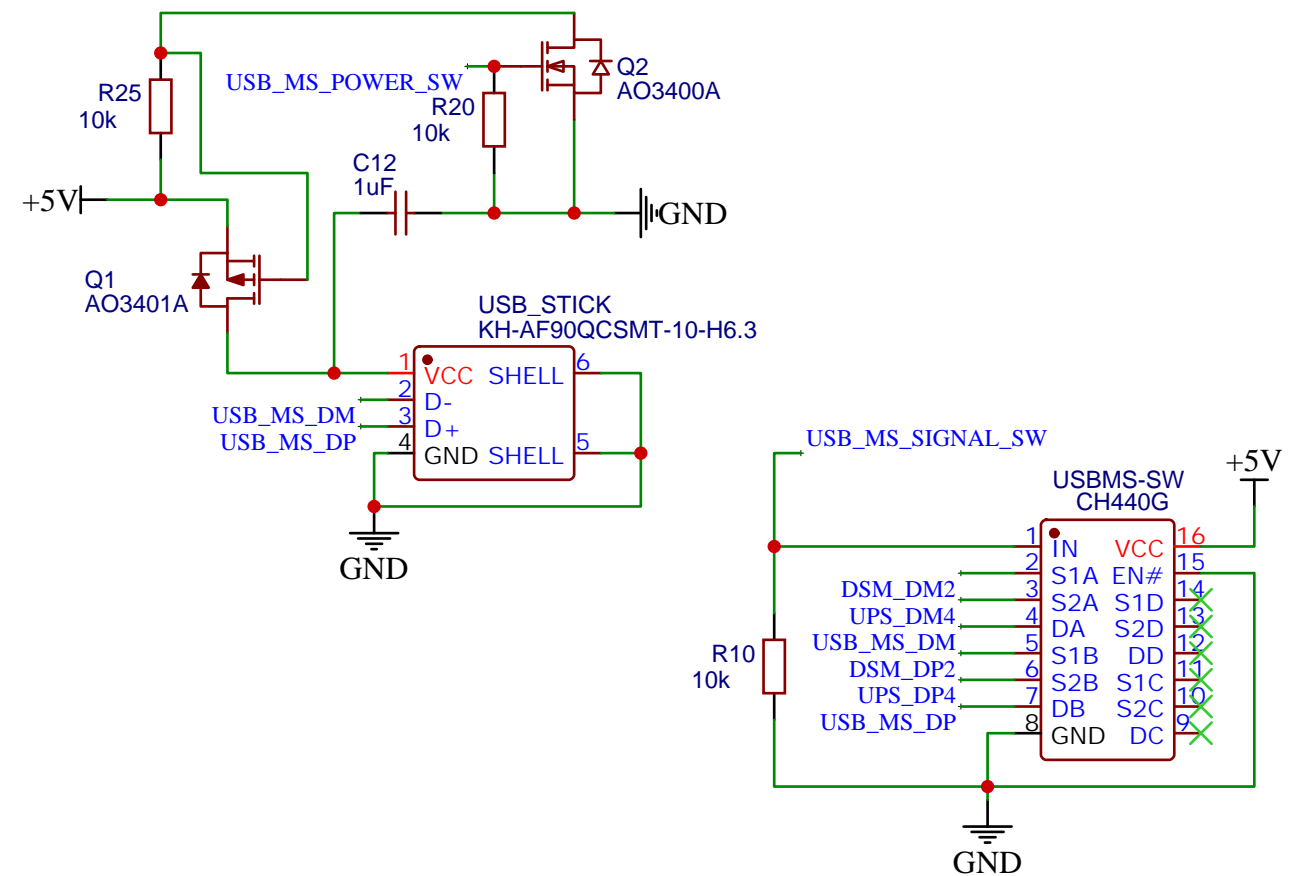
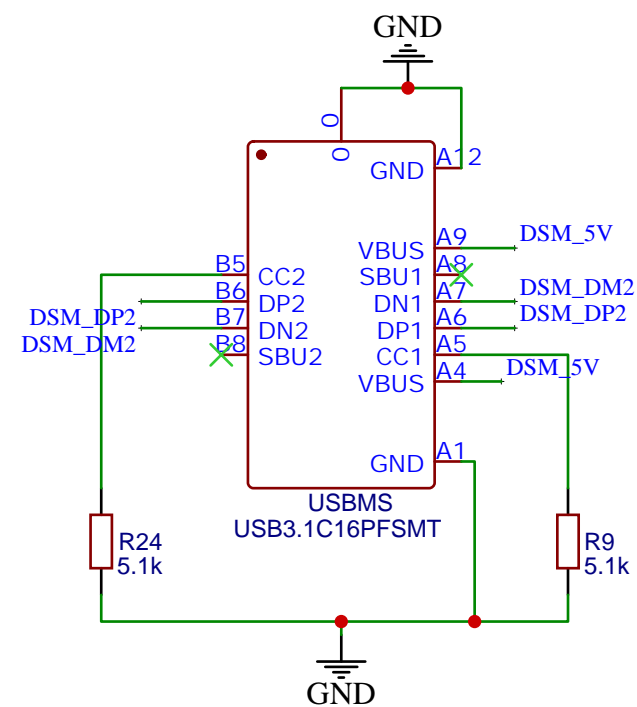
The port connecting to the KVM control SBC (e.g. Orange Pi)




TITLE: KVM Hub		REV: 1.0
	Company: RemdesKVM	Sheet: 1/1
	Date: 2025-02-18 Drawn By: tobychui	



The schematic diagram illustrates the electrical connections for the USB3.1C16PFSMT connector. The connector is represented by a central rectangular block with pins labeled on its left and right sides. The left side labels are B5, B6, B7, B8, CC2, DP2, DN2, and SBU2. The right side labels are A9, A8, A7, A6, A5, A4, and A1. The top of the connector is connected to GND. The bottom of the connector is connected to GND. The connector is labeled USB3.1C16PFSMT. The pins are connected to various components: B5 is connected to DSM_DP; B6 is connected to DSM_DM; B7 is connected to DSM_DP; B8 is connected to DSM_DM; CC2 is connected to DSM_DP; DP2 is connected to DSM_DM; DN2 is connected to DSM_DP; SBU2 is connected to DSM_DM; A9 is connected to DSM_5V; A8 is connected to DSM_DM; A7 is connected to DSM_DP; A6 is connected to DSM_DP; A5 is connected to DSM_5V; A4 is connected to DSM_5V; A1 is connected to DSM_5V. The connector is also connected to a 5.1k resistor (R1) and a 5.1k resistor (R2). The connector is connected to a 5.1k resistor (R1) and a 5.1k resistor (R2). The connector is connected to a 5.1k resistor (R1) and a 5.1k resistor (R2).



TITLE: Guest Side		REV: 1.0
	Company: imuslab	Sheet: 1/1
	Date: 2025-02-20 Drawn By: tobychui	

ATX Power Control

Notes: ATX_PWR_LED and ATX_HDD_LED default to LOW, active HIGH

This design should be compatible with PiKVM ATX Board Pinsout (Not Tested)

