Page 2

CPU, Reset, oscillators, jtag/swd/trace connectors

Page 3

Ethernet interface

Page 4

SDRAM and QSPI-FLASH

Page 5

RF interface socket and 4x LEDs

Page 6

USB interfaces

Page 7

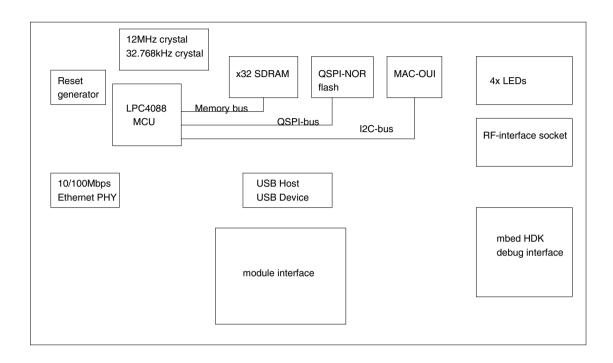
mbed HDK debug interface

Page 8

Supply voltages

Page 9

mbed and display interfaces



UL = UnLoaded = normally not mounted component.

Default jumper settings are indicated in the schematic. However, always check jumper positions on actual boards since there is no guarantee that all jumpers are in default place.

Rev A

First release

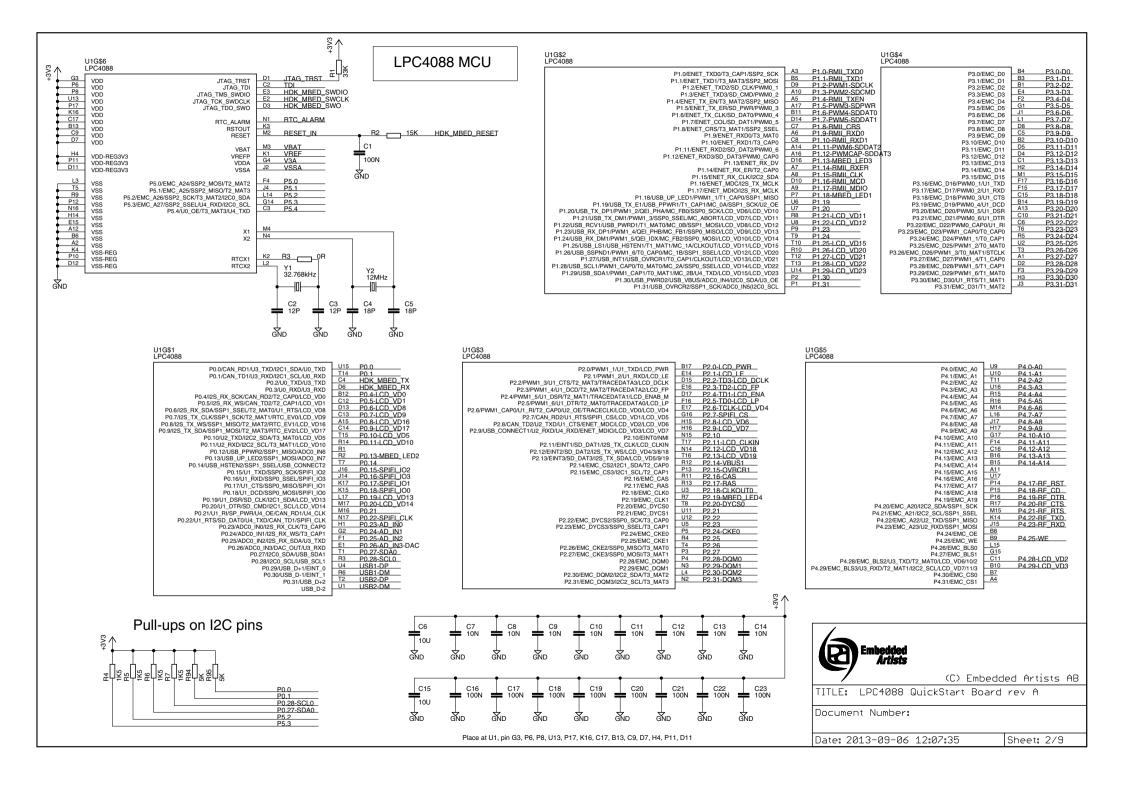


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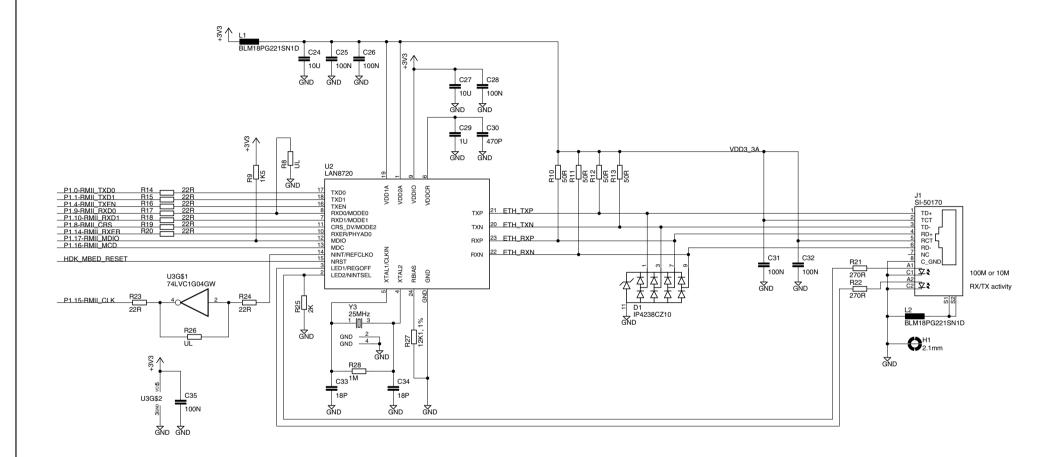
TITLE: LPC4088 QuickStart Board rev A

Document Number:

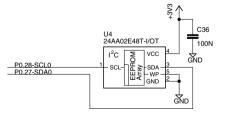
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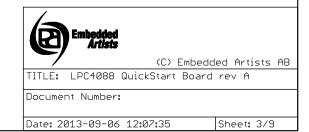
100/10M Ethernet PHY



1Kbit I2C-E2PROM with EUI-48

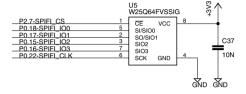


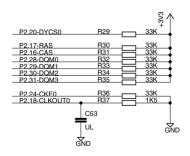
I2C address (0xA0/0xA1): 1.0.1.0.0.0.0.RW



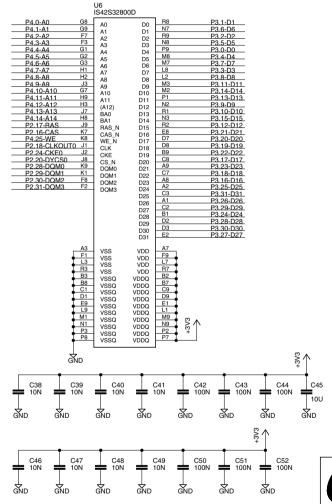
QSPI FLASH and SDRAM

64Mbit QSPI Flash (SPIFI) (Address range: 0x2800 0000 - 0x28FF FFFF)





256Mbit (32MByte) SDRAM (Address range: 0xA000 0000 - 0xAFFF FFFF)





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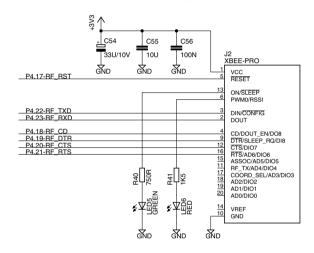
TITLE: LPC4088 QuickStart Board rev A

Document Number:

Date: 2013-09-06 12:07:35 | Sheet: 4/9

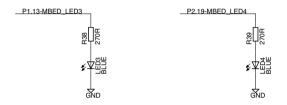
XBee Interface, Push-button and LEDs

Digi XBee(R) RF-module



mbed LED3

mbed LED4



mbed LED1 and LED2 can be found at USB interfaces

Push-button for ISP enable and User functionalty



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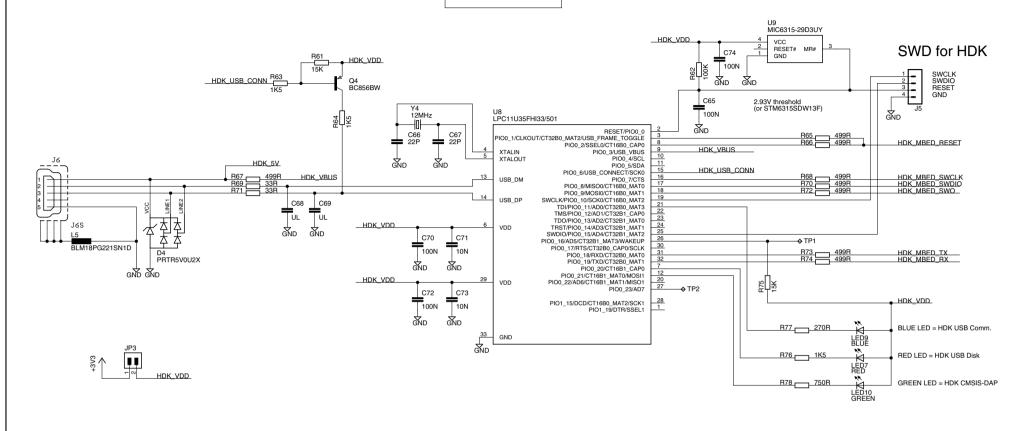
TITLE: LPC4088 QuickStart Board rev A

Document Number:

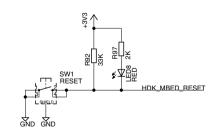
Date: 2013-09-06 12:07:35

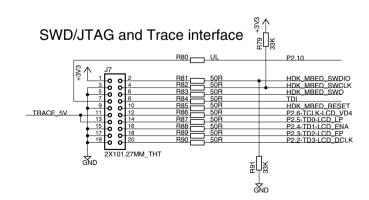
USB interfaces USB1 interface: Host P2.14-VBUS1 Default: 1-2 shorted SJ1 📥 J3 USB-A upright L3 BLM18PG221SN1D P1.22-LCD VD12 USB1-DP USB1-DM **USB Host** C60 33U/10V C59 100N C61 STMPS2171STR GND GND J3G\$3 J3G\$2 J3G\$4 J3G\$5 VIN VOUT GND GND D2 PRTR5V0U2X P1.19 P2.15-OVRCR1 EN FAULT GND Default: 1-2 shorted BLM18PG221SN1D GND SJ2 __P1.27-LCD_VD21 USB2 interface: Device (or external Host) __P1.30 __USB2-DP __USB2-DM SEL-LINK **USB** Device R54 UL GND C63 SEL-LINK GND SND SND EXT-USB-DP EXT-USB-DM D3 PRTR5V0U2X Alternative USB interface GND USB#1 UP and mbed LED1 USB#2 UP and mbed LED2 P1.18-MBED_LED1 P0.13-MBED_LED2 (C) Embedded Artists AB TITLE: LPC4088 QuickStart Board rev A Document Number: Date: 2013-09-06 12:07:35 Sheet: 6/9

mbed HDK



Reset generation







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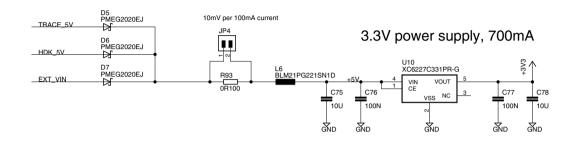
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TITLE: LPC4088 QuickStart Board rev A

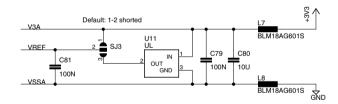
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Date: 2013-09-06 12:07:35

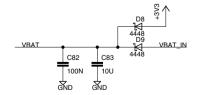
Power supplies



ADC supply and VREF voltage



VBAT supply





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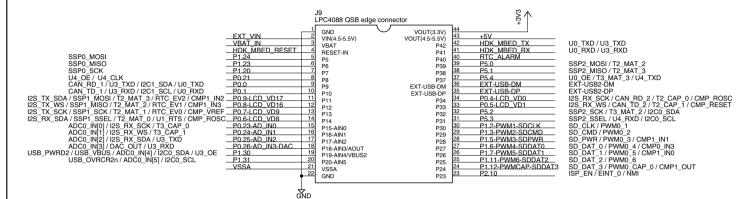
TITLE: LPC4088 QuickStart Board rev A

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Expansion Connectors

mbed Interface



mbed LPC1768	LPC4088 QuickStart Board
GND	GND
VIN (4-11V)	VIN (4.5-5.5V) Note reduced voltage range!
VBAT	VBAT
RESET	RESET
SPI1-MOSI	SSP0_MOSI
SPI-MISO SPI1-MISO	SSP0_MISO
SPI1-SCK	SSP0_SCK
GPIO	U4_OE / U4_CLK
UART1-TX/I2C1-SDA	CAN_RD_1 / U3_TXD / I2C1_SDA / U0_TXD
UART1-RX/I2C1-SCL	CAN_TD_1 / U3_RXD / I2C1_SCL / U0_RXD
SPI2-MOSI	I2S_TX_SDA / SSP1_MOSI / T2_MAT_3 / RTC_EV2 / CMP1_IN2
SPI2-MISO	I2S_TX_WS / SSP1_MISO / T2_MAT_2 / RTC_EV1 / CMP1_IN3
SPI2-SCK/UART2-TX	I2S_TX_SCK / SSP1_SCK / T2_MAT_1 / RTC_EV0 / CMP_VREF
UART2-RX	I2S_RX_SDA/SSP1_SSEL/T2_MAT_0/U1_RTS/CMP_ROSC
AIN0	ADC0_IN[0] / I2S_RX_SCK / T3_CAP_0
AIN1	ADC0_IN[1] / I2S_RX_WS / T3_CAP_1
AIN2	ADC0_IN[2] / I2S_RX_SDA / U3_TXD
AIN3/AOUT	ADC0_IN[3] / DAC_OUT / U3_RXD
AIN4	USB_PWRD2 / USB_VBUS / ADC0_IN[4] / I2C0_SDA / U3_OE (Note: used by USB2 Device interface)
AIN5	USB_OVRCR2n / ADC0_IN[5] / I2C0_SCL
	VSSA
	GND

mbed LPC1768	LPC4088 QuickStart Board
VOUT 3.3V	VOUT 3.3V
VOUT 5V	VOUT 5V Note: not current limited!
NC	U0_TXD / U3_TXD
NC	U0_RXD / U3_RXD
ETH-RDN	RTC ALARM
ETH-RDP	SSP2_MOSI / T2_MAT_2
ETH-TDN	SSP2_MISO / T2_MAT_3
ETH-TDP	U0_OE/T3_MAT_3/U4_TXD
USB-DM	EXT-USB2-DM
USB-DP	EXT-USB2-DP
CAN-RD	I2S_RX_SCK / CAN_RD_2 / T2_CAP_0 / CMP_ROSC
CAN-TD	I2S_RX_WS / CAN_TD_2 / T2_CAP_1 / CMP_RESET
UART3-TX/I2C2-SDA	SSP2_SCK / T3_MAT_2 / I2C0_SDA
UART3-RX/I2C2-SCL	SSP2_SSEL / U4_RXD / I2C0_SCL
PWMOUT0	SD_CLK / PWM0_1
PWMOUT1	SD_CMD / PWM0_2
PWMOUT2	SD_PWR / PWM0_3 / CMP1_IN1
PWMOUT3	SD_DAT_0 / PWM0_4 / CMP0_IN3
PWMOUT4	SD_DAT_1/PWM0_5/CMP1_IN0
PWMOUT5	SD_DAT_2/PWM0_6
	SD_DAT_3/PWM0_CAP_0/CMP1_OUT
	ISP_EN / EINT_0 / NMI

Display Interface

FH26-61S-0.3SHW(05) or 5034256191 P0.4-LCD_VD0 P0.5-LCD_VD1 .18-1 — 18-3 P4.28-LCD_VD2 - J8-4 - J8-5 P4.29-LCD VD3 - .18-6 P2.6-TCLK-LCD_VD4 ___ J8-8 ___ J8-9 __P0.10-LCD_VD5 − J8-10 - J8-11 P2.8-LCD_VD6 - J8-12 - J8-13 P2.9-LCD VD7 - J8-14 ____ J8-15 ____ J8-16 P0,6-LCD_VD8 P0.7-LCD VD9 −**−** J8-17 P0.11-LCD_VD10 J8-18 — J8-19 P1.21-LCD_VD11 - J8-20 - J8-21 P1.22-LCD_VD12 - J8-22 J8-23 P0.19-LCD VD13 - J8-24 ___ J8-25 ___ J8-26 P0.20-LCD_VD14 - J8-27 P1.25-LCD_VD15 P0.8-LCD_VD16 - .18-28 - J8-29 P0.9-LCD_VD17 - J8-30 - J8-31 P2.12-LCD_VD18 ─**-** J8-32 - J8-33 P2.13-LCD_VD19 - J8-34 ___ J8-35 ___ J8-36 P1.26-LCD VD20 - J8-37 P1.27-LCD_VD21 - .18-38 - J8-39 P1.28-LCD_VD22 -- .18-40 - J8-41 P1.29-LCD_VD23 ___ J8-42 ___ J8-43 P2.11-LCD CLKIN − J8-44 - J8-45 P2.2-TD3-LCD_DCLK **−** J8-47 P2.4-TD1-LCD ENA - J8-48 - J8-49 P2.3-TD2-LCD FP - J8-50 - J8-51 P2.5-TD0-LCD_LP P2.1-LCD_LE ___ J8-52 ___ J8-53 P0.27-SDA0 - J8-54 P0.28-SCL0 - J8-55 - J8-56 P2.23 - J8-57 P2.26 - J8-58 P2.27 P2.21 - J8-59 - J8-60 - J8-61 GND



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