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LPC4088 QuickStart Board - Hardware Information

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LPC4088 QuickStart Board Hardware Features

MCU

- LPC4088, Cortex-M4F core running at up to 120MHz

Memory

- 512 KByte on-chip FLASH
- 96 KByte on-chip SRAM
- 4 KByte on-chip E2PROM
- 8 MByte QSPI FLASH (can execute program code and/or contain a file system)
- 32 MByte SDRAM with 32-bit databus access
- On-board globally unique MAC address.

Connectors

- 2x22 pin edge pins that are very compatible with the original 2x20 pin LPC1768 mbed pinning
- 10/100Mbps Ethernet (RJ45)
- USB-A (USB Host interface)
- USB-micro B (USB Device interface)
- USB-micro B (mbed HDK debug interface)
- 20 pos SWD/Trace connector (ARM standard debug connector)
- 61 pos 0.3mm pitch FPC connector for **display** expansion
- 20 pos **XBee** compatible connector for RF module add-on

Display expansion

- Up to 24-bit pixel data (16-bit most common) via 61 pos, 0.3mm pitch FPC connector.

Other

- Proper **ESD protection** on communication interfaces.
- On-board HDK (debug interface functions)
- Supported by the on-line mbed SDK.
- Supported by a lot of professional quality software and examples by EA.
- Industrial temperature specified (**-40 to +85** degrees Celsius).
- **ISO 9001** produced.
- Production compensated for carbon dioxide emission.
- Current consumption down to about **5 mA**.

Pin Usage