

NUCLEO-64

MB1814

Table of contents

Sheet 1: Project overview (this page)

Sheet 2: mb1814_Top

Sheet 3: STM32_microcontroller_IOs

Sheet 4: STM32_microcontroller_power

Sheet 5: Arduino & Morpho extension connectors

Sheet 6: Power

Sheet 7: USB User

Sheet 8: STLINK-V3EC

Top
Top.SchDoc

Legend

General comment such as function title, configuration, ...

Text to be added to silkscreen.

Warning text.

▲ Notes to generate the board layout.

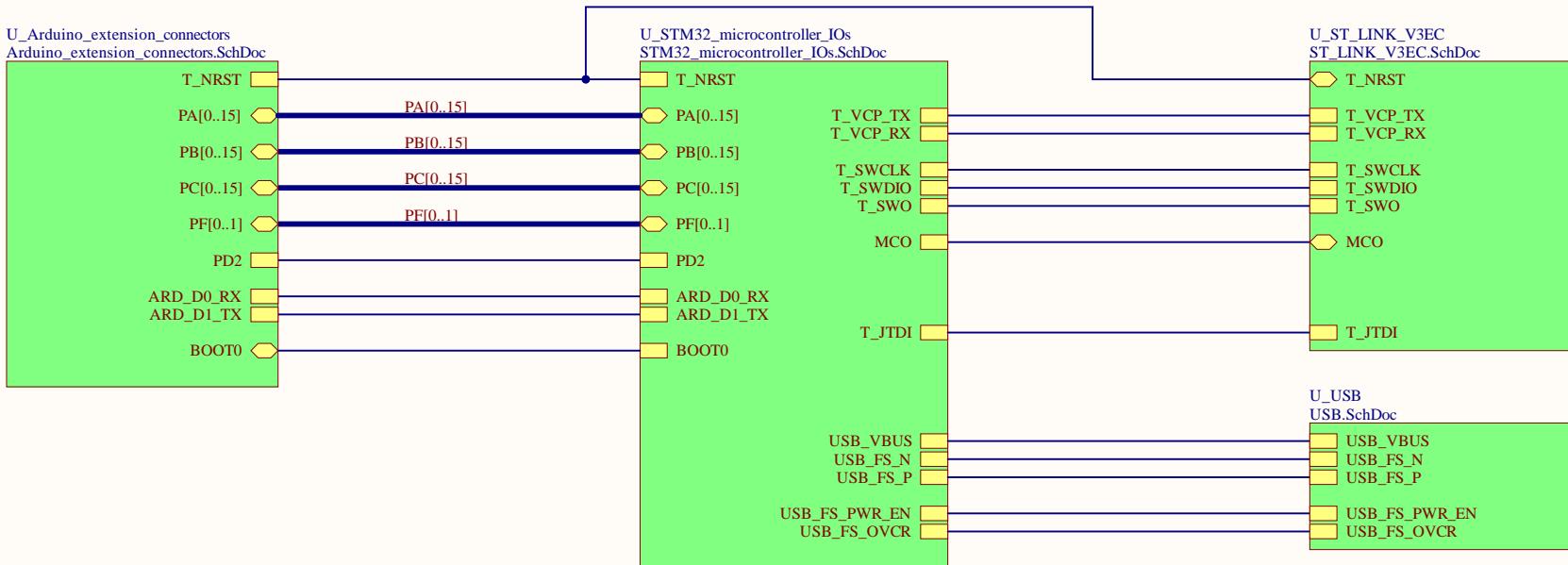
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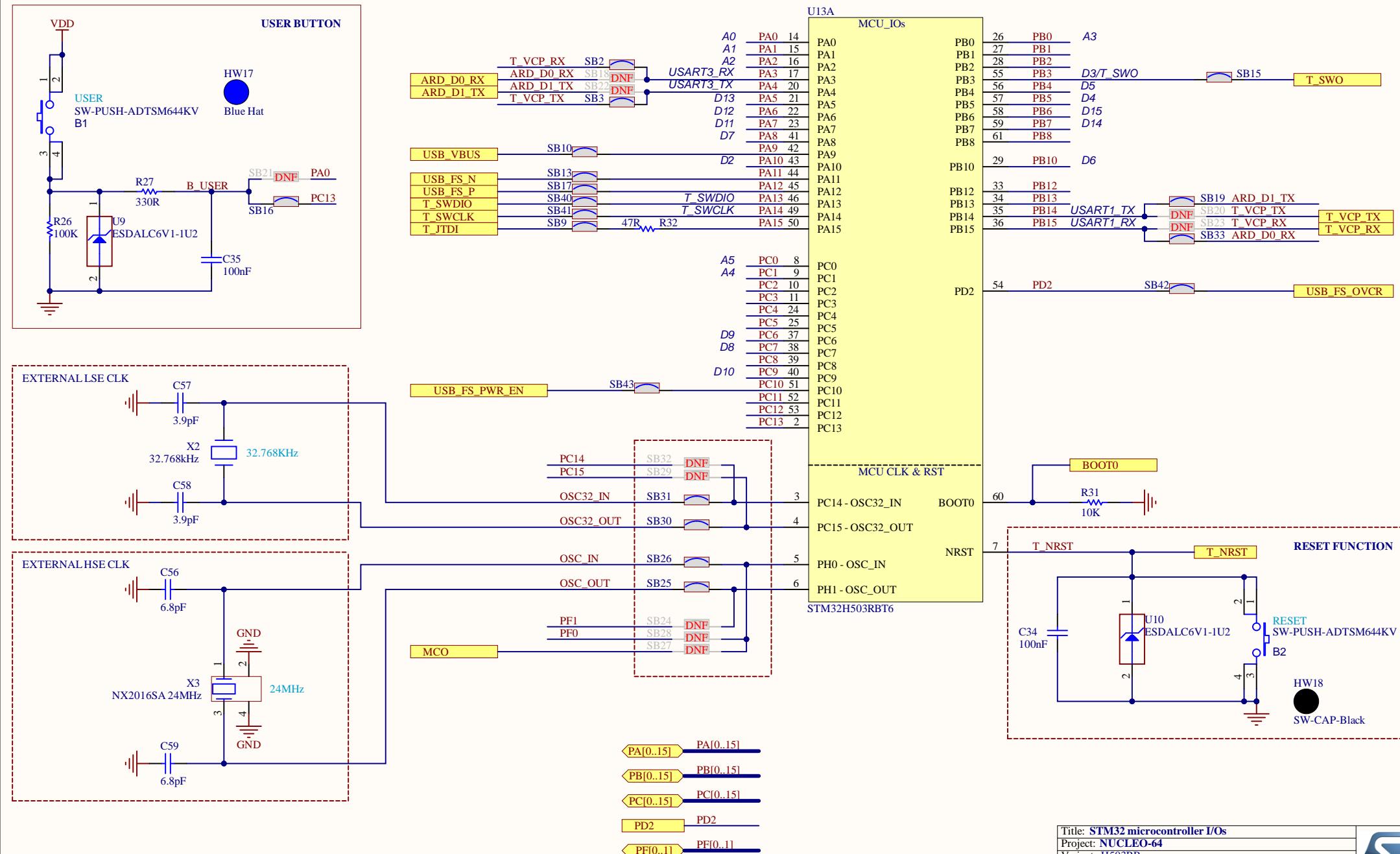
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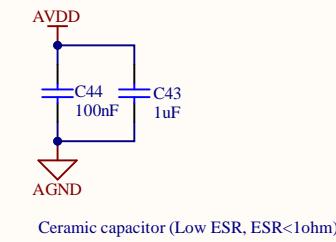
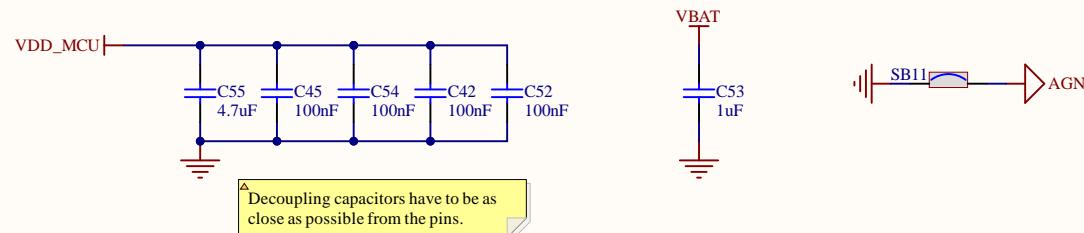
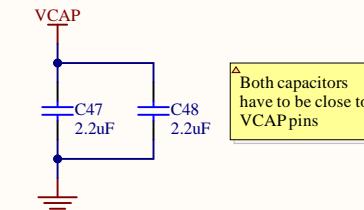
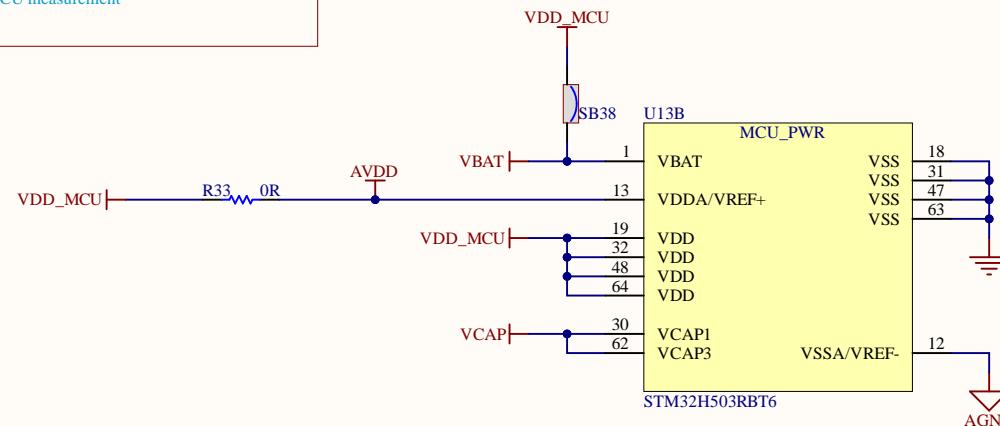
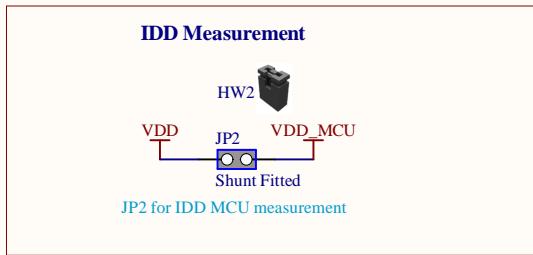
Title: Project overview	Project: NUCLEO-64	Variant: H503RB	Revision: B-02	Reference: MB1814
Size: A4	Date: 24-JAN-15	Sheet: 1 of 8	 life.augmented	

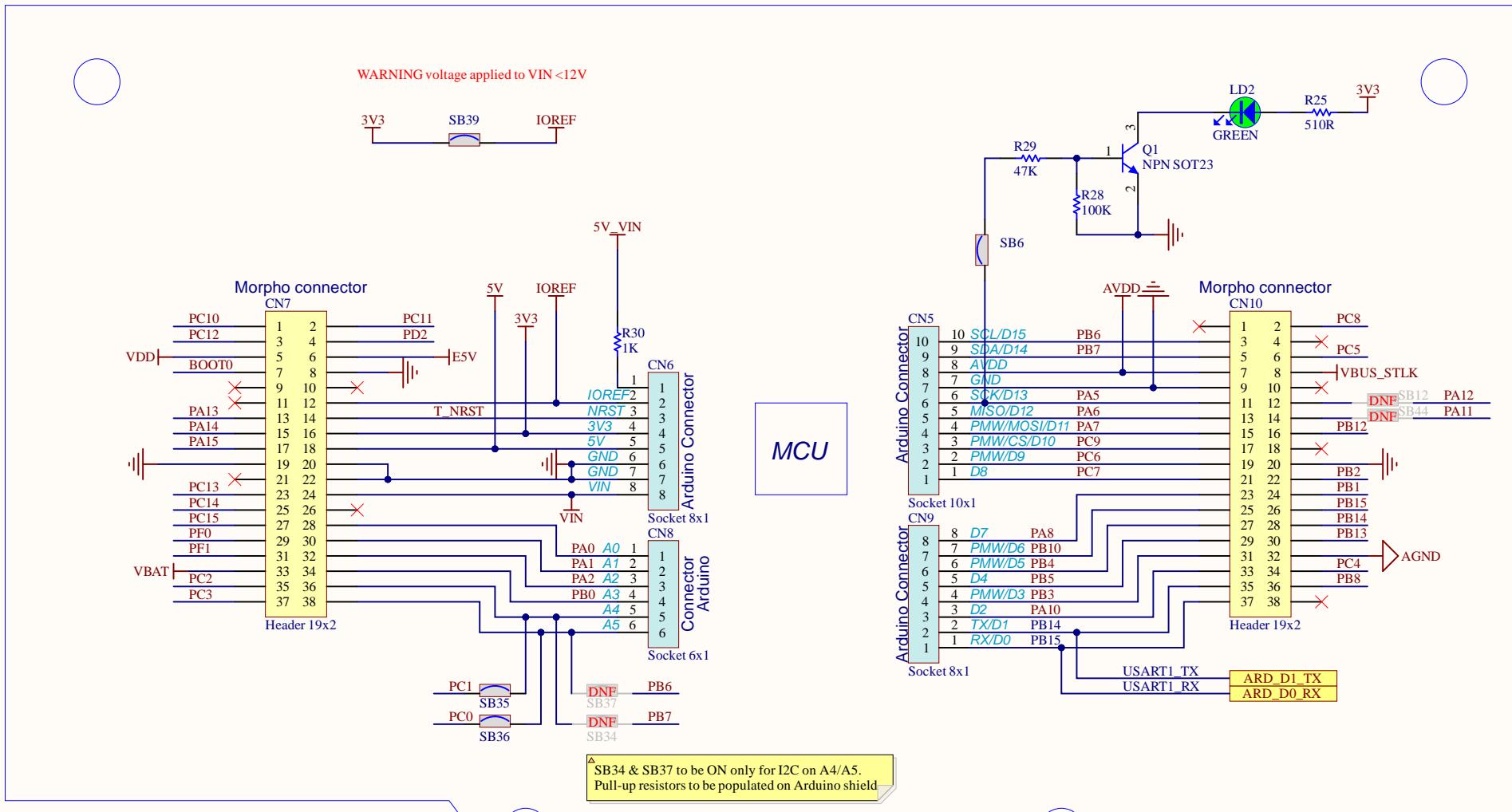


U_Power
Power.SchDoc

U_STM32_microcontroller_power
STM32_microcontroller_power.SchDoc

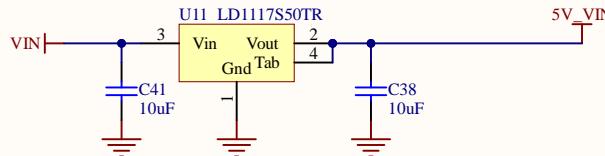




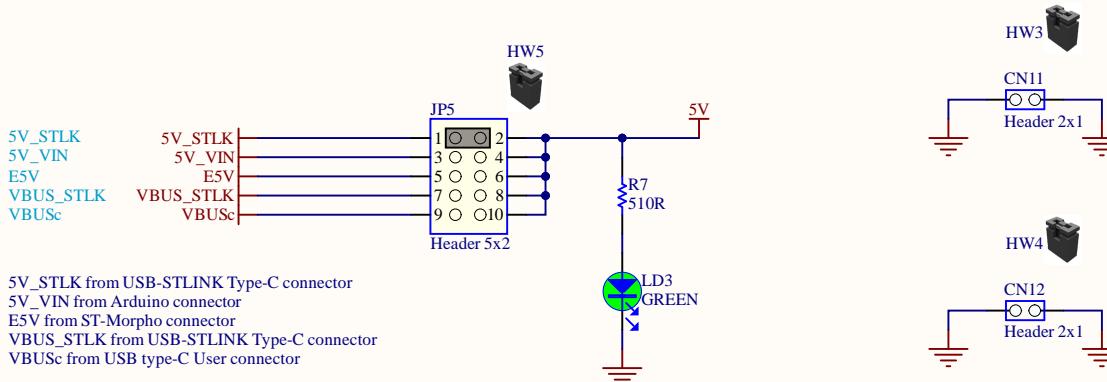


- PA[0..15] → PA[0..15]
- PB[0..15] → PB[0..15]
- PC[0..15] → PC[0..15]
- PD2 → PD2
- T_NRST → T_NRST
- PF[0..1] → PF[0..1]
- BOOT0 → BOOT0

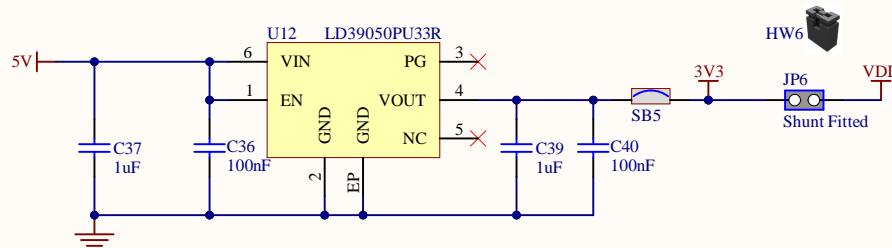
VIN / 5V POWER



5V POWER SOURCE SELECTION



VDD POWER



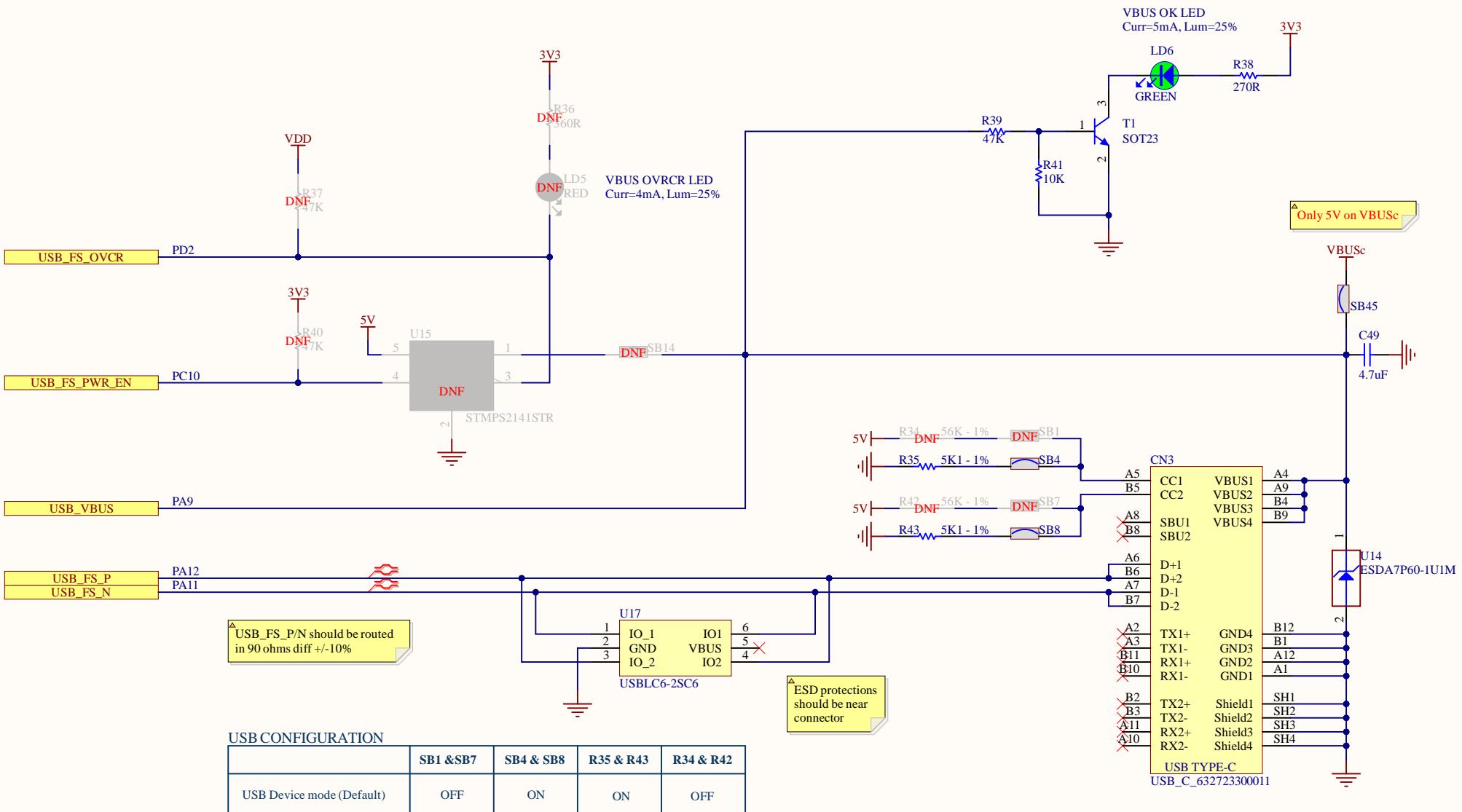
Title: Main power 5V / 3V3

Project: NUCLEO-64

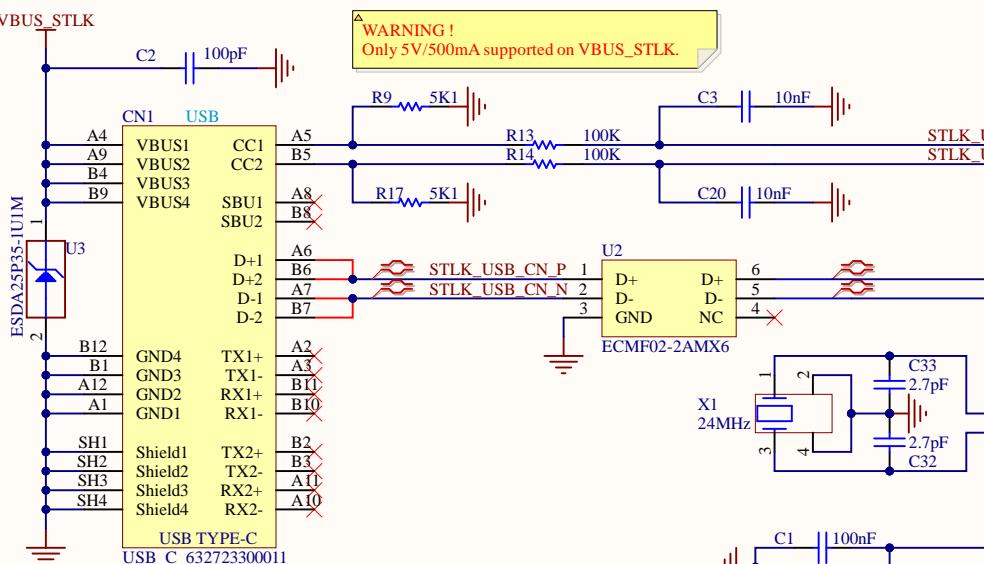
Variant: H503RB

Revision: B-02

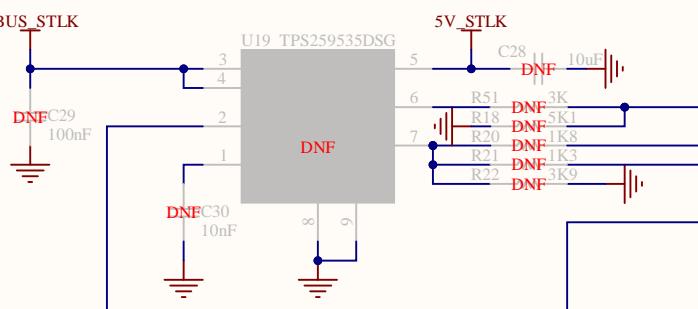
Size: A4 | Date: 24-JAN-15



ST-LINK USB CONNECTOR



5V_STLK OVERVOLTAGE AND OVERCURRENT PROTECTION

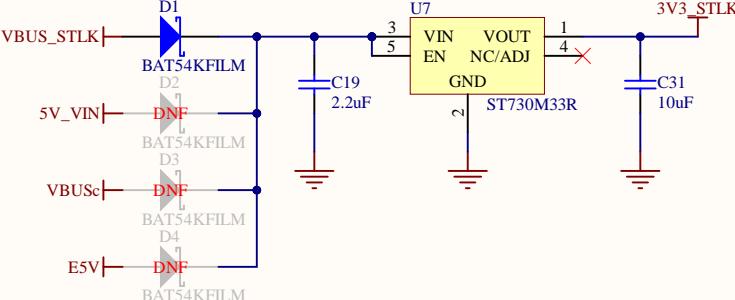


5V_STLK OVERCURRENT PROTECTION MANAGEMENT

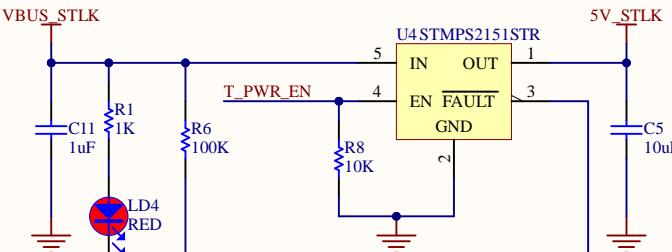
	T_PWR_SEL2/PD5	T_PWR_SEL1/PD4
PowerDefault SNK (Current limit: 550mA)	Hi-Z	Hi-Z
Power1.5.SNK (Current limit: 1.66A)	Hi-Z	0
Power3.0.SNK (Current limit: 3.2A)	0	0

HZ= IO set in high impedance

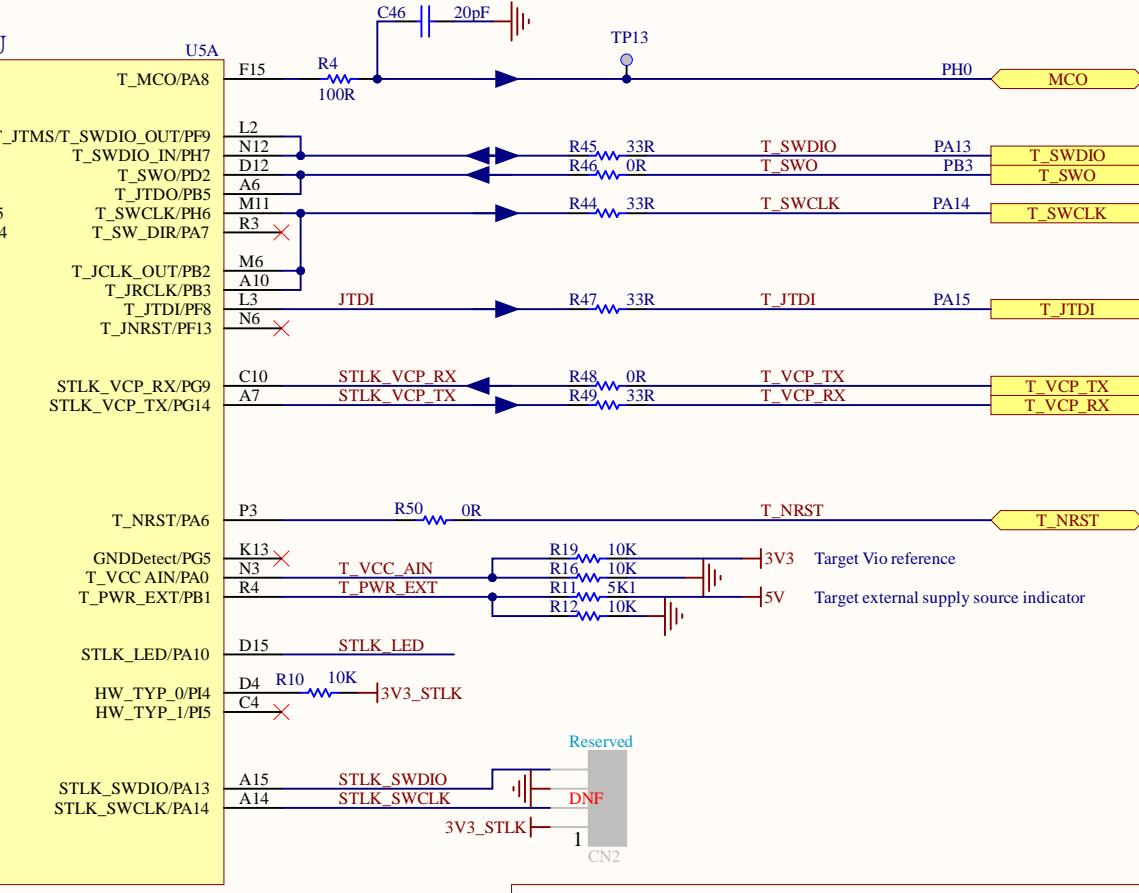
ST-LINK POWER (3V3/300mA)



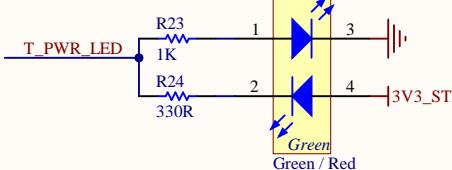
5V ST-LINK PROTECTION for legacy



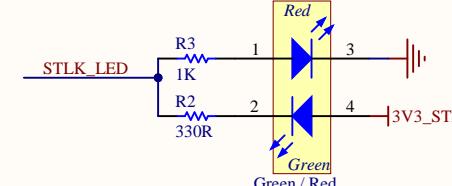
STLINK MCU



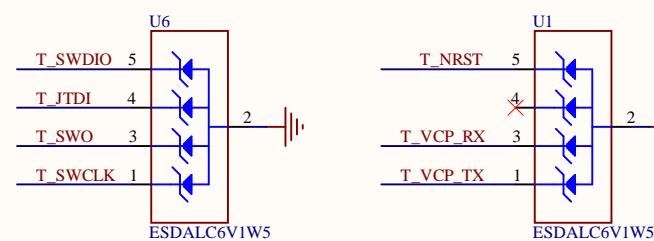
LED POWER STATUS



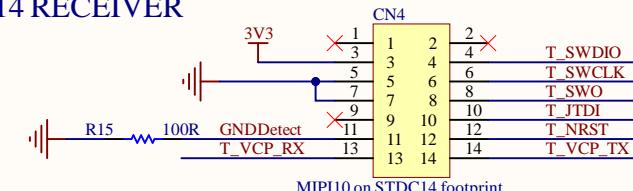
LED STLINK



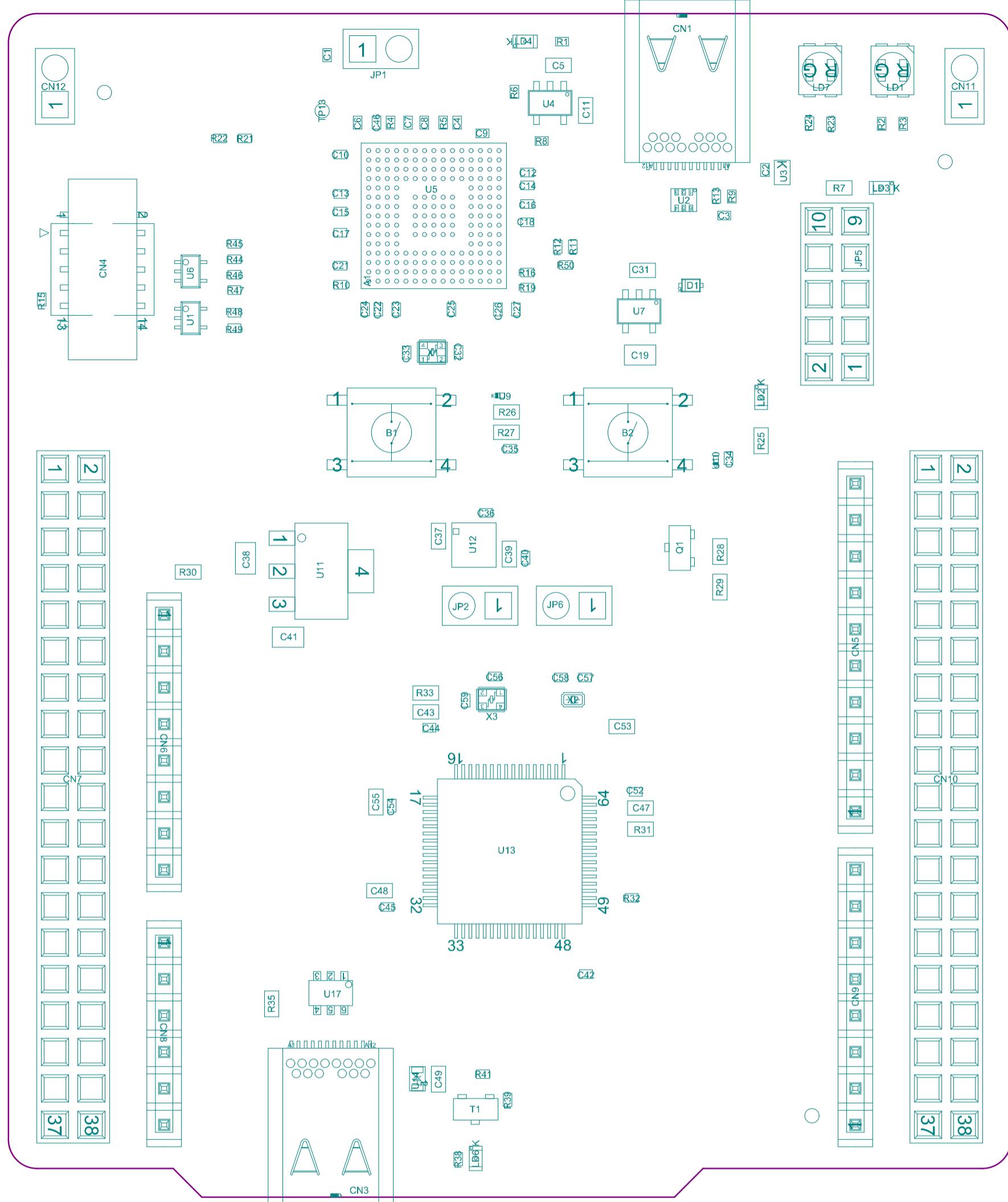
ESD PROTECTIONS



STDC14 RECEIVER



Note: JP1 must be ON when an external probe is connected to the STDC14



Project: NUCLEO-64

Layer: M14-Top Assembly

Gerber: .GM14

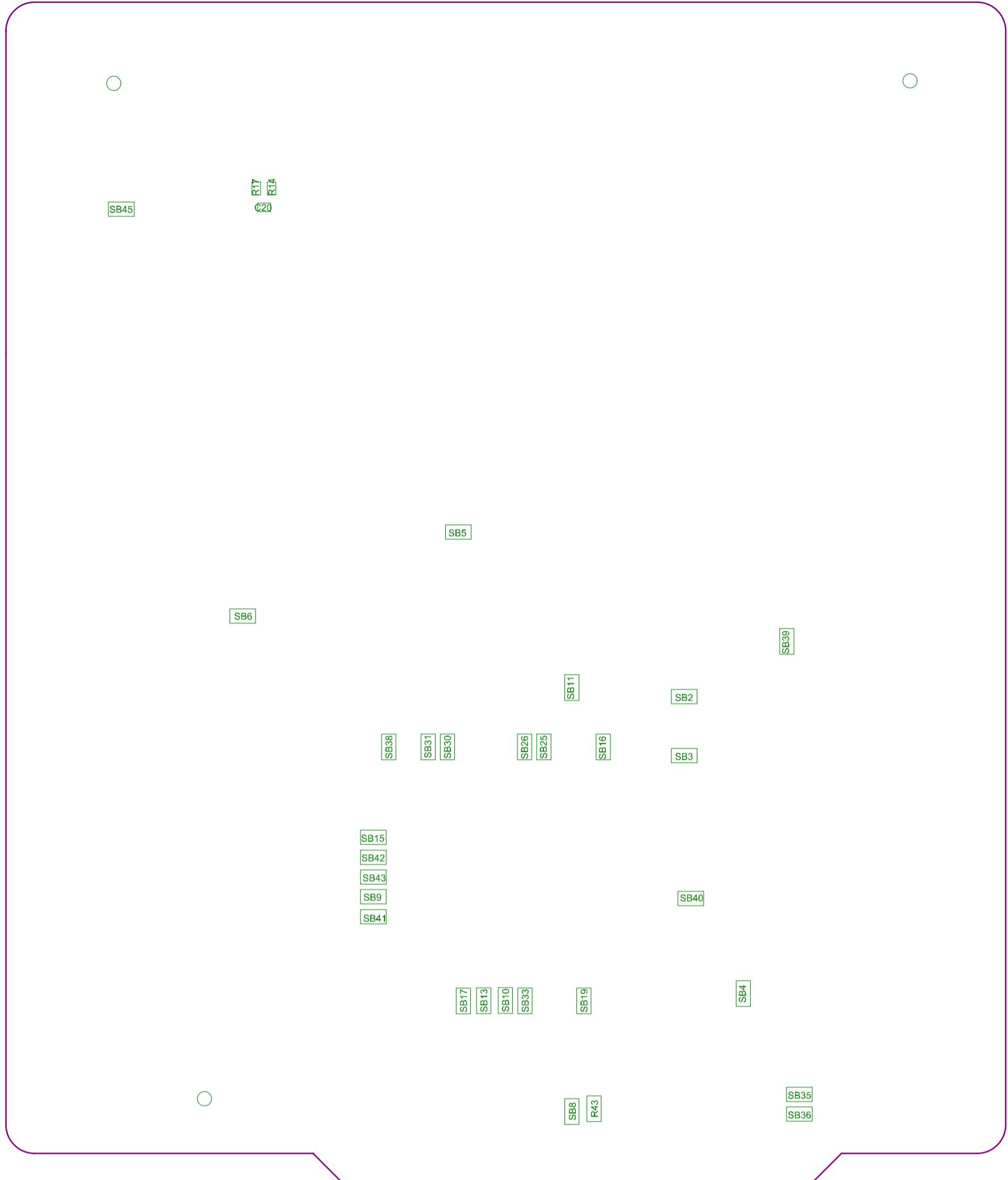
Variant: H503RB

MB1814

Date: 24-JAN-15

Rev: B





Project: NUCLEO-64

Layer: M15-Bottom Assembly

Gerber: .GM15

Variant: H503RB

MB1814

Date: 24-JAN-15

Rev: B



