

*"A Simple Low-Cost Comb Generator Frequency Calibrator"
by Larry Martin, Ham Radio magazine, July 1988*

The schematic diagram illustrates the SW bus for three connectors: J3, J5, and J6. Each connector has a switch (J3 DIST SW, J5 CMOS SW, J6 COMB SW) with two pins. Pin 1 of each switch is connected to VDD through a 100 1/2W resistor (R8, R10, R12 respectively). Pin 2 of each switch is connected to a common SW bus. The SW bus is connected to VDD through a 10k resistor and to ground through a 10k resistor. The bus is labeled SW_DIST, SW_CMOS, and SW_COMB.

A circuit diagram for the J9 BASE TEMP sensor. The sensor is represented by a blue rectangular block with three pins labeled 1, 2, and 3. Pin 1 is connected to a red line labeled VDD. Pin 2 is connected to a red line labeled V_TBASE. Pin 3 is connected to a red line that goes to a ground symbol (three horizontal lines of decreasing width).

The diagram shows the pin connections for a PIC16F887PT microcontroller. The pins are organized into two columns: pins 1-10 on the left and pins 11-28 on the right. The connections are as follows:

- Pin 1:** RB_BITE (Input)
- Pin 2:** EN_V24SW (Input)
- Pin 3:** V24_GOOD (Input)
- Pin 4:** RESE (Input)
- Pin 5:** RB_BITE (Input)
- Pin 6:** EN_V24SW (Input)
- Pin 7:** V24_GOOD (Input)
- Pin 8:** RESE (Input)
- Pin 9:** RB_BITE (Input)
- Pin 10:** EN_V24SW (Input)
- Pin 11:** V24_GOOD (Input)
- Pin 12:** RESE (Input)
- Pin 13:** RB_BITE (Input)
- Pin 14:** EN_V24SW (Input)
- Pin 15:** V24_GOOD (Input)
- Pin 16:** RESE (Input)
- Pin 17:** RB_BITE (Input)
- Pin 18:** EN_V24SW (Input)
- Pin 19:** V24_GOOD (Input)
- Pin 20:** RESE (Input)
- Pin 21:** RB_BITE (Input)
- Pin 22:** EN_V24SW (Input)
- Pin 23:** V24_GOOD (Input)
- Pin 24:** RESE (Input)
- Pin 25:** RB_BITE (Input)
- Pin 26:** EN_V24SW (Input)
- Pin 27:** V24_GOOD (Input)
- Pin 28:** RESE (Input)

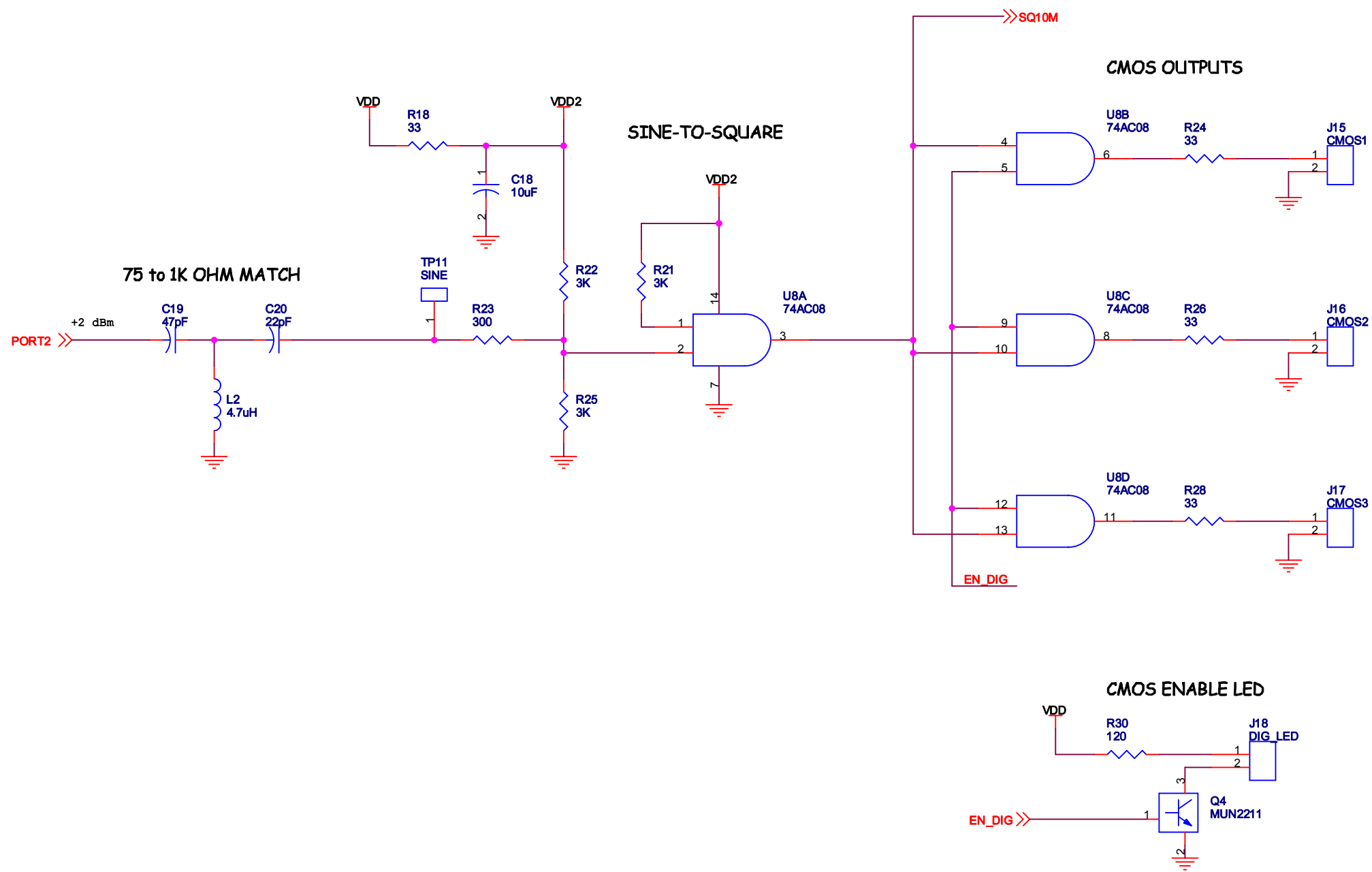
Peripheral connections include:

- T1CKI:** Pin 32
- CCP1/IN:** Pin 36
- UART TX:** Pin 44
- UART RX:** Pin 1
- I/O:** Pins 2, 3, 4, 5
- EN_DIG:** Pin 25
- EN_DIV:** Pin 26

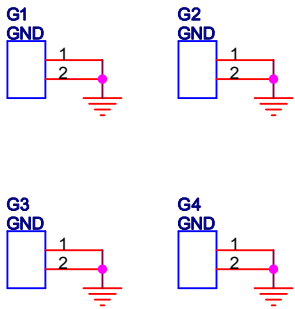
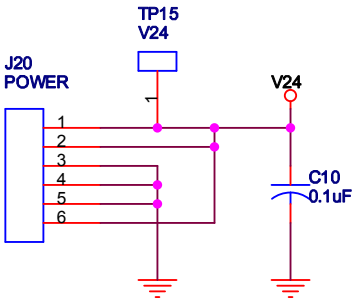
The schematic shows the J8 ICSP header with six pins. Pin 1 is connected to MCLR, pin 2 to VDD, pin 3 to GND, pin 4 to PGD, pin 5 to PGC, and pin 6 is left unconnected. To the right, two decoupling capacitors, C8 and C9, are shown. Both are 0.1µF capacitors connected between VDD and GND.

The schematic diagram shows the input circuit for the 2811 module. A 120 ohm resistor (R9) is connected to the 2811 input. The input is also connected to VDD and ground. The input is labeled J4 PIXEL.

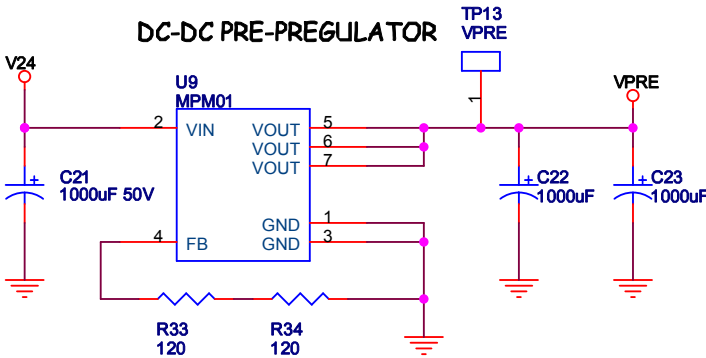
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| Size B | Document Number TJL2015-001 | | Rev 0 |
| Date: | Saturday, February 28, 2015 | Sheet | 2 of 5 |



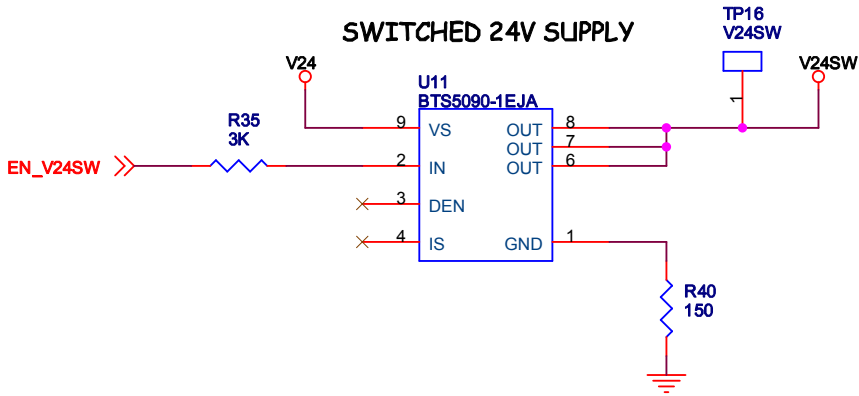
24V DC POWER INPUT



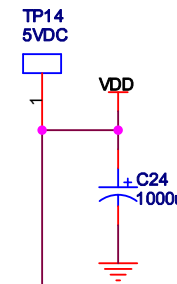
DC-DC PRE-PREGULATOR



SWITCHED 24V SUPPLY



5V SUPPLY



SWITCHED 9V SUPPLY

