



Design Variables

	Name	Value
1	VDD	598m
2	VEXT	598m

Analyses

	Type	Enable	Arguments
1	tran	<input checked="" type="checkbox"/>	0 90.1n moderate
2	dc	<input checked="" type="checkbox"/>	t

Outputs

	Name/Signal/Expr	Value	Plot	Save	Save Options
1	X0		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	allv
2	X1		<input checked="" type="checkbox"/>	<input type="checkbox"/>	allv
3	X2		<input checked="" type="checkbox"/>	<input type="checkbox"/>	allv
4	X3		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	allv
5	X4		<input checked="" type="checkbox"/>	<input type="checkbox"/>	allv
6	X5		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	allv
7	OUT		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	allv
8	V0/MINUS		<input type="checkbox"/>	<input checked="" type="checkbox"/>	yes
9	V0/PLUS		<input type="checkbox"/>	<input checked="" type="checkbox"/>	yes
10	tp1h_X0_OUT	355.067p	<input type="checkbox"/>	<input checked="" type="checkbox"/>	
11	tphi_X0_OUT	383.481p	<input type="checkbox"/>	<input checked="" type="checkbox"/>	
12	tp1h_X1_OUT	773.285p	<input checked="" type="checkbox"/>	<input type="checkbox"/>	
13	tphi_X1_OUT	605.252p	<input checked="" type="checkbox"/>	<input type="checkbox"/>	
14	tp1h_X2_OUT	795.759p	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
15	tphi_X2_OUT	643.016p	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
16	tp1h_X3_OUT	799.802p	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
17	tphi_X3_OUT	676.378p	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
18	tp1h_X4_OUT	519.773p	<input checked="" type="checkbox"/>	<input type="checkbox"/>	
19	tphi_X4_OUT	747.178p	<input checked="" type="checkbox"/>	<input type="checkbox"/>	
20	tp1h_X5_OUT	579.178p	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
21	tphi_X5_OUT	767.054p	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
22	Delay_X5_OUT	673.116p	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
23	Delay_X4_OUT	633.475p	<input checked="" type="checkbox"/>	<input type="checkbox"/>	
24	Delay_X3_OUT	738.09p	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
25	Delay_X2_OUT	719.387p	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
26	Delay_X1_OUT	689.268p	<input checked="" type="checkbox"/>	<input type="checkbox"/>	
27	Delay_X0_OUT	369.274p	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
28	Energy	218.302f	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	

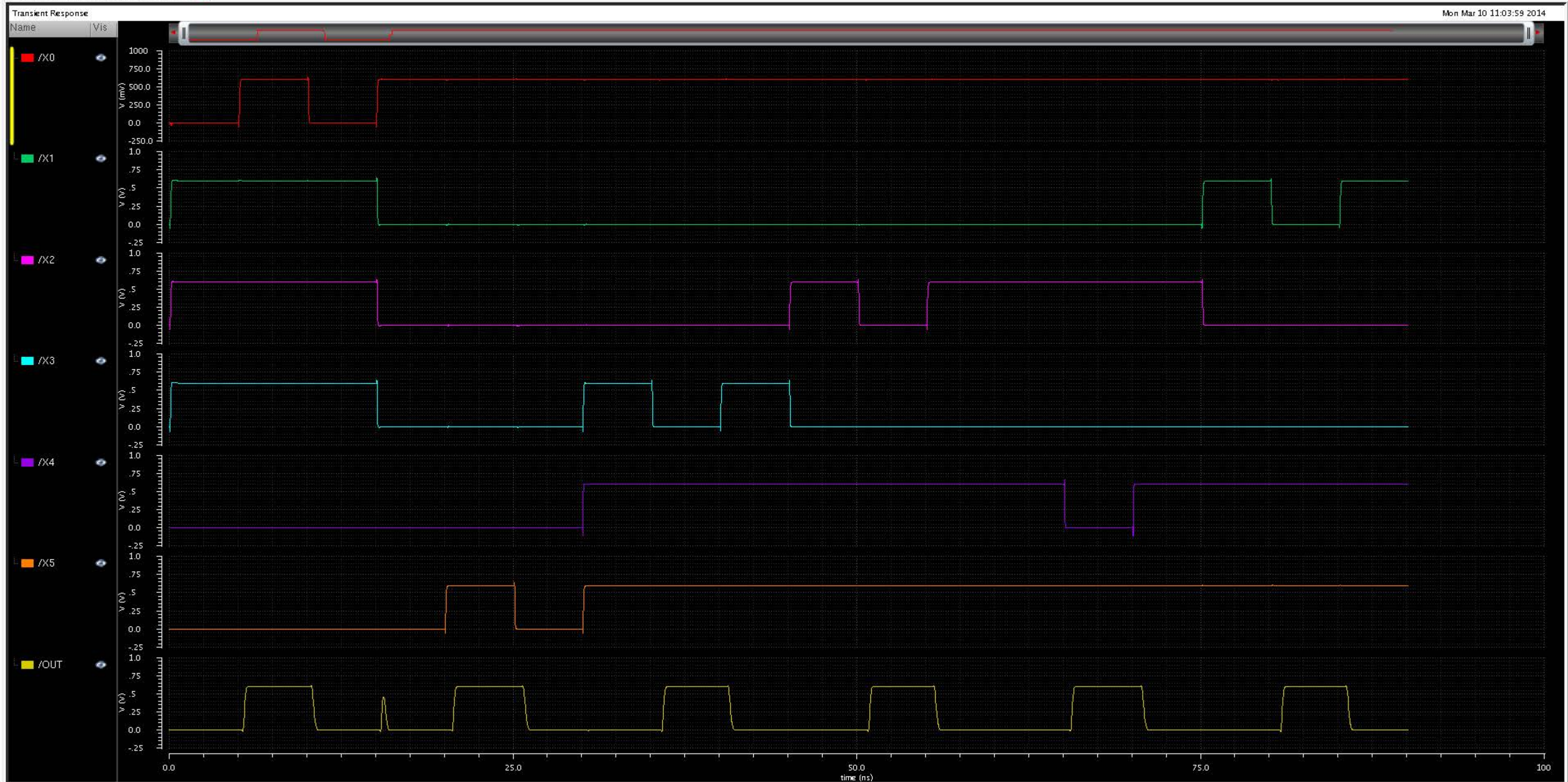
> Results in .../ee/dwyer/simulation/tb_Abs_Value_Detector/spectre/config

Plot after simulation: Auto

Plotting mode: Replace

Schematic min VDD Waveform

tb_project tb_Abs_Value_Detector ...





Design Variables

	Name	Value
1	VDD	657.65m
2	VEXT	657.65m

Analyses

	Type	Enable	Arguments
1	tran	<input checked="" type="checkbox"/>	0 90.1n moderate
2	dc	<input checked="" type="checkbox"/>	t

Outputs

	Name/Signal/Expr	Value	Plot	Save	Save Options
1	X0		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	allv
2	X1		<input checked="" type="checkbox"/>	<input type="checkbox"/>	allv
3	X2		<input checked="" type="checkbox"/>	<input type="checkbox"/>	allv
4	X3		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	allv
5	X4		<input checked="" type="checkbox"/>	<input type="checkbox"/>	allv
6	X5		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	allv
7	OUT		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	allv
8	V0/MINUS		<input type="checkbox"/>	<input checked="" type="checkbox"/>	yes
9	V0/PLUS		<input type="checkbox"/>	<input checked="" type="checkbox"/>	yes
10	tplh_X0_OUT	342.285p	<input type="checkbox"/>	<input checked="" type="checkbox"/>	
11	tphi_X0_OUT	351.114p	<input type="checkbox"/>	<input checked="" type="checkbox"/>	
12	tplh_X1_OUT	755.201p	<input checked="" type="checkbox"/>	<input type="checkbox"/>	
13	tphi_X1_OUT	585.76p	<input checked="" type="checkbox"/>	<input type="checkbox"/>	
14	tplh_X2_OUT	786.623p	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
15	tphi_X2_OUT	630.135p	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
16	tplh_X3_OUT	799.942p	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
17	tphi_X3_OUT	664.489p	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
18	tplh_X4_OUT	513.737p	<input checked="" type="checkbox"/>	<input type="checkbox"/>	
19	tphi_X4_OUT	679.533p	<input checked="" type="checkbox"/>	<input type="checkbox"/>	
20	tplh_X5_OUT	588.074p	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
21	tphi_X5_OUT	721.193p	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
22	Delay_X5_OUT	654.634p	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
23	Delay_X4_OUT	596.635p	<input checked="" type="checkbox"/>	<input type="checkbox"/>	
24	Delay_X3_OUT	732.216p	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
25	Delay_X2_OUT	708.379p	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
26	Delay_X1_OUT	670.48p	<input checked="" type="checkbox"/>	<input type="checkbox"/>	
27	Delay_X0_OUT	346.7p	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	
28	Energy	305.683f	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	

> Results in .../ee/dwyer/simulation/tb_Abs_Value_Detector/spectre/config

Plot after simulation: Auto Plotting mode: Replace

mouse L:

M:

R:

/X0

/X1

/X2

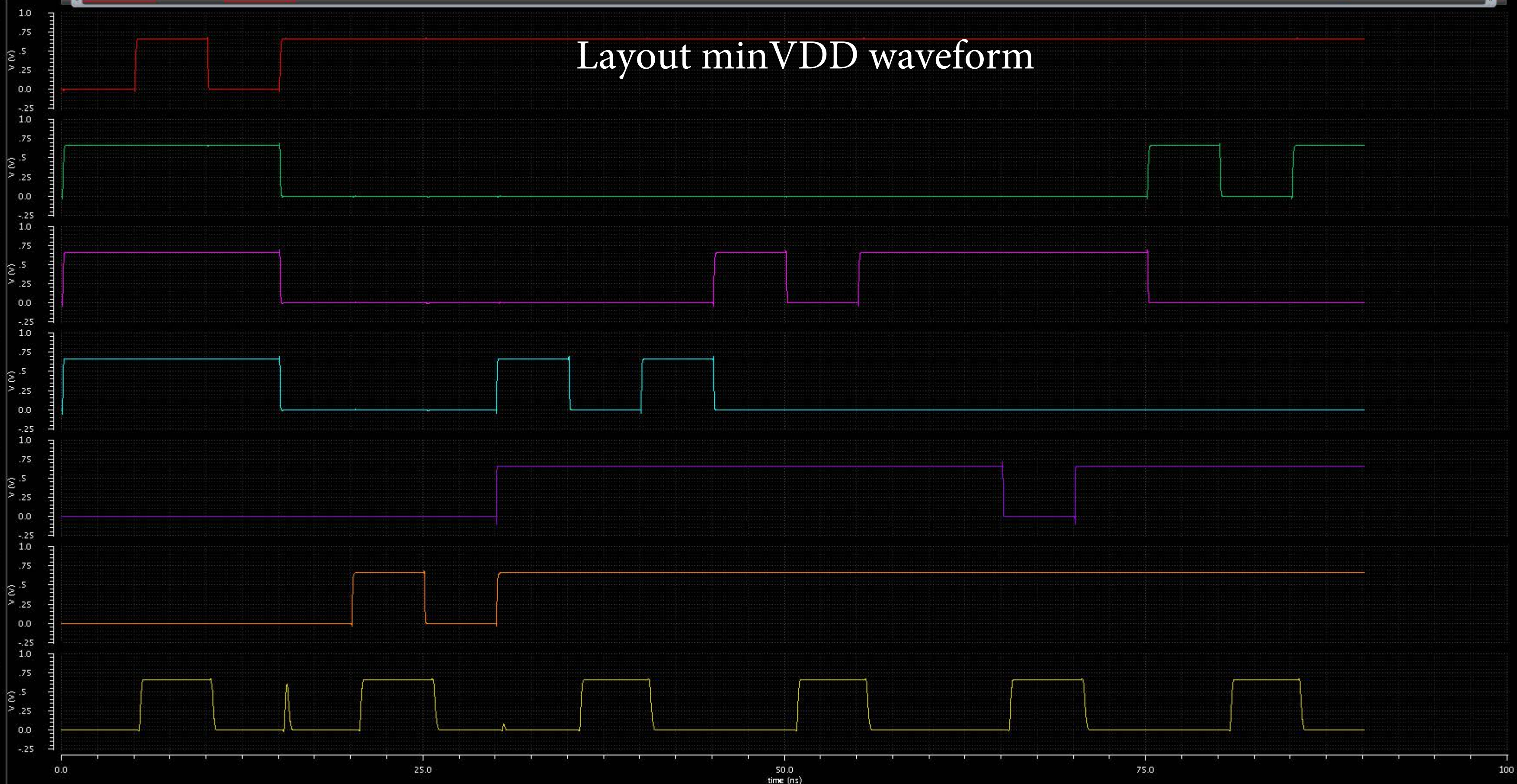
/X3

/X4

/X5

/OUT

Layout minVDD waveform



Launch File Edit View Create Check Options Migrate Window Help

Workspace: Constraints

Navigator

- Project11NEW
 - I38 (finalstage)
 - I99 (M...2OLDY)
 - I100 (...2OLDY)
 - I104 (...2OLDY)
 - I76 (Compcarry)
 - I77 (CompS1NEW)
 - I78 (CompS2NEW)
 - I79 (CompS2NEW)
 - I80 (CompS2NEW)
 - I90 (carrystage3)
 - I107 (XOR2X1)
 - I108 (CompS2NEW)
 - GND
 - M2
 - M3
 - M4
 - M5
 - net026
 - net027
 - net036
 - net040
 - net042
 - net043

Property Editor

Constraint Manager

Editing: constraint

Show: All

Type (0)

Project11NEW...

mouse L: schSingleSelectPt()

M: lxLaunchLayoutXL()

R: schHiMousePopUp()

5(9) >

File Tools Options

Host is
cmd is /w/apps3/Ca
Starting the Assura
Loading layers.cxt
Checking out licens
WARNING No DRC ex

M51module222
M51module222OLDY
MOS_IV
NAND2X1
NAND2X8
NOR2X2
NOR3X2
NewCompS2
ORgate
Project11
Project11NEW
Project11NEW1

Messages

Log file is "Aw/ugrad.12/ee/dwyer/ee115c/libManager.log.1".

Launch File Edit View Create Verify Connectivity Options Tools Window Assura QRC Optimize Place Floorplan Route Help

Workspace: Classic

Layers

AV NV AS NS

Nwell drawing

All Valid Layers

Used Layers Only

Search

Layer	Purpose	V	S
Nwell drawing			
Oxide drawing			
Oxi... drawing			
Poly drawing			
Pimp drawing			
Nhvt drawing			
Nimp drawing			
Phvt drawing			
Nzvt drawing			
SiProt drawing			
Cont drawing			
Met... drawing			
Via1 drawing			
Met... drawing			
Via2 drawing			
Met... drawing			
Via3 drawing			
Met... drawing			
Via4 drawing			
Met... drawing			
Via5 drawing			
Met... drawing			
Via6 drawing			
Met... drawing			
Via7 drawing			
Met... drawing			

Objects

Object	V	S
Shapes		
Circle/Elli...		
Donut		
Label		
Path		
PathSeg		
Polygon		
Rectangle		
Other Sh...		
Instances		
Fluid Guardri...		
Mosaic		
Pins		
Vias		
Fluid Shapes		
Fig Groups		
Boundaries		

Objects Guides

mouse L: mouseSingleSelectPt() _leiLMBPress()

M: vuiDRCRun()

R: _lxHiMousePopUp()

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LaunchFileEditViewCreateCheckOptionsMigrateWindowHelp

Workspace: Constraints

Search

Navigator

Default

Search

Name

Project11NEW

I38 (finalstage)

I99 (M...2OLDY)

I100 (...2OLDY)

I104 (...2OLDY)

I76 (Compcarry)

I77 (CompS1NEW)

I78 (CompS2NEW)

I79 (CompS2NEW)

I80 (CompS2NEW)

I90 (carrystage3)

I107 (XOR2X1)

I108 (CompS2NEW)

GND

M2

M3

M4

M5

net026

net027

net036

net040

net042

net043

Property Editor

Constraint Manager

Run: "Project11NEW"

Run: "Project11NEW" from
Aw/ugrad.12/ee/dwyer/ee115c/

Schematic and Layout Match.
You currently have an open run (project).

Do you want to close current project and view the results of new run?

Summary of LVS Issues

Extraction Information:

0 cells have 0 mal-formed device problems
0 cells have 0 label short problems
0 cells have 0 label open problems

Comparison Information:

0 cells have 0 Net mismatches
0 cells have 0 Device mismatches
0 cells have 0 Pin mismatches
0 cells have 0 Parameter mismatches

YesNoHelp

FileToolsOptions

Assura LVS: State 1
INFO: Checking wh
INFO: Schematic 3
Assura LVS: State s
Host is
cmd is /w/apps3/Ca
Starting the Assura
STATUS: Schematic a

M51module222
M51module222OLDY
MOS_IV
NAND2X1
NAND2X8
NOR2X2
NOR3X2
NewCompS2
ORgate
Project11
Project11NEW
Project11NEW1

Messages

Log file is "Aw/ugrad.12/ee/dwyer/ee115c/libManager.log.1".

LaunchFileEditViewCreateVerifyConnectivityOptionsToolsWindowAssuraQRCOptimizePlaceFloorplanRouteHelp

Workspace: Classic

32

(F)Select:0 Sel(N):0 Sel(I):0 Sel(O):0 X:-1.7600 Y:23.4300 dX: dY: Dist: >>

Layers

Objects

Object	V	S
Shapes		
Circle/Ell...		
Donut		
Label		
Path		
PathSeg		
Polygon		
Rectangle		
Other Sh...		
Instances		
Fluid Guardi...		
Mosaic		
Pins		
Vias		
Fluid Shapes		
Fig Groups		
Boundaries		

ObjectsGuides

12:34 PM
3/10/2014