



**POLITECNICO  
DI TORINO**

Dipartimento  
di Automatica e Informatica

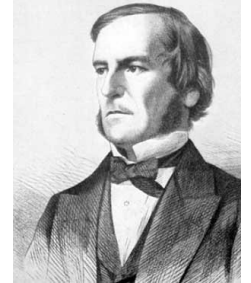
# Boolean Algebra and Logic Functions

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# Boolean Algebra



- It was introduced in 1854 by George Boole to algebraically analyze propositional calculus problems (in order to study the laws of thought)
- It was then used as a foundation for the formal logic that serves as the basis for scientific reasoning
- It is a mathematical model used to express logical conditions and for the synthesis and analysis of digital systems
- Boolean Algebra is an abstract model: there are many Boolean Algebras!

# Boolean Algebras

A Boolean algebra is an algebraic structure consisting of:

- a set of elements  $B$  that includes at least 0 and 1
- two binary operations (= operating on 2 operands)  $\{+, \cdot\}$
- a unary operation (= operating on 1 operand)  $\{'\}$

that satisfy Huntington's axioms (1904):

1.  $B$  contains at least two different elements  $a$  and  $b$  with  $a \neq b$
2. *Closedness:*  $\forall a, b \in B$ 
  - (i)  $a + b \in B$
  - (ii)  $a \cdot b \in B$
  - (iii)  $a' \in B$

3. *Commutativity*:  $\forall a, b \in B$

- (i)  $a + b = b + a$
- (ii)  $a \cdot b = b \cdot a$

4. *Identity*: there exist 2 values  $0, 1 \in B$  such that

- (i)  $a + 0 = a$
- (ii)  $a \cdot 1 = a$

5. *Distributivity*:

- (i)  $a + (b \cdot c) = (a + b) \cdot (a + c)$
- (ii)  $a \cdot (b + c) = a \cdot b + a \cdot c$

6. *Complement*:

- (i)  $a + a' = 1$
- (ii)  $a \cdot a' = 0$

# Examples of Boolean Algebras

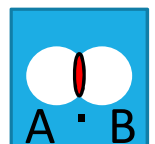
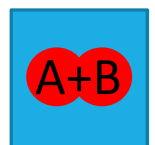
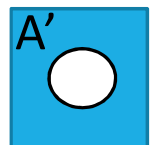
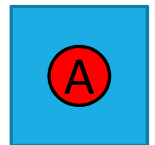
By specifying the elements of  $B$  and the operations, an **interpretation** is defined and a **model** of the algebra is obtained.

## Set algebra:

Let a universe set  $S$  be given. The powerset of  $S$  is the set of subsets of  $S$ , including  $S$  itself and the empty set  $\emptyset$ .

- $B$  is the powerset of  $S$
- $'$  is the complement operation with respect to the universe  $S$  as a whole
- $+$  is the set union operation
- $\cdot$  is the set intersection operation

$S$



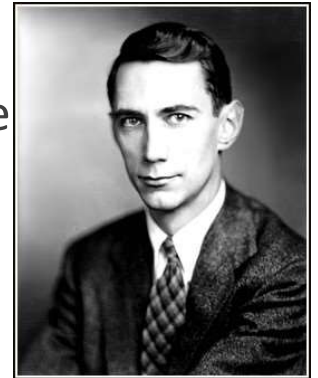
## Switching Algebra

Claude Shannon (1938) introduced switching algebra to describe the properties of electrical circuits with switches.

- $B = \{0, 1\}$
- binary operations: OR (+), AND ( $\cdot$ )
- unary operation: NOT ('or  $\bar{\phantom{x}}$  or  $\sim$  or overline $\phantom{x}$ ).

Switching algebra (hereinafter called with abuse of notation Boolean Algebra):

- is used to express the logical conditions in programming languages
- is the basis for designing and analyzing digital systems.



# Switches and Switching Algebra

Inputs:

- 1 corresponds to a closed switch
- 0 corresponds to an open switch

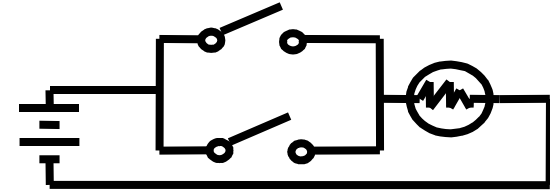
Output:

- 1 corresponds to lamp on
- 0 corresponds to lamp off

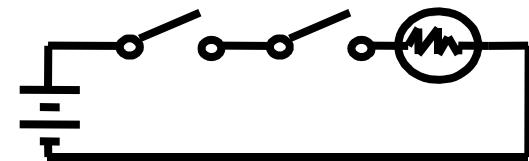
NOT uses a switch such that:

- 1 is the open switch
- 0 is the closed switch

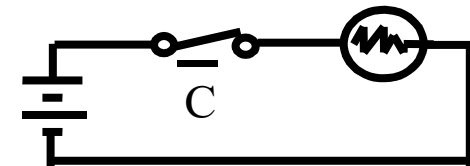
**Switches in parallel => OR**



**Switches in series => AND**



**Normally closed switch => NOT**



# Boolean Variables

- The two (binary) values in  $B$  have several names:
  - True/False
  - On/Off
  - Yes/No
  - 1/0
- We use 1 and 0
- Boolean variable: variable that takes values in  $B$
- Examples of variable identifiers:  $A, B, x, Y, y, z$



## Examples of notation

Let  $A, B, x, Y, y, z$  be binary variables:

- $Y = A \cdot B$  reads “ $Y$  equals  $A$  AND  $B$ .”
- $z = x + y$  reads “ $z$  equals  $x$  OR  $y$ .”
- $X = A'$  reads “ $X$  equals NOT  $A$ .”

Remark: the statement:

- $1 + 1 = 2$  (read “1 plus 1 equals 2”) holds when  $+$  is an arithmetic sum operator
- $1 + 1 = 1$  (read “1 or 1 equals 1”) holds when  $+$  is the OR operator in Boolean Algebra

# Basic Theorems

Idempotency:

$$a + a = a$$

$$a \cdot a = a$$

Null Element:

$$a + 1 = 1$$

$$a \cdot 0 = 0$$

Absorption:

$$a + (a \cdot b) = a$$

$$a \cdot (a + b) = a$$

$$a \cdot (a' + b) = a \cdot b$$

$$a + (a' \cdot b) = a + b$$

Involution:

$$(a')' = a$$

Associativity:

$$a + (b + c) = (a + b) + c$$

$$a \cdot (b \cdot c) = (a \cdot b) \cdot c$$

**De Morgan's Law:**

$$(a + b)' = a' \cdot b'$$

$$(a \cdot b)' = a' + b'$$

Combination:

$$(a \cdot b) + (a \cdot b') = a$$

$$(a + b) \cdot (a + b') = a$$

Consensus:

$$(a \cdot b) + (a' \cdot c) + (b \cdot c) = (a \cdot b) + (a' \cdot c)$$

$$(a + b) \cdot (a' + c) \cdot (b + c) = (a + b) \cdot (a' + c)$$

Factorization:

$$(a + b) \cdot (a' + c) = (a \cdot c) + (a' \cdot b)$$

$$(a \cdot b) + (a' \cdot c) = (a + c) \cdot (a' + b)$$



# Properties

- If there is no ambiguity, symbol  $\cdot$  can be omitted
- Symbol  $'$  is used for NOT
- The dual of an algebraic expression is obtained by exchanging  $+$  and  $\cdot$  and exchanging 0 and 1.
- Theorems appear in dual pairs. When there is only one theorem in a line, it is called self-dual, i.e. dual expression = original expression
- The fundamental theorems are demonstrated starting from Huntington's postulates.

# Demonstrations

Absorption theorem:  $a + (a' \cdot b) = a + b$

- Perfect Induction (all cases)

a	a'	b	a + a'b	a + b
0	1	0	0	0
0	1	1	1	1
1	0	0	1	1
1	0	1	1	1

- Using postulates and already demonstrated theorems

$$a + a'b = (a + a') \cdot (a + b)$$

$$(a + a') \cdot (a + b) = 1 \cdot (a + b)$$

$$1 \cdot (a + b) = a + b$$

distributivity  
complement  
identity

Consensus theorem:  $ab + a'c + bc = ab + a'c$

- Using postulates and already demonstrated theorems

$ab + a'c + bc$	$= ab + a'c + 1 \cdot (bc)$	identity
$ab + a'c + 1 \cdot (bc)$	$= ab + a'c + (a + a') \cdot (bc)$	complement
$ab + a'c + (a + a') \cdot (bc)$	$= ab + a'c + abc + a'bc$	distributivity
$ab + a'c + abc + a'bc$	$= ab + abc + a'c + a'bc$	commutativity
$ab + abc + a'c + a'bc$	$= ab + a'c$	absorption

## Definition of Boolean Operators

AND

$$0 \cdot 0 = 0$$

$$0 \cdot 1 = 0$$

$$1 \cdot 0 = 0$$

$$1 \cdot 1 = 1$$

OR

$$0 + 0 = 0$$

$$0 + 1 = 1$$

$$1 + 0 = 1$$

$$1 + 1 = 1$$

NOT

$$0' = 1$$

$$1' = 0$$

# Boolean Expressions

- Boolean expressions are all and only those formed through the use of:
  - constants 0, 1
  - Boolean variables  $x_1, x_2, \dots, x_n$
  - AND, OR, NOT operators
- applying the following rules:
  - a constant is an expression
  - a variable is an expression
  - if  $g$  and  $h$  are Boolean expressions, then so are  $g + h$ ,  $g \cdot h$  and  $g'$

# Boolean Operators Precedence

Boolean operators precedence is:

1. brackets
2. NOT
3. AND
4. OR

Consequence: brackets enclose OR expressions

Example:  $A(B + C)(C + D')$



# Boolean Functions

Given  $B=\{0,1\}$ ,  $f$  is a logic or Boolean function of  $n$  Boolean variables  $x_1, x_2, \dots, x_n$  :

$$f(x_1, x_2, \dots, x_n): B^n \rightarrow B$$

that matches all value assignments to Boolean variables  $x_1, x_2, \dots, x_n$  to either  $0$  or  $1$ .

$B^n$  is the Cartesian product  $\underbrace{B \times B \times \dots \times B}_n$   $n$  times

How many functions of  $n$  Boolean variables exist?

- $n$  variables can take  $k = 2^n$  possible values, i.e. the number of arrangements with repetitions of 2 elements taken from  $n$  ( $n$  choose 2)
- for each assignment the function takes either value 0 or 1 ( $m = 2$ )
- the number of functions on  $n$  Boolean variables equals the number of repeated arrangements of  $m$  elements taken from  $k$  ( $m$  choose  $k$ )

$$D'_{m,k} = m^k = 2^{2^n}$$

When  $n = 1$  there are 4 functions:

- $f(x) = 0$  constant output 0
- $f(x) = 1$  constant output 1
- $f(x) = x$  input transferred onto output
- $f(x) = x'$  complemented input transferred onto output

When  $n=2$  there are 16 possible functions:

- |                    |                  |                            |                               |
|--------------------|------------------|----------------------------|-------------------------------|
| ■ $f(x,y) = 0$     | constant 0       | ■ $f(x,y) = x+y$           | OR                            |
| ■ $f(x,y) = 1$     | constant 1       | ■ $f(x,y) = (x+y)'$        | <b>NOR</b>                    |
| ■ $f(x,y) = x$     | transfer         | ■ $f(x,y) = x'+y$          | implication $x \rightarrow y$ |
| ■ $f(x,y) = y$     | transfer         | ■ $f(x,y) = x+y'$          | implication $y \rightarrow x$ |
| ■ $f(x,y) = x'$    | transfer and NOT | ■ $f(x,y) = xy'$           | inhibition of y               |
| ■ $f(x,y) = y'$    | transfer and NOT | ■ $f(x,y) = x'y$           | inhibition di x               |
| ■ $f(x,y) = xy$    | AND              | ■ $f(x,y) = x \oplus y$    | <b>EXOR</b>                   |
| ■ $f(x,y) = (xy)'$ | <b>NAND</b>      | ■ $f(x,y) = (x \oplus y)'$ | <b>EXNOR</b>                  |

NAND, NOR, EXOR, EXNOR: non-elementary Boolean functions.

<b>x</b>	<b>y</b>	<b>0</b>	<b>1</b>	<b>x</b>	<b>y</b>	<b>x'</b>	<b>y'</b>	<b>xy</b>	<b>x↑y</b>
0	0	0	1	0	0	1	1	0	1
0	1	0	1	0	1	1	0	0	1
1	0	0	1	1	0	0	1	0	1
1	1	0	1	1	1	0	0	1	0

<b>x</b>	<b>y</b>	<b>x+y</b>	<b>x↓y</b>	<b>x→y</b>	<b>y→x</b>	<b>xy'</b>	<b>x'y</b>	<b>x⊕y</b>	<b>(x⊕y)'</b>
0	0	0	1	1	1	0	0	0	1
0	1	1	0	1	0	0	1	1	0
1	0	1	0	0	1	1	0	1	0
1	1	1	0	1	1	0	0	0	1

# Representation of Boolean Functions

Formalisms:

- Expressions
- Truth tables
- Circuit diagrams.

A representation formalism is said to be canonical if, given two arbitrary functions  $f$  and  $g$  these are equal if and only if their representations are the same.

Truth tables are canonical representations, expressions and circuit diagrams are not.

# Truth Tables

The truth table of a Boolean function  $f$  is a tabular list of the values of  $f$  for all possible combinations of its arguments. For a function  $f$  of  $n$  variables, the table has  $2^n$  rows and  $n + 1$  columns.

AND		
x	y	xy
0	0	0
0	1	0
1	0	0
1	1	1

OR		
x	y	$x+y$
0	0	0
0	1	1
1	0	1
1	1	1

NOT	
x	$x'$
0	1
1	0

# NAND and NOR

NAND and NOR are used for building digital circuits using logic gates. From a technological point of view it is easier to implement NAND and NOR gates than AND, OR and NOT gates.

Associativity is not valid for NAND and NOR.

NAND		
x	y	$(xy)'$
0	0	1
0	1	1
1	0	1
1	1	0

NOR		
x	y	$(x+y)'$
0	0	1
0	1	0
1	0	0
1	1	0

Symbols found  
in literature:  
NAND  $\uparrow$ , NOR  $\downarrow$

# EXOR and EXNOR

EXOR is the difference operator  $x \neq y = xy' + x'y$

EXNOR is the equality operator  $x \equiv y = xy + x'y'$

EXOR		
x	y	$x \oplus y$
0	0	0
0	1	1
1	0	1
1	1	0

EXNOR		
x	y	$(x \oplus y)'$
0	0	1
0	1	0
1	0	0
1	1	1

Symbols found in literature:  
EXOR  $\oplus$ , EXNOR  $\equiv$ .



# Implication

It is used to express conditionals like

*If the sun shines, we go out for a walk*

$x \rightarrow y$

$x$  implies  $y$

Case by case analysis:

- $x=1$  and  $y=1$ :  $x \rightarrow y$  is true (it equals 1) INTUITIVE!
- $x=1$  and  $y=0$ :  $x \rightarrow y$  is false (it equals 0) INTUITIVE!
- $x=0$  and  $y=0$  or  $y=1$ :  $x \rightarrow y$  is true (it equals 1) COUNTERINTUITIVE!

**If the assumption is false, we can draw any conclusion!**

Boolean expression:

$$\begin{aligned}x \rightarrow y &= xy + x' && \text{absorption} \\ &= x' + y\end{aligned}$$

Implication is not an elementary logic gate.

# Functionally Complete Sets

A set of operators is **functionally complete** if all Boolean functions can be described by an expression that uses only the operators of that set.

Functionally complete sets:

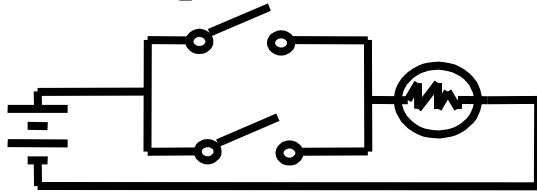
- AND, OR, NOT
- AND, NOT: in fact  $x + y = (x'y')'$  (De Morgan)
- OR, NOT: in fact  $xy = (x' + y')'$  (De Morgan)
- NAND: in fact  $x' = (x \cdot x)' = x \uparrow x$ ,  $xy = ((x \cdot y)')' = (x \uparrow y)' = (x \uparrow y) \uparrow (x \uparrow y)$
- NOR: in fact  $x' = (x + x)' = x \downarrow x$ ,  $x + y = ((x + y)')' = (x \downarrow y)' = (x \downarrow y) \downarrow (x \downarrow y)$

The functional completeness of NAND and NOR has important consequences on the implementation of digital circuits.

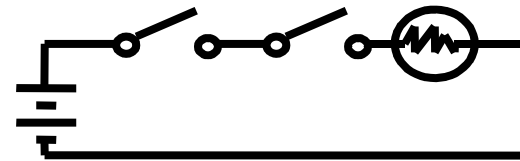
# Logic Gates

Boolean algebra operators can be physically realized using electronic circuits that operate as suitably connected switches.

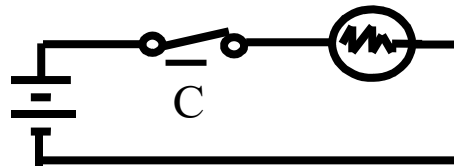
**Switches in parallel => OR**



**Switches in series => AND**



**Normally closed switch => NOT**



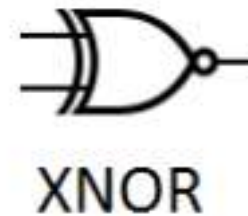
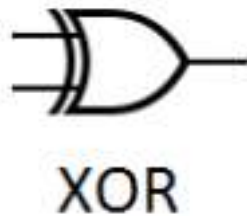
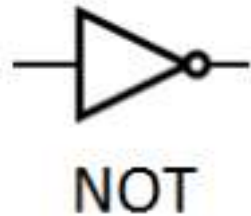
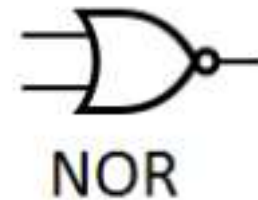
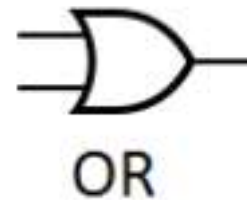
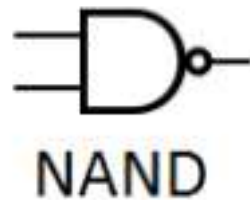
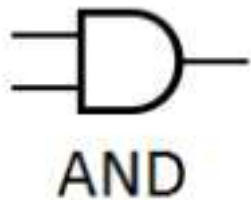
In early computers, switches were operated by magnetic fields produced in coils called relays. Switches allowed or prevented current flow.

Later vacuum tubes to allow or prevent current flow replaced relays.

Today, transistors are used as electronic switches allow or prevent current flow.

Logic gates are electronic devices that physically implement the Boolean operators AND, OR, NOT, NAND, NOR, EXOR, EXNOR.

# Logic Gate Symbols



# Logic Diagrams

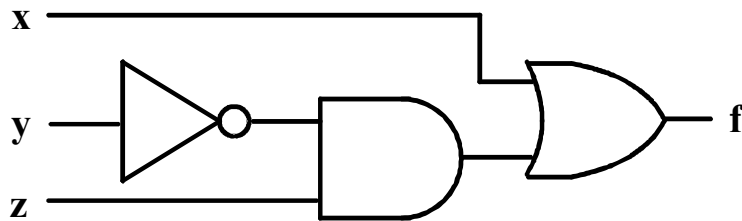
- A logic diagram is composed by logic gates connected by wires.
- Boolean expressions, truth tables and logic diagrams describe the same function!
- Truth tables are unique, not so are logical expressions and diagrams. This allows flexibility in the implementation of functions.

# Example

Boolean Function

$$f(x, y, z) = x + y'z$$

Logic Diagram



Truth Table

f			
x	y	z	$x+y'z$
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	1
1	1	1	1

# Digital (Logic) Circuits

Given a set of input symbols coded on  $n$  bits ( $n \geq 0$ ) and a set of output symbols coded on  $m$  bits ( $m > 0$ ), a **digital (logic) circuit** is a hardware processing system that accepts a sequence of input symbols and transforms it into a sequence of output symbols.

The circuit is **combinational** if the output symbol depends solely on the input symbol at that same time. The network therefore has **no memory**.

The circuit is **sequential** if the output symbol depends on both the current and the past input symbols. The network has **memory**. The **state** stores relevant information of the input sequence that determines the evolution of the circuit over time. The number of states in sequential networks is **finite**.



# Combinational Circuits: the Adder

If you want to add 2 n-bit numbers, what would be the size of the truth table?  $2^{(n+n)}$

- $n = 2$ :  $2^4 = 16$  rows
- $n = 4$ :  $2^8 = 256$  rows
- $n = 8$ :  $2^{16} = 65536$  rows
- $n = 16$ :  $2^{32} = 4$  billion rows.

Conclusion: we need an alternative design method, which imitates the manual procedure of adding:

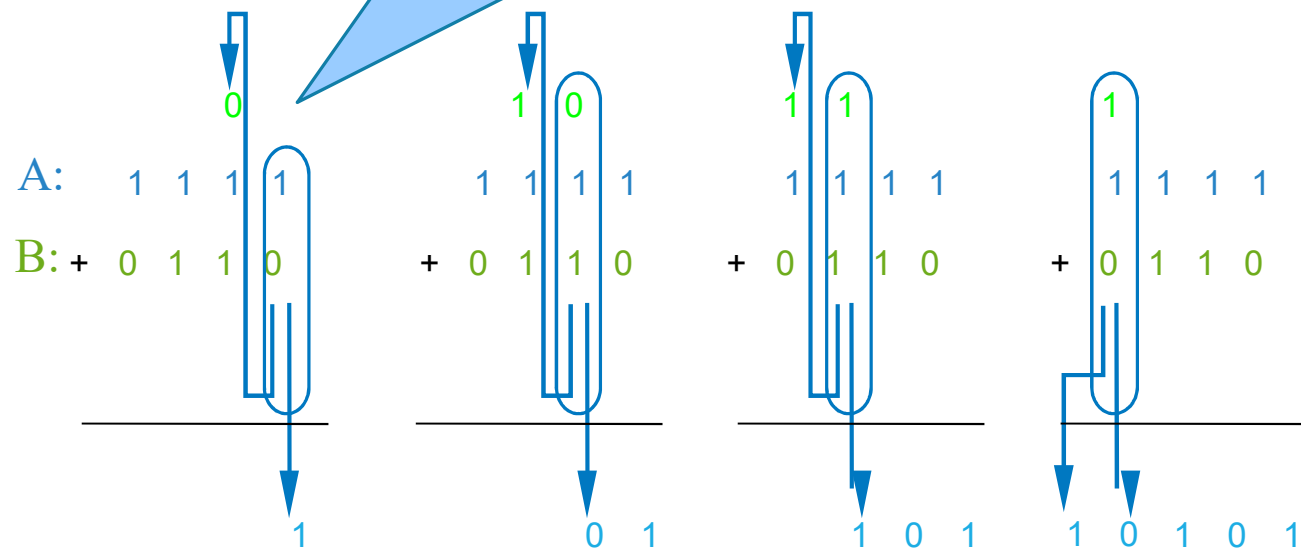
- put numbers in columns (bits of the same weight)
- proceed column by column from the least significant weight (rightmost column)
- compute the sum of the column and the carry that is propagated to the next column

# Informatics Unit T1 Binary sum

- Basic rules:

$$\begin{array}{rclcl} 0 & + & 0 & = & 0 \\ 0 & + & 1 & = & 1 \\ 1 & + & 0 & = & 1 \\ 1 & + & 1 & = & 0 \quad ( \text{carry} = 1 ) \end{array}$$

The carry-in in the rightmost column is zero by definition



To add the columns, let us design the following functional blocks using truth tables:

- Half-Adder: 1-bit  $X$  and  $Y$  inputs, 1-bit  $C_{out}$  (carry out) and  $S$  sum outputs (for the rightmost column)
- Full-Adder: 1-bit  $X$ ,  $Y$  and  $C_{in}$  (carry in) inputs, 1-bit carry  $C_{out}$  and sum  $S$  outputs (for the other columns)

and let us connect them to implement a Ripple Carry Adder: a functional block for the sum of a pair of integers  $X$  and  $Y$  on  $n$  bits with  $C_{in}$  on 1 bit.

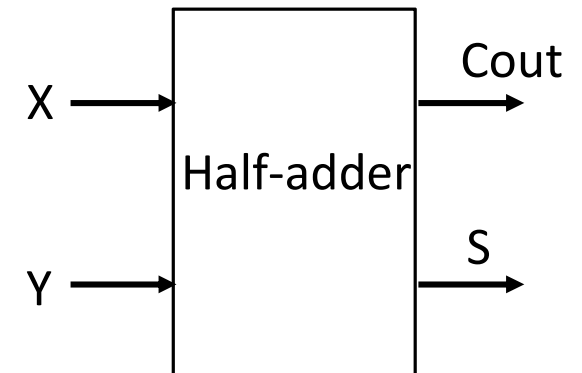
# Half-adder

Operations:

<b>X</b>	<b>0</b>	<b>0</b>	<b>1</b>	<b>1</b>
<b>+</b>	<b>+</b>	<b>+</b>	<b>+</b>	<b>+</b>
<u><b>Y</b></u>	<u><b>0</b></u>	<u><b>1</b></u>	<u><b>0</b></u>	<u><b>1</b></u>
<b>Cout S</b>	<b>0 0</b>	<b>0 1</b>	<b>0 1</b>	<b>1 0</b>

Truth table:

Half-adder			
X	Y	Cout	S
0	0	0	0
0	1	0	1
1	0	0	1
1	1	1	0



## Boolean Function

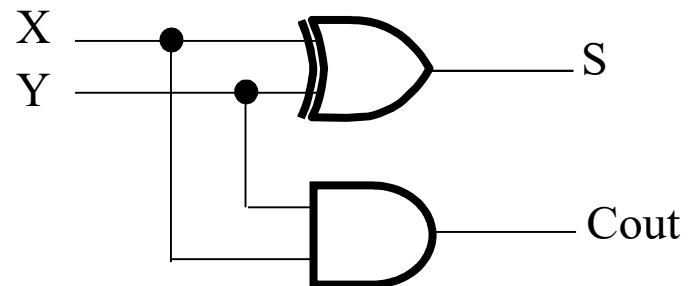
S is 1 if and only if (X = 0 AND Y = 1) OR (X = 1 AND Y = 0)

$$S = XY' + X'Y = X \oplus Y$$

Cout is 1 if and only if X = 1 AND Y = 1

$$\text{Cout} = XY$$

## Logic Diagram



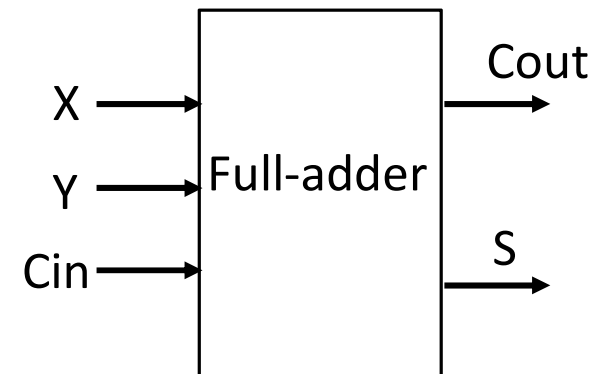
# Full-adder

Operations:

<b>Cin</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>0</b>	<b>1</b>	<b>1</b>	<b>1</b>	<b>1</b>
+	+	+	+	+	+	+	+	+
<b>X</b>	<b>0</b>	<b>0</b>	<b>1</b>	<b>1</b>	<b>0</b>	<b>0</b>	<b>1</b>	<b>1</b>
+	+	+	+	+	+	+	+	+
<u><b>Y</b></u>	<u><b>0</b></u>	<u><b>1</b></u>	<u><b>0</b></u>	<u><b>1</b></u>	<u><b>0</b></u>	<u><b>1</b></u>	<u><b>0</b></u>	<u><b>1</b></u>
<b>Cout S</b>	<b>0 0</b>	<b>0 1</b>	<b>0 1</b>	<b>1 0</b>	<b>0 1</b>	<b>1 0</b>	<b>1 0</b>	<b>1 1</b>

Truth table:

Full-adder				
X	Y	Cin	Cout	S
0	0	0	0	0
0	0	1	0	1
0	1	0	0	1
0	1	1	1	0
1	0	0	0	1
1	0	1	1	0
1	1	0	1	0
1	1	1	1	1





Boolean function

S is 1 if and only if

(Cin = 1 AND X = 0 AND Y = 0) OR  
(Cin = 0 AND X = 0 AND Y = 1) OR  
(Cin = 0 AND X = 1 AND Y = 0) OR  
(Cin = 1 AND X = 1 AND Y = 1)

$$S = \text{Cin}X'Y' + \text{Cin}'X'Y + \text{Cin}'XY' + \text{Cin}XY$$

Let us resort to Boolean Algebra rules to manipulate the expression:

$$S = \text{Cin}X'Y' + \text{Cin}'X'Y + \text{Cin}'XY' + \text{Cin}XY$$

$$= \text{Cin}(XY + X'Y')$$

$$= \text{Cin}(X \oplus Y)'$$

$$= \text{Cin} \oplus (X \oplus Y)$$

distributivity

definition of EXOR/EXNOR

definition of EXOR

Cout is 1 if and only if

$$\begin{aligned} & (\text{Cin} = 1 \text{ AND } X = 0 \text{ AND } Y = 1) \text{ OR} \\ & (\text{Cin} = 1 \text{ AND } X = 1 \text{ AND } Y = 0) \text{ OR} \\ & (\text{Cin} = 1 \text{ AND } X = 1 \text{ AND } Y = 1) \text{ OR} \\ & (\text{Cin} = 0 \text{ AND } X = 1 \text{ AND } Y = 1) \text{ OR} \end{aligned}$$

$$\text{Cout} = \text{Cin}X'Y + \text{Cin}XY' + \text{Cin}XY + \text{Cin}'XY$$

Let us resort to Boolean Algebra rules to manipulate the expression:

$$\text{Cout} = \text{Cin}X'Y + \text{Cin}XY' + \text{Cin}XY + \text{Cin}'XY$$

$$= \text{Cin}(X'Y + XY') + XY(\text{Cin} + \text{Cin}')$$

$$= \text{Cin}(X \oplus Y) + XY(1)$$

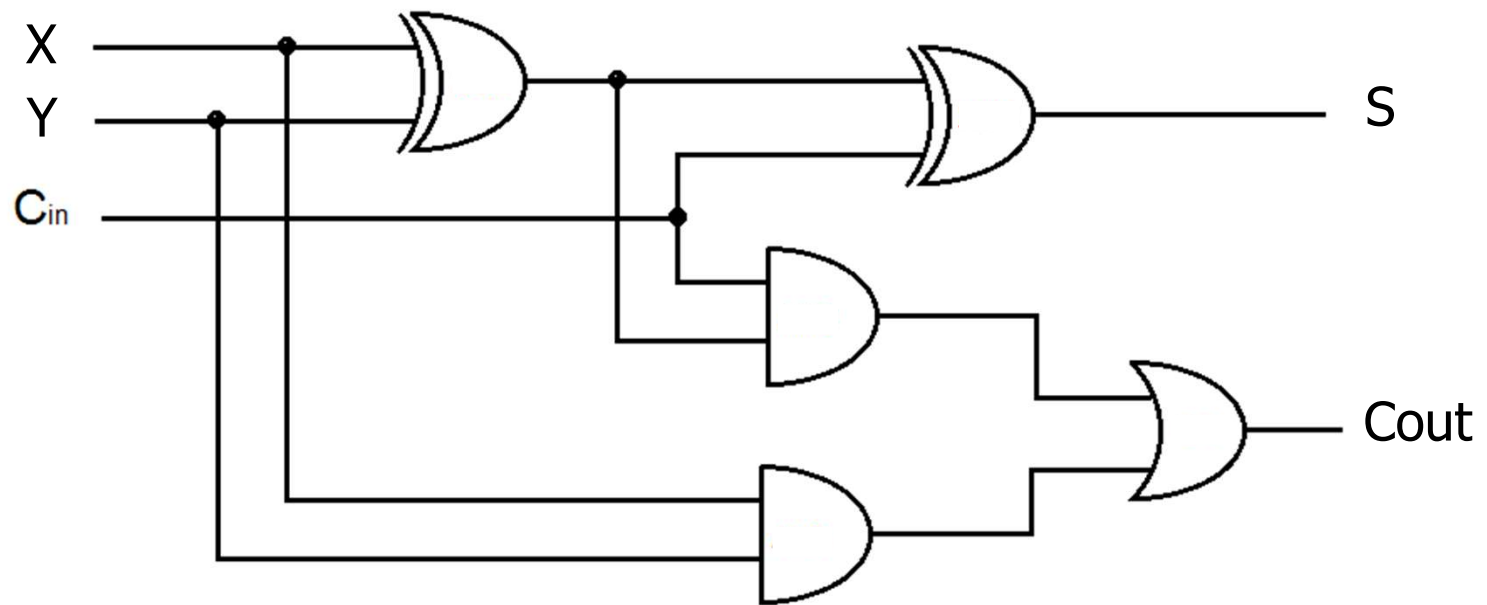
$$= \text{Cin}(X \oplus Y) + XY$$

distributivity

EXOR/complement

identity

## Logic Diagram



# Binary Adders

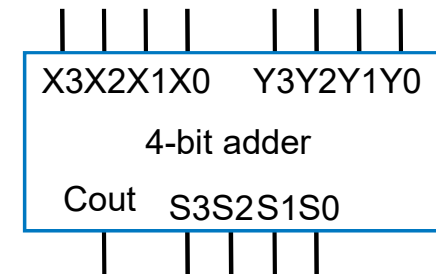
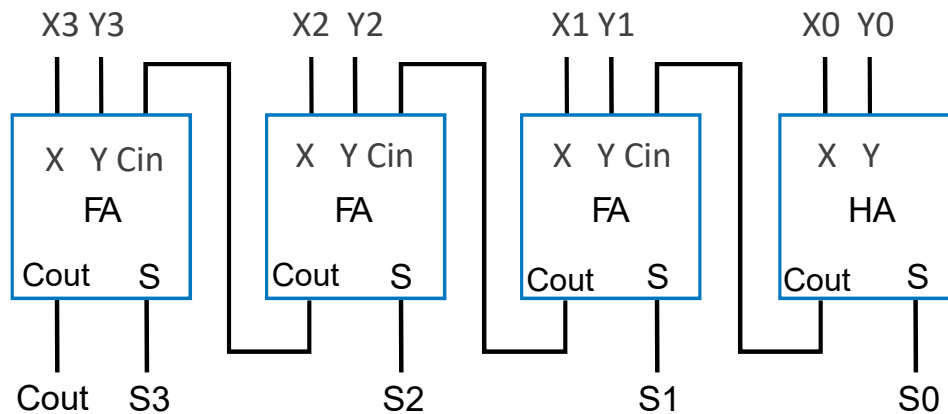
To add operands on several bits, we "group" the logic signals together in vectors and use functional blocks that work on vectors

Example: 4-bit ripple carry adder:  
it add vectors  $X(3:0)$  and  $Y(3:0)$  to  
get a sum vector  $S(3:0)$   
The carry out from the  $i$ -th cell  
becomes the carry in of the  $i + 1$ -th  
cell

Description	Subscript 3 2 1 0	Name
Cin	0 1 1 0	$C_i$
Augend	1 0 1 1	$X_i$
Addend	<u>0 0 1 1</u>	$Y_i$
Sum	1 1 1 0	$S_i$
Carry	0 0 1 1	$C_{i+1}$

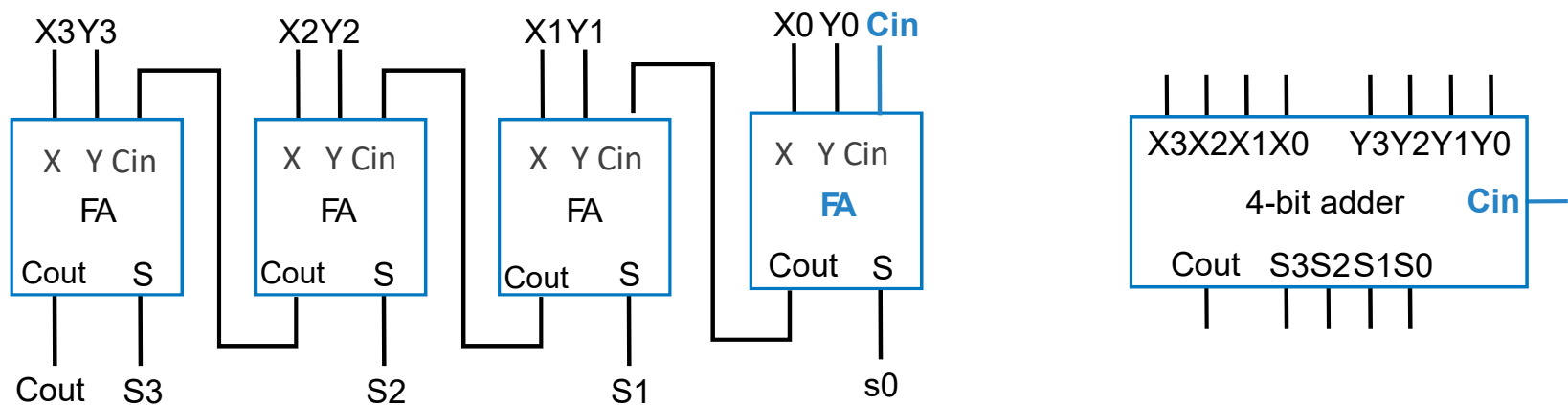
# 4-bit Ripple Carry adder

- A 4-bit Ripple Carry Adder consists of 1 Half-adder and 3 Full Adders in cascade
- Ripple carry: the carry propagates from right to left through the blocks



- By replacing the Half-adder with a Full-adder, we implement  

$$X + Y + C_{in}$$
- Useful for cascade connection of multiple adders



- By cascading k 4 bit adders, 4k-bit adders are obtained.
- Example: 8-bit adder obtained as a cascade of k = 2 4-bit adders:

