

Versuch V5

C405 Hardwarepraktikum II

Abnahme: 20. Januar 2025

Stand: 21. Januar 2025

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1 Schaltplan

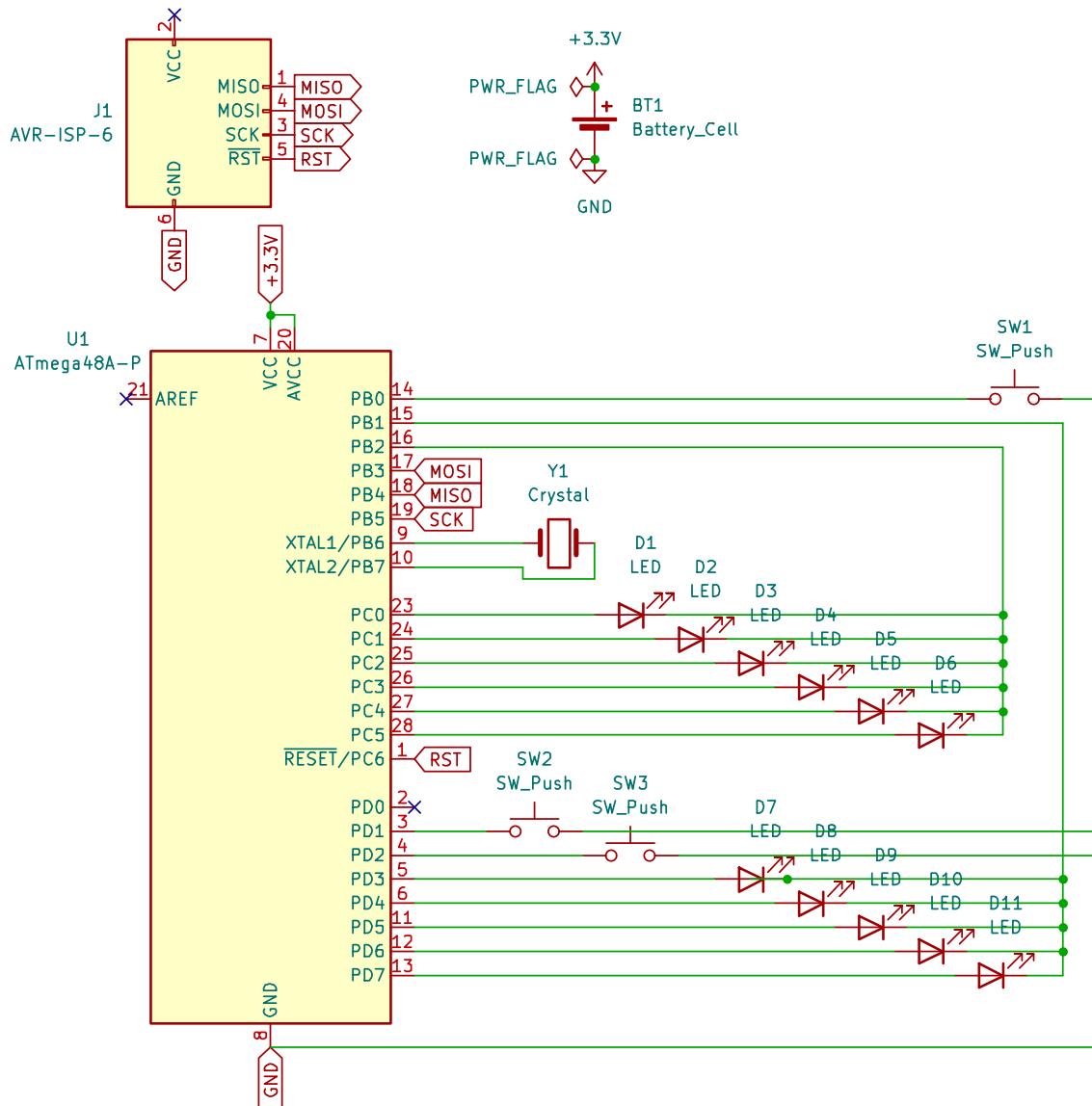


Abb. 1.1: Schaltplan

2 PCB Design

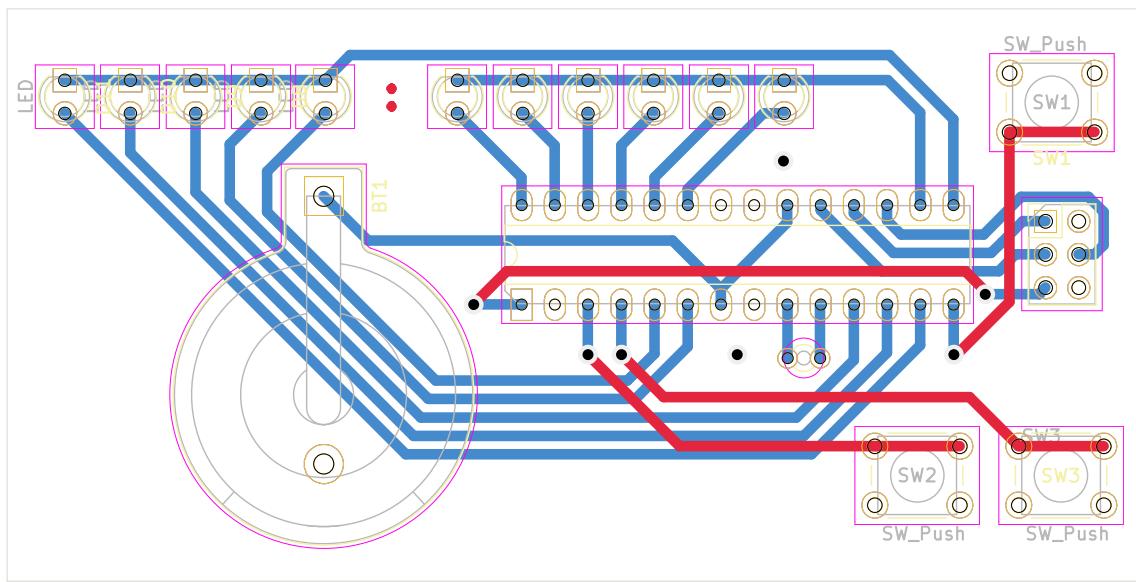


Abb. 2.1: Entwurfsansicht Leiterplatte

3 Produkt

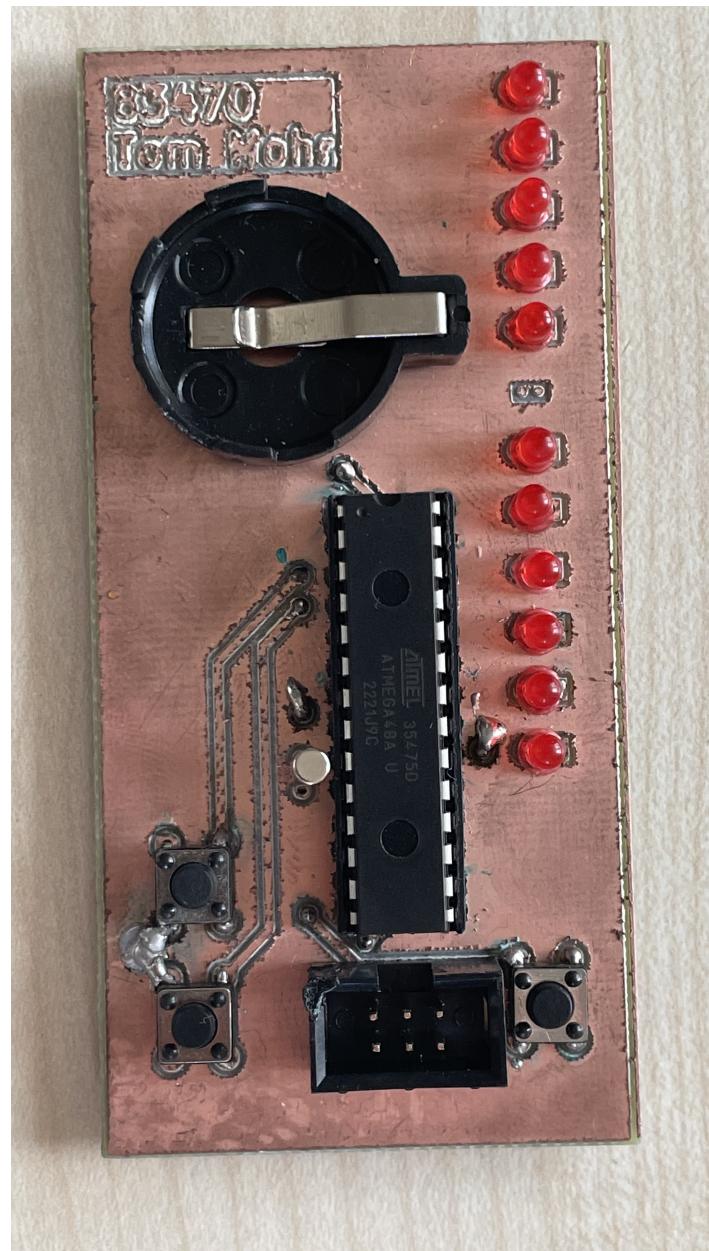


Abb. 3.1: Platine (Vorderseite)

3 Produkt

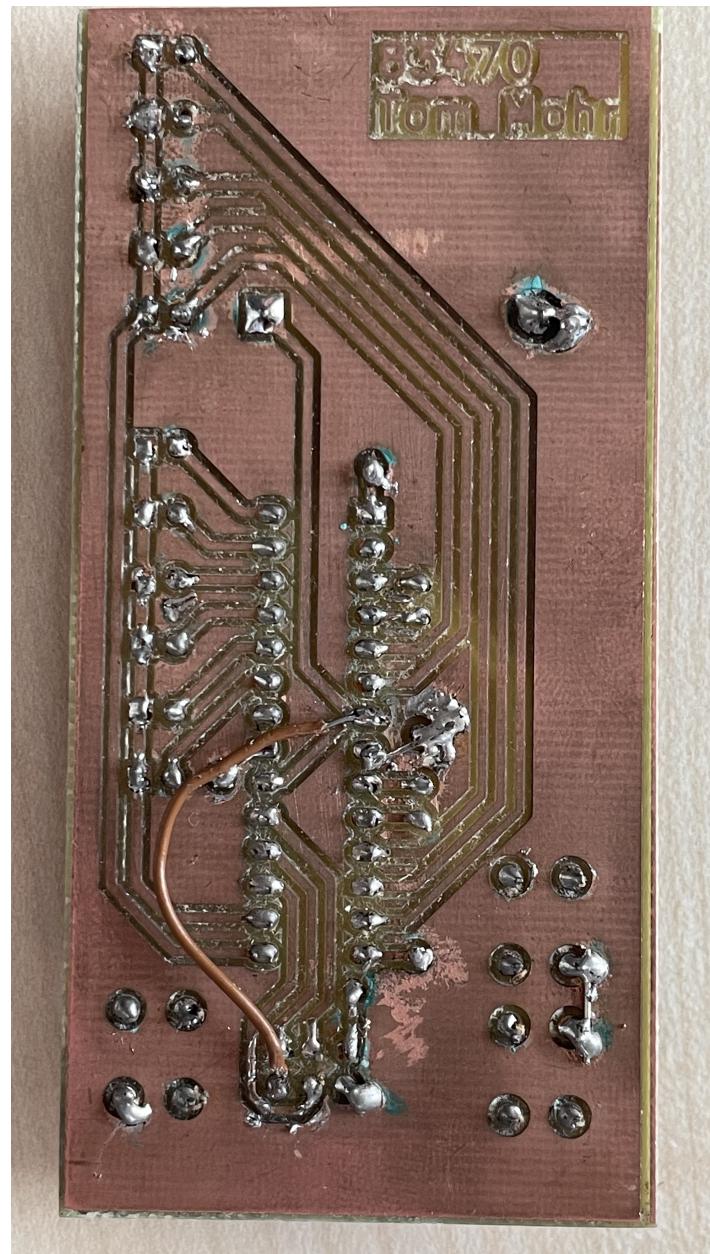


Abb. 3.2: Platine (Rückseite)