XIAOLI GUO

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OBJECTIVE: full time level course-based graduate school study in control systems, signal processing, microcontroller applications, and other electrical engineering applications

EDUCATION: Bachelor of Science in Electrical Engineering December 2012

Minor: Mathematics

The Pennsylvania State of University, University Park, PA Cumulative GPA: 3.03/4.00

- Autonomous Intelligent Car Design and Build
- Digital and Analog Control Feedback Systems (PID, PI, PD)
- Digital and Analog Communication Systems

WORK EXPERIENCE:

Technical Developer **Freescale Semiconductor, Austin, Texas** May 2012 to Aug 2012

- Developed codes and made documentations for microcontroller applications
- Worked as a technical assistant during Freescale Technology Forum (FTF) America
- Participated Freescale/ARM conference during FTF
- Evaluated and developed high school Freescale Cup Kit included a 45-page manual
- Teamed with technical personal, developed timing gate program for Kinetis K70 MCU

Lab Assistant **Material Research Lab (MRL), University Park, PA** Jun 2011 to Aug 2011

- Tested substrates' electrical characteristics by using split cavity resonator

Math/Physics Tutor **Academic Excellence Center, University Park, PA** Dec 2010 to May 2011

Technical Developer **Minmetal Trading Co., Shanghai, China** May 2009 to Aug 2009

- Channeled, organized and classified trade documents and contracts
- Responsible for building database and other operations assigned by superiors

PROJECTS:

In-Class Autonomous Intelligent Car Design

- Researched, designed and built an intelligent car with a team of two
- Awarded the 4th place in the Freescale Cup Intelligent Car Race America

In-Class Servo Control System Design

- Designed and simulated a control feedback system to achieve required specs

SKILLS: MATLAB CodeWarrior 10.X IAR Embedded Workbench

Oscilloscope Mandarin (fluent) Simulink