# **Specific-Width Floating-Point Typedefs**

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ISO/IEC JTC1 SC22 WG21/SG6 Numerics N??? - 2013-4-??

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### **Important**

This is NOT an official Boost library.



### Note

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### **Abstract**

It is proposed to add to the C++ standard several optional typedefs for floating-point types having specified width. In particular, the optional types include float16\_t, float32\_t, float64\_t, float128\_t, and their corresponding fast and least types. The optional floating-point types are to conform with the corresponding types binary16, binary32, binary64, and binary128 described in IEEE\_ floating-point format.

The optional floating-point types having specified width are to be contained in a new standard library header <cstdfloat>. Any of the optional floating-point types having specified width included in the implementation should have full support for the functions in <cmath> and seamlessly interoperate with <complex>. Any of the optional floating-point types having specified width included in the implementation must template specializations of std::numeric\_limits in limits>. The proposed new floating-point types having specified width will be defined in the global and std namespaces.

It is also proposed to provide additional suffix(es) to specify constants to suit precision lower than that of float and precision higher than that of long double.

Floating-point types having specified width are expected to significantly improve clarity of code and portability of floating-point calculations. Analogous improvements for integer calculations were recently achieved via standardization of integer types having specified width such as int8\_t, int16\_t, int32\_t, and int64\_t.

The main objectives of this proposal are to:

- Extend the range of floating-point precision.
- Reduce errors in precision.
- Improve clarity of coding.
- Improve portability, reliability and safety.



# **Background**

Support for mathematical facilities and specialized number types in C++ is progressing rapidly. Currently, C++11 supports floating-point calculations with its built-in types float, double, and long double as well as imlementations of numerous elementary and transcendental functions.

A variety of higher transcendental functions of pure and applied mathematics were added to the C++11 libraries via technical report TR1. It is now proposed to fix these into the next C++1Y standard.

Other mathematical special functions are also now proposed, for example, A proposal to add special mathematical functions according to the ISO/IEC 80000-2:2009 standard Document number: N3494 Version: 1.0 Date: 2012-12-19

The Boost.Math library was accepted into Boost several years ago. It implements many of the functions in both documents mentioned above and has become quite widely used.

There is also progress in C++ in the area of multiprecision, including support of user-defined multiprecision floating-point numbers. In particular, the acceptance and release of Boost.Multiprecision provides much higher precision than built-in long double with its cpp\_dec\_float data type. Boost.Multiprecision has a flexible front-end that employs a variety of backends to implement multiprecision floating-point types including the well-established GNU Multiple Precision Arithmetic Library and GNU MPFR library libraries as well as a full open-license backend that originates from the e\_float (TOMS Algorithm 910) library by Christopher Kormanyos and John Maddock.

Since Boost.Multiprecision and Boost.Math work seamlessly, a float\_type typedef can be used to switch from a built-in type to a multiprecision type with tens or even hundreds of decimal digits. This allows all the special functions and distributions in Boost.Math to be used at any chosen precision.

Other users and domains are finding the need and utility of decimal and binary fixed-point.

Of course, moving away from hardware supported types to software using C++ templates carries a small price at compile-time, and potentially a much bigger price at runtime. Nonetheless, the new numerical types have wide ranges of application required in numerous programming domains.

All these development have made C++ much more attractive to the scientific and engineering community, especially those needing mathematical functions and higher (or lower) precision for some of their calculations. Previously these domains were predominantly covered by computer algebra systems.



<sup>&</sup>lt;sup>1</sup> Conditionally-supported Special Math Functions for C++14, N3584, Walter E. Brown

### Introduction

Since the inceptions of C and C++, the built-in types float, double, and long double have provided a strong basis for floating-point calculations. Optional compiler conformance with IEEE\_ floating-point format has generally led to a relatively reliable and portable environment for floating-point calculations in the programming community.

It is, however, emphasized that floating-point adherence to IEEE\_floating-point format is not mandated by the current C++ language standard. Nor does the standard specify the widths, precisions or lyaout of its built-in types float, double, and long double. This can lead to portability problems, introduce poor efficiency on cost-sensitive microcontroller architectures, and reduce reliability and safety.

This situation reveals a need for a standard way to specify precision. It is also desirable to extend the precision of existing types to both lower and higher precisions. The extension to lower precision is expected to simplify and improve efficiency of floating-point implementations on cost-sensitive architectures such as small microcontrollers. The extension to higher precision is useful for large-scale high-performance numerical calculations and should ease the transition to multiprecision by providing built-in types with progressing precision of finer granularity.

All of these improvements should improve portability, reliability, and safety of floating-point calculations in C++ by ensuring that the actual precision of a floating-point type can be exactly determined both at compile-time as well as during the run of a calculation. Strong interest in floating-point typedefs having specified width has, for example, recently been expressed on the Boost list discussion of precise floating-point types.

Recent specification of integer typedefs having specified width in C99, C11, C++11, and C++ draft specification has drastically improved integer algorithm portability and range.

One example of how integer typedefs having specified width have proven to be essential is described by Robert Ramey Usefulness of fixed integer sizes in portability (for Boost serialization library).

The motivations to provide floating-point typedefs having specified width are analogous to those that led to the introduction of integers having specified width such as int8\_t, int16\_t, int32\_t, and int64\_t. The specification of floating-point typedefs having specified width and adherence to IEEE\_floating-point format can potentially improve the C++ language significantly, especially in the scientific and engineering communities where other languages have found benefit from types that conform exactly to the IEEE\_floating-point format.

(Notes on jargon: Section 22.3 in the book "The C++ Standard Library Extensions", P. Becker, Addison Wesley 2007, ISBN 0-321-41299-0 is called "Fixed-Size Integer Types". Use of the descriptor *fixed* has lead to some confusion. So the descriptor *specific* in conjunction with width is here used to match the wording of C99 and C11 in the sections and subsections describing stdint.h.)



# The proposed typedefs and potential extensions

The core of this proposal is based on the typedefs float16\_t, float32\_t, float64\_t, float128\_t, and their corresponding least and fast types. These floating-point typedefs have specified widths and they are to conform with the corresponding types binary16, binary32, binary64, and binary128 specified in IEEE\_floating-point format. In particular, float16\_t, float32\_t, float64\_t, and float128\_t correspond to floating-point types with 11, 24, 53, and 113 binary significand digits, respectively. These are defined in IEEE\_floating-point format, and there are more detailed descrptions of each type at IEEE half-precision floating-point format, IEEE single-precision floating-point format, Quadruple-precision floating-point format, and IEEE 754 extended precision formats and x86 80-bit Extended Precision Format.

One could envision two ways to name the proposed floating-point types having specified width:

```
float11_t, float24_t, float53_t, float113_t, ...float16_t, float32_t, float64_t, float128_t, ...
```

The first set above is intuitively coined from IEE754:2008. It is also consistent with the gist of integer types having specified width such as int64\_t, in so far as the number of binary digits of *significand* precision is contained within the name of the data type.

On the other hand, the second set with the size of the *whole type* contained within the name may be more intuitive to users. Here, we prefer this naming scheme.

No matter what naming scheme is used, the exact layout and number of significand and exponent bits can be confirmed as IEEE754 by checking std::numeric\_limits<type>::is\_iec559 == true, and the byte-order.

We will now consider several examples showing how various implementations might include floating-point typedefs having specified width.

An implementation that has float and double corresponding to IEEE 754 binary32, binary64, respectively, could introduce float32\_t and float64\_t into the std namespace as shown below.

```
namespace std
{
  typedef float float32_t;
  typedef double float64_t;
}
```

There may be a need for octuple-precision float, in other words float256\_t with about 240 binary significand digits of precision. In addition, a float512\_t type with even more precision may be considered as a option. Beyond these, there may be potential extension to multiprecision types, even arbitrary precision, in the future.

Consider an implementation for a supercomputer. This platform might have float, double, and long double corresponding to IEEE 754 binary32, binary64, and binary128, respectively. In addition, this platform might have a user-defined type with octuple-precision. The implementation for this supercomputer could introduce floating-point types having specified width into the std namespace as shown below.

A cost-sensitive 8-bit microcontroller platform without an FPU does not have sufficient resources to support eight-byte binary64 in a feasible fashion. An implementation on this platform can, however, support half-precision float16\_t and single-precision float32\_t. The implementation for this 8-bit microcontroller could introduce floating-point types having specified width into the std namespace as shown below.



```
namespace std
{
  typedef my_half_precision_type float16_t;
  typedef float float32_t;
}
```

The popular Intel X8087 chipset architecture supports a 10-byte floating-point format. So it may be useful to provide optional support for float80\_t. Several implementations using \_\_IEEE\_80\_bit already exist in practice.

An implementation that supports single-precision float, double-precision double, and 10-byte long double could introduce float32\_t, float64\_t, and float80\_t into the std namespace as shown below.

```
namespace std
{
  typedef float     float32_t;
  typedef double     float64_t;
  typedef long double float80_t;
}
```

We will now examine how to use floating-point literal constants in combination with floating-point types having specified width.

At present, the only way to provide a floating-point literal constant value with precision exceeding the precision of long double is to use a character string in association with type conversion for a user-defined extended-precision type. For example, construction from a string as well as the from\_string method are used for this purpose in Boost.Math, Boost.Multiprecision and GCC libquadmath.

The sample below, for instance, uses the cpp\_dec\_float\_50 type from Boost.Multiprecision to initialize the Euler-gamma constant with 50 decimal digits of precision.

```
#include <boost/multiprecision/cpp_dec_float>

typedef boost::multiprecision::cpp_dec_float_50 mp_type;

const mp_type euler("0.577215664901532860606512090082402431042159335939924");
```

Construction from string is inappropriate for the proposed floating-point types having specified width. These should be copy assignable and copy constructable from floating-point literal constants. This requires slight changes to the core language including the addition of new floating-point literal constant suffixes. For instance, the sample below uses a potential Q suffix is used to initialize the Eulergamma constant stored in a float128\_t.

```
#include <cstdfloat>
constexpr std::float128_t euler = 0.57721566490153286060651209008240243104216Q;
```

Suffixes will be described in greater detail below.

It would also be useful to have a method of querying the size of types, similar to that provided by GCC 3.7.2 Common Predefined Macros, for example, \_\_SIZEOF\_LONG\_DOUBLE\_\_. But similar macros are not defined for \_\_float128 nor for \_\_float80.



# How to define floating-point literal suffixes?

The standard specifies that the type of a floating-point literal is double unless explicitly specified by a suffix. The standard continues by specifying that the suffixes f and F specify float, and the suffixes l and L specify long double.

Recent discussion on extended precision floating-point types in C++ has also raised the issue of how to specify constant values with a precision greater than long double, now signified by the suffix L or 1. One possible way is to add Q or q suffixes to signify that floating-point literal has quadruple precision.

Code using the Q suffix scheme is shown in the sample below.

```
#include <cstdfloat>
constexpr std::float128_t pi = 3.1415926535897932384626433832795028841972Q;
```

For half-precision floating-point literals, the suffix H or h could be used. One potential suffix for octuple-precision floating-point literals is 0 or o.

For example,

```
#include <cstdfloat>
constexpr std::float16_t euler = 0.577216H;
```

Higher precisions also require construction from floating-point literals. As the list of available suffixes dwindles, however, available suffixes might run out and the myriad of suffixes may become confusing. Floating-point literals for precisions higher than quadruple precision, then, might be better served with construction from string literals.

An alternative suffix scheme could use hybrid suffixes composed of, say, the letter F or f which stands for floating-point, to which the specified width of the type is appended, for example F16, F32, F64, F128, etc. Code using the F128 suffix scheme is shown in the sample below.

```
#include <cstdfloat>
constexpr std::float128_t pi = 3.1415926535897932384626433832795028841972F128;
constexpr std::float16_t euler = 0.577216F16;
```

This suffix scheme is unequivocal and it can be easily extended to unlimited precision. On the other hand, it may be difficult for programmers to separate the character part of the suffix from its numerical part when analyzing source code. For example, it is particularly difficult to resolve the F suffix in the initialization of euler above.



### Place in the standard

The proper place for floating-point typedefs having specified width should be oriented along the lines of the current standard. Consider the existing specification of integer typedefs having specified precision in C++11. A partial synopsis is shown below.

18.4 Integer types [cstdint] 18.4.1 Header <cstdint> synopsis [cstdint.syn]

```
namespace std
{
  typedef signed integer type int8_t; // optional
  typedef signed integer type int16_t; // optional
  typedef signed integer type int32_t; // optional
  typedef signed integer type int64_t; // optional
}

// ... and the corresponding least and fast types.
```

It is not immediately obvious where the typedefs for floating-point types having specified width should reside. One potential place is <cstdint>. The int, however, implies integer types. Here, we prefer the proposed new header <cstdfloat>.

We propose to add a new header <cstdfloat> to the standard library. The header <cstdfloat> should contain all floating-point typedefs having specified width in the implementation. Section 18.4 could be extended as shown below.

18.4? Integer and Floating-Point Types Having Specified Width 18.4.1 Header <cstdint> synopsis [cstdint.syn] 18.4.2? Header <cstdfloat> synopsis [cstdfloat.syn]

```
namespace std {
  typedef signed floating-point type float16_t;
                                                  // optional.
  typedef signed floating-point type float32_t;
                                                  // optional.
  typedef signed floating-point type float64_t;
                                                  // optional.
  typedef signed floating-point type float80_t;
  typedef signed floating-point type float128_t;
                                                 // optional.
  typedef signed floating-point type float256_t;
                                                  // optional.
  typedef signed floating-point type floatmax_t;
                                                  // optional.
  typedef signed floating-point type float_least16_t; // optional.
  typedef signed floating-point type float_least32_t; // optional.
                                                      // optional.
  typedef signed floating-point type float_least64_t;
  typedef signed floating-point type float_least80_t; // optional.
  typedef signed floating-point type float_least128_t; // optional.
  typedef signed floating-point type float_least256_t; // optional.
  typedef signed floating-point type float_fast16_t; // optional.
  typedef signed floating-point type float_fast32_t;
                                                      // optional.
  typedef signed floating-point type float_fast64_t;
                                                      // optional.
  typedef signed floating-point type float_fast80_t;
                                                     // optional.
  typedef signed floating-point type float_fast128_t; // optional.
  typedef signed floating-point type float_fast256_t; // optional.
 // namespace std
```



# Interaction with imits>

It is not proposed to make any change to std::numeric\_limits. It is, however, mandatory to provide std::numeric\_limits specializations for all floating-point types having specified width included in the implementation.

This will ensure that programs can use the established std::numeric\_limits<>::is\_iec559 member to determine if a floating-point type conforms with IEEE\_ floating-point format.



# Interoperation with <cmath>

Experience with Boost.Math and Boost.Multiprecision has shown that the normal set of elementary and transcendental functions (and possibly additional higher transcendental functions) is also essential to make the type useful in real-life computational regimes. Therefore, the implementation must provide support for the mathematical functions in the std namespace for each of the floating-point types having specified width included in the implementation.

point types having specimed within included in the imprementation.
<cmath> contains</cmath>

# Trigonometric functions: cos sin tan acos asin atan atan

### Hyperbolic functions:

cosh sinh tanh	

### Exponential and logarithmic functions:

exp frexp			
ldexp			
log			
ldexp log log10 modf			
modf			

### Power functions

pow	•		
sqr	'τ		

### Rounding, absolute value and remainder functions:

```
ceil
fabs
floor
fmod
```



# Interoperation with <complex>

TBD by Chris: Describe interoperation with <complex>.



# Improved efficiency and robustness for microcontrollers

TBD by Chris: Describe cost-sensitive floating-point regime. TBD by Chris - add reference to your book!

TBD by Chris: Describe recent confidential meetings with tier-one silicon suppliers and the relevant problems discussed therein.

TBD: Cite these as personal communications.

TBD by Chris: Explain how standards adherence and specified width can help to solve these problems by improving reliability and safety.

TBD ba Chris: Add an example and remarks on functional safety and any relevant citations from to ISO/IEC 26262.



# The context within existing implementations

Many existing implementations already support float, double, and long double. In addition, some of these either are or strive to be compliant with IEEE\_floating-point format. In these cases, it will be straightforward to support (at least) a subset of the proposed floating-point typedefs having specified width using type definitions. This was discussed above.

Some implementations for cost-sensitive microcontroller platforms support float, double, and long double, and some of these are compliant with IEEE\_floating-point format. It is not uncommon on microcontroller platforms to treat double exactly as float, and to even treat long double exactly as double. This is permitted by the standard which does not prescribe the precision for any floating-point (or integer) types, leaving them to be implementation-defined. On these platforms, the existing floating-point types could be type-defined to float32\_t. Optional support for float16\_t could provide a very useful high-performance floating-point type with half-precision.

On powerful desktop computers and workstations, long double has been treated in a variety of ways, and this has given rise to numerous portability problems. For example, suppose we wish to achieve a precision higher than the most common IEEE 64-bit floating-point type supported by the X86 chipsets normally used for double (double precision providing a precision of between 15 and 17 decimal digits).

The options for long double are many. At least one popular compiler treats long double exactly as double.

However the Intel X8087 chipset can do calculations using internal 80-bit registers, increasing the significand from 53 to 63 bits, and gaining about 3 decimal digits precision from 18 and 21. If we wish to ensure that we use all 80 bits available from Intel 8087 chips to calculate Extended precision we would use a typedef float80\_t, as shown above. If the compiler could not generate code this type directly, then it would substitute software emulation, perhaps using a Boost.Multiprecision type such as cpp\_dec\_float\_21 (or in future, cpp\_bin\_float\_21).

Some hardware, for example Sparc, provides a full 128-bit quadruple precision floating-point chip.

As of gcc 4.3, a quadruple precision is also supported on x86, but as the nonstandard type \_\_float128 rather than as a long double.

Darwin long double uses a double-double format developed first by Keith Briggs. This gives about 106-bits of precision (about 33 decimal digits) but has rather odd behavior at the extremes making implementation of std::numeric\_limits<>::epsilon() problematic.

Clang uses a similar technique:

```
#ifdef __clang__
  typedef struct { long double x, y; } __float128;
#endif
```

as described in Clang float128.

In the future, it may be useful on powerful desktop computers and workstations to strive to make long double equivalent to quadruple-precision (Quadruple-precision floating-point format) and to type define this to be float128\_t. Some architectures have hardware support for this. Those lacking hardware support for float128\_t can use software emulation to generate it. This could also be preliminarily delegated to a potential cpp\_bin\_float\_128 type, which is under development for Boost.Multiprecision.

### Survey of existing extended precision types

- 1. GNU C supports additional floating types, \_\_float80 and \_\_float128 to support 80-bit (XFmode) and 128-bit (TFmode) floating types.
- 2. Extended or Quad IEEE FP formats by Intel Intel64 mode on Linux (V12.1) provides 128 bit long double in C, however it appears that it only provides computation at 80-bit format giving 64-bit significand precision, and other bits are just padding.



- 3. Intel FORTRAN REAL\*16 is an actual 128-bit IEEE quad, emulated in software. But "I don't know of any plan to implement full C support for 128-bit IEEE format, although evidently ifort has support libraries." This is equivalent to the proposed float128\_t type.
- 4. The 360/85 and follow-on System/370 added support for a 128-bit "extended" IBM extended precision formats. These formats are still supported in the current design, where they are now called the "hexadecimal floating point" (HFP) formats.
- 5. With the availability of Boost.Multprecision, C++ programmers can now easily switch to using floating-point types that give far more decimal digits of precision (hundreds) than the built-in types float, double and long double.



## References

- 1. isocpp.org C++ papers and mailings
- 2. C++ Binary Fixed-Point Arithmetic, N3352, Lawrence Crowl
- 3. Proposal to Add Decimal Floating Point Support to C++, N3407 Dietmar Kuhl
- 4. The C committee is working on a Decimal TR as TR 24732. The decimal support in C uses built-in types \_Decimal32, \_Decimal64, and \_Decimal128. 128-bit decimal floating point in IEEE 754:2008
- 5. lists binary 16, 32, 64 and 128 (and also decimal 32, 64, and 128)
- 6. IEEE Std 754-2008
- 7. IEEE Standard for Floating-point Arithmetic, IEEE Std 754-2008
- 8. How to Read Floating Point Numbers Accurately, William D Clinger
- 9. Conditionally-supported Special Math Functions for C++14, N3584, Walter E. Brown
- 10. Walter E.Brown, Opaque Typedefs
- 11. Specification of Extended Precision Floating-point and Integer Types, Christopher Kormanyos, John Maddock
- 12. X8087 notes



# **Version Info**

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### Tip

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