

VISVESVARAYA TECHNOLOGICAL UNIVERSITY

(State University of Government of Karnataka Established as per the VTU Act, 1994)

"Jnana Sangama", Belagavi – 590 018



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

**B.Tech. in Electronics and Computer Engineering
IV Semester**

ALGORITHMS LAB (BUEL404) RECORD

Name of the Student	
USN	

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

CERTIFICATE

This is to certify that the **Mr./Miss.** _____
bearing **USN:** _____ has satisfactorily completed the experiments
in **ALGORITHMS LAB (BUEL404)** prescribed by the **Visvesvaraya
Technological University, Belagavi** for **IV Semester** UG Program
Electronics and Computer Engineering for the Academic Year **202 - 202** .

Faculty In-Charge

Chairperson

Name of the Examiner

Signature

Examiner 1:

Examiner 2:

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Expt. No.	Date	Name of the Experiment	Page No.	Marks (10 M)	Faculty Sign
1.		Selection Sort			
2.		Quick Sort			
3.		Merge Sort			
4.		All-Pairs Shortest Paths Problem using Floyd's Algorithm			
		Travelling Salesperson Problem using Dynamic Programming			
5.		Sum of Subset Problem			
6.		Hamiltonian Cycles in Undirected Graph using Backtracking			
7.		0/1 Knapsack Problem using Dynamic Programming			
		Knapsack Problem using Greedy Method			
8.		Dijkstra's Algorithm			
Demonstration Experiments					
9.		Minimum Cost Spanning Tree using Prim's Algorithm			
10.		Hamiltonian Cycles in Directed Graph using Backtracking			
11.		Multi-thread Application			
12.		Stack using Arrays			
A	Average Marks of All Experiments (30 M)				
B	Lab CIE Test (20 M)				
A+B	Total CIE Marks (50 M)				