**Computer Organization**

**Architecture diagram:**

**Detailed description of the implementation:**

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Instr** | **RW** | **B** | **J** | **WB0** | **WB1** | **MR** | **MW** | **SrcA** | **SrcB** | **Op** |
| **R-type** | **1** | **0** | **0** | **0** | **0** | **0** | **0** | **X** | **0** | **10** |
| **Addi** | **1** | **0** | **0** | **0** | **0** | **0** | **0** | **X** | **1** | **10** |
| **Load** | **1** | **0** | **0** | **1** | **0** | **1** | **0** | **X** | **1** | **00** |
| **Store** | **0** | **0** | **0** | **X** | **X** | **0** | **1** | **X** | **1** | **00** |
| **Branch** | **0** | **1** | **0** | **X** | **X** | **0** | **0** | **0** | **0** | **01** |
| **JAL** | **1** | **X** | **1** | **X** | **1** | **X** | **0** | **0** | **X** | **XX** |
| **JALR** | **1** | **X** | **1** | **X** | **1** | **X** | **0** | **1** | **X** | **XX** |

**Implementation results:**

**Problems encountered and solutions:**

**Comment:**