# Tony Dong

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## Skills Summary

Languages: Scala, Java, C#, C++, C, Python, Verilog, Assembly (M68K/ColdFire)

Interested In: Large Scale Distributed Systems, Datastores, Type Systems, Data Mining, Machine Learning

### Work Experience

Twitter (San Francisco, CA)

Software Engineer

May '15 - Present

- Worked on the transition of the TellApart infrastructure and business into Twitter
- Managed and improved the infrastructure of Periscope
- Implemented systems to connect Periscope's AWS infrastructure with Twitter's DCs

#### TellApart (Acquired by Twitter)

(Burlingame, CA)

Platform Engineer

July '14 - May '15

- Improving infrastructure to provide a service oriented architecture with strict latency requirements
- Provide base frameworks for building new applications/services at TellApart.
- Migration of identity system for audience targeting and real time bidding to lambda architecture

Twitter (San Francisco, CA)

Software Engineer, Trust and Safety Engineering

Sept - Dec '13

- Worked on a service for reporting illegal media content to the authorities
- Built new tools and features for use by the Trust and Safety Team

#### **University of Waterloo**

(Waterloo, ON) May - Sept '13

Undergraduate Research Assistant (Kshirasagar Naik, Ph.D., P.Eng.)

Implemented processes to determine resource usage of applications in Android devices

#### CI Investments Inc.

(Toronto, ON)

Programmer Analyst, Unitholder Systems

Sept - Dec '12

• Redesigned and re-implemented a system used for providing transaction settlement instructions and settlement confirmations to FundSERV Inc.

#### **Picton Mahoney Asset Management**

(Toronto, ON)

Software Developer

May - Sept '11 & Jan - Apr '12

Designed and implemented WPF, and ASP.NET applications for financial analysis using C#

## **Proiects**

- Fourth Year Design Project: Created a "Music Graph Search Service" Please Visit www.TryRhino.com
- Developing a secure distributed file system built on top of Dropbox
- Developed an algorithm to increase the number of Twitter followers @TonyDong3
- School: Created a cycle-accurate implementation of a MIPS processor using Verilog
- School: Designed an optimized WLAN simulator that implemented CSMA/CD as its MAC layer protocol
- School: Built a sound processing embedded C applications for a NIOS II processor on an Altera board
- Check out Github/tonyd3 for more projects and my open source contributions

## Education

#### **University of Waterloo**

Bachelor of Applied Science, Honours Computer Engineering

Sept '09 - Apr '14

- Achieved **Outstanding** reviews on 6/6 co-op positions
- Graduated with **Distinction**