

# Tony Dong

tonydong91@gmail.com

## Skills Summary

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**Languages:** Scala, Java, C#, C++, C, SQL, R, Matlab, Verilog, Assembly (M68K/ColdFire)

**Datastores:** MySQL, PostgreSQL, Titan, Cassandra, memcached, Voldemort, HBase

**Interested In:** Distributed Systems, Type Systems, Data Mining, Machine Learning

## Work Experience

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### Tellapart

Platform Engineer

(Burlingame, CA)

July '14 - Now

### Twitter

Software Engineer, Trust and Safety Engineering

(San Francisco, CA)

Sept - Dec '13

- Worked on a service to report illegal media content to the authorities
- Built new tools and features for use by the Trust and Safety Team

### University of Waterloo

Undergraduate Research Assistant (Kshirasagar Naik, Ph.D., P.Eng.)

(Waterloo, ON)

May - Sept '13

- Implemented processes to determine resource usage of applications in Android devices

### CI Investments Inc.

Programmer Analyst, Unitholder Systems

(Toronto, ON)

Sept - Dec '12

- Redesigned and re-implemented a system used for providing transaction settlement instructions, and settlement confirmations to FundSERV Inc.

### Picton Mahoney Asset Management

Software Developer

(Toronto, ON)

May - Sept '11 & Jan - Apr '12

- Designed and implemented WPF, and ASP.NET applications for financial analysis using C#
- Automated performance reports with a robust application designed to facilitate future additions
- Worked on a Market On Close (MOC) price alert system to take advantage of bid-ask spreads
- Responsible for performance analysis, and optimization of existing applications

### University of Waterloo

Computing Assistant, Arts Computing Office

(Waterloo, ON)

Sept - Dec '10

### Virtual Team Builders

Sales and Marketing Assistant

(Toronto, ON)

Jan - Apr '10

## Projects

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- Creating a "Music Graph Search Service" - Please Visit [www.TryRhino.com](http://www.TryRhino.com)
- Developing a secure distributed file system built on top of Dropbox
- Developed an algorithm to increase the number of Twitter followers - [@TonyDong3](https://twitter.com/TonyDong3)
- [@TweetAGIF](https://twitter.com/TweetAGIF) - Hackathon Project that lets you Tweet GIFs at people
- Created a cycle-accurate implementation of a MIPS processor using Verilog
- Designed an optimized WLAN simulator that implemented CSMA/CD as its MAC layer protocol
- Developed a Kirsch edge-detector circuit in VHDL
- Built a sound processing embedded C applications for a NIOS II processor on an Altera DE2 board
- Check out [Github/tonyd3](https://github.com/tonyd3) for more projects

## Education

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### University of Waterloo

Bachelor of Applied Science, Honours Computer Engineering

Sept '09 - Apr '14

- Achieved **Outstanding** reviews on 6/6 co-op positions
- Graduated with **Distinction**