Tony Dong tonydong91@gmail.com

Skills Summary

Languages: C#, Java, Scala, C++, C, SQL, R, Matlab, Verilog, Assembly (M68K/ColdFire) Datastores: MySQL, PostgreSQL, Titan, Cassandra, memcached, Microsoft SQL Server Interested In: Distributed Systems, Language Design, Data Mining, Machine Learning

Work Experience

Tellapart (Burlingame, CA)

Software Engineer

Twitter

July '14 - Now

(San Francisco, CA)

Software Engineer, Trust and Safety Engineering

Sept - Dec '13

- Worked on a service to report illegal media content to the authorities
- Built new tools and features for use by the Trust and Safety Team

University of Waterloo

(Waterloo, ON)

Undergraduate Research Assistant (Kshirasagar Naik, Ph.D., P.Eng.)

May - Sept '13

Implemented processes to determine resource usage of applications in Android devices

CI Investments Inc.

(Toronto, ON)

Programmer Analyst, Unitholder Systems

Sept - Dec '12

 Redesigned and re-implemented a system used for providing transaction settlement instructions, and settlement confirmations to FundSERV Inc.

Picton Mahoney Asset Management

(Toronto, ON)

Software Developer

May - Sept '11 & Jan - Apr '12

- Designed and implemented WPF, and ASP.NET applications for financial analysis using C#
- Automated performance reports with a robust application designed to facilitate future additions
- Worked on a Market On Close (MOC) price alert system to take advantage of bid-ask spreads
- Responsible for performance analysis, and optimization of existing applications

University of Waterloo

(Waterloo, ON)

Computing Assistant, Arts Computing Office

Created and managed departmental websites

Sept - Dec '10

Virtual Team Builders

(Toronto, ON)

Sales and Marketing Assistant

Jan - Apr '10

- Creating a "Music Graph Search Service" Please Visit www.TryRhino.com
- Developing a secure distributed file system built on top of Dropbox
- Developed an algorithm to increase the number of Twitter followers @TonyDong3
- @TweetAGIF Hackathon Project that lets you Tweet GIFs at people
- Created a cycle-accurate implementation of a MIPS processor using Verilog
- Designed an optimized WLAN simulator that implemented CSMA/CD as its MAC layer protocol
- Developed a Kirsch edge-detector circuit in VHDL
- Built a sound processing embedded C applications for a NIOS II processor on an Altera DE2 board
- Check out Github/tonyd3 for more projects

ducation

University of Waterloo

Bachelor of Applied Science, Honours Computer Engineering, Co-op ProgramSept 2009 - Apr 2014

Achieved Outstanding reviews on 6/6 co-op positions