Test Engineer

JOB DESCRIPTION

- Regular schedule of test execution activities on existing products, e.g. SDK/drivers
- Manual test execution of WiFi driver on Window platform
- Perform different types of testing (functional, regression, performance, etc)
- Liase with internal teams (e.g. project lead/developer/team members) to communicate on bugs
- Co-work with R&D team to reproduce customer issues and help verify fixes
- Track problems, resolutions, and bug fixes throughout testing

JOB REQUIREMENTS

- Diploma holder or any bachelor's degree holder who interested to get involve into testing field
- Good problem solver, ability to troubleshoot and think out of box
- Investigative mind, paying attention to small details and good observer
- Strong analytical skills, able to identify discrepancies during the test process
- Excellent time-management skills and commitment to meet deadlines
- Good communication skills and a team player
- Singaporeans/ PRs are welcome to apply

Test Automation Engineer

JOB DESCRIPTION

- Design automated test scenarios accordingly to test plan with its acceptance criteria
- Manage end to end automated test framework such as prepare test environment, test data, update functional testing scripts in accordance with changing requirements
- Work with QC project leader to identify, propose, prioritize and implement test automations.
- Work with test engineers to troubleshoot observed issue, determine root cause and identify solutions
- Contribute towards continuous improvement of testing practices/tools to improve test strategy and efficiency
- Help to ensure every release happens in a timely manner with a high level of quality.

JOB REQUIREMENTS

- Bachelor's/ Master's degree in Computer Science, Software Engineering or other equivalent degree/experience
- Strong python scripting skill and C++ programming background
- Good written and verbal communication skills in Chinese would be advantage
- Able to work well both independently as well as in a team

To apply: Please send an email titled "Realtek Singapore job application 2021" to Realtek_HR@realtek-sg.com with your Resume, CV and state the position(s) you're interested in.

Software Engineer – IoT Development

JOB DESCRIPTION

- Embedded system software development
- IoT related protocol and software development
- Peripheral device driver and Wi-Fi related software development
- Customer issues support

JOB REQUIREMENTS

- Bachelor's degree or above in Computer Engineering, Electronics or Networking engineering
- Familiar with C/C++
- Familiar with embedded system and experience in driver-related development is an advantage
- Peripheral and Wi-Fi-related experience is an advantage
- Good communication and team working skills
- Singaporeans are welcome to apply

Software Engineer – IoT Development & Promotion

JOB DESCRIPTION

- Embedded system software development
- IoT related protocol, software and innovative project development
- Peripheral device driver, Bluetooth and Wi-Fi related software development
- Publicity and promotion for Realtek IoT solution on social media, public workshop etc.
- Customer issues support

JOB REQUIREMENTS

- Bachelor's degree or above in Computer Engineering, Electronics or Networking engineering, or related majors
- Familiar with C/C++ or other languages
- Familiar with embedded system and experience in driver-related, peripheral or Wi-Fi related development is an advantage
- Able to follow up the latest software and electronics technology trend
- Good communication and team working skills
- Singaporeans are welcome to apply

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Software Engineer – WiFi/ Embedded System Development

JOB DESCRIPTION

- Wireless AP software development and firmware development
- Network communication protocol development
- Embedded system development
- Perform different types of testing on networking devices
- Execution of pre-test for IoT/ Wi-Fi router related certification

JOB REQUIREMENTS

- Bachelor's Degree or above in Computer Science, Computer Engineering, Electronic, Telecommunication or Networking Engineering
- Familiar with C/C++ and Linux system
- Familiar with networking and embedded system development
- WiFi development experience is an advantage
- Good communication and team working skills
- Good problem solver, ability to think out of the box
- Passion in programming is an advantage
- Entry/ Senior levels are all welcome to apply

Application Engineer – IoT Development

JOB DESCRIPTION

- Customer project tracking
- Customer issue reproduce and solve
- IoT firmware debug and coding
- Performance tuning
- Image quality fine tune based on customer's requirement

JOB REQUIREMENTS

- Bachelor's degree in Computer Science, Computer Engineering, Electrical Engineering
- Familiar with C/C++ programming
- Excellent problem solving and debugging skills
- Good communication skills
- Singaporeans are welcome to apply

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Digital IC Design Engineer

JOB DESCRIPTION

- Work on SoC architecture discussion and optimization with architect; SoC integration; system block development, e.g., power management, clock/reset, system register, test control, PinMux, etc;
- Work on SoC floorplan with architect and APR; SoC power check; SoC timing constraints and review support; SoC DFT support; SoC verification plan and SoC verification support

JOB REQUIREMENTS

- Bachelor's or Master's Degree in Electronic Engineering with ASIC design experience.
- Familiar with ASIC design flow
- Experience in RTL coding, RTL and gate-level simulation, logic synthesis, static timing analysis, timing closure and verification
- Good understanding of DFT
- Familiar with UNIX/ Linux environment and scripting
- Good communication and interpersonal skills
- Strong analytical and problem-solving skills
- Singaporeans and entry levels are welcome to apply

System Architecture Engineer

JOB DESCRIPTION

- Analyze simulation statistics for architectural design alternatives exploration and performance trade-offs
- Define SoC specification and functional task partition including operation of HW/FW/Driver
- Optimize functional performance and power efficiency across HW/FW/Driver

JOB REQUIREMENTS

- Bachelor's Degree or above in Computer or Electronics Engineering with one or more of following experience and knowledge
- Experience with microprocessor, interconnect/memory, networking, and video architecture
- Experience with hardware design and verification for ASIC/FPGA using C/C++/Verilog/Perl
- Willing to learn new things and self-motivated
- Singaporeans are welcome to apply

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Machine Learning and System Architecture Engineer

JOB DESCRIPTION

- Algorithm development and validation and SoC architecture definition
- Development of software framework and firmware for algorithm integration into SoC
- Algorithm integration into SoC and testing, and performance optimization

JOB REQUIREMENTS

- Bachelor's Degree or above in Computer or Electronics Engineering with one or more of following experience and knowledge
- Willing to learn new things and self-motivated
- Good knowledge and experience with image processing, computer vision and machine learning
- Proficient in C/C++/ Python programming skills
- Experience in OpenCV/ TensorFlow/ Caffe machine learning framework
- Experience in algorithms such as object detection, face recognition, video analytics, deep learning CNN and RNN
- Singaporeans are welcome to apply

System Architecture Engineer (BSP)

JOB DESCRIPTION

- Develop Board Support Package (BSP) software, initialization and start-up routines and Hardware Abstraction Layer (HAL) software
- Develop specification for System-on-Chip (SoC) peripherals and their driver code and test specification
- Board bring up, functional test and debug

JOB REQUIREMENTS

- Bachelor's Degree or above in Computer or Electronics Engineering with one or more of following experience and knowledge
- Experience with embedded software development and RTOS
- Experience with ARM Architecture, SoC peripherals, connectivity (WLAN/ BT) and power management
- Willing to learn new things and self-motivated
- Singaporeans are welcome to apply

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System Architecture Engineer (SoC)

JOB DESCRIPTION

- System architecture definition and system test specification and implementation
- Power management and sequence, reset and clock sequence and system boot up
- Pre-silicon power estimation and post-silicon power characterization
- Pre-silicon validation with emulation/FPGA and post-silicon bring up, functional test and debug

JOB REQUIREMENTS

- Bachelor's Degree or above in Computer or Electronics Engineering with one or more of following experience and knowledge
- Experience with system architecture and SoC integration and power management
- Experience with board support package (BSP) and CPU peripherals
- Willing to learn new things and self-motivated
- Singaporeans are welcome to apply

Physical Design Engineer

JOB DESCRIPTION

To provide back-end design support to the various business units, taking care of System-on-Chip top-level floorplanning, partitioning and timing budgeting, power structure, place and route, clock tree synthesis, timing closure, power/signal integrity signoff, physical verification (DRC/LVS/Antenna).

In order to ensure successful tapeout, active participation in working with frontend and integration team is required.

JOB REQUIREMENTS

- Bachelor's/ Master's in Electrical Engineering with hands-on experience in back-end physical design and verification
- Familiar with hierarchical physical design strategies, methodologies and deep sub-micron technology issues
- Experience in top-level back-end physical design, mainly top-level floorplanning and partitioning, congestion analysis, placement optimization, clock-tree synthesis and timing closure
- Proficient in static timing analysis (PrimeTime)
- Proficient in programming/scripting with good coding experience in Tcl/Perl/Python.
- Possesses good working ethic, good verbal and written communication skills
- Familiar with tools such as Synopsys IC Compiler/Cadence Encounter and Primetime
- Good knowledge on EM/IR-Drop/crosstalk analysis (PTSI, Redhawk), formal or physical verification (Calibre) will be an advantage
- Singaporeans are welcome to apply

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Design Verification Engineer

JOB DESCRIPTION

In this position, the individual thoroughly understands digital design specs of various IP blocks and SoC architecture definition

• Develop detailed module level and SoC level testplans for all the functional features, based on the design spec.

• Develop ASIC verification environment including all the respective components such as stimulus, checkers, assertions, monitors and scoreboards.

• Develop directed and constrain-random verification functional tests and simulate using EDA tools to verify functional spec is working

• Execute verification plans, including design bring-up, DV bring-up, regression enabling for all the features.

• Collaborate with digital design team to debug functional testcases and deliver functionally correct designs

The ideal individual must have proven ability to achieve results in a fast moving, dynamic environment

JOB REQUIREMENTS

- BSEE or MSEE, entry level
- Strong communication, analytical and documentation skills and ability to interface with other groups
- Strong VLSI functional verification experience, preferably with exposure to complex, high speed custom VLSI products
- Strong hardware functional verification language and Object-oriental language development skill. Prefer with SystemVerilog/ UVM experience
- Familiar with ASIC verification methodology, tools, and development flow
- Working experience or familiar with Ethernet L2/L3 switch/router, SOC, AMBA bus, high-speed IO (USB-2/3, PCIe gen2/3, SATA), Flash controller, or CPU peripherals
- Singaporeans are welcome to apply

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