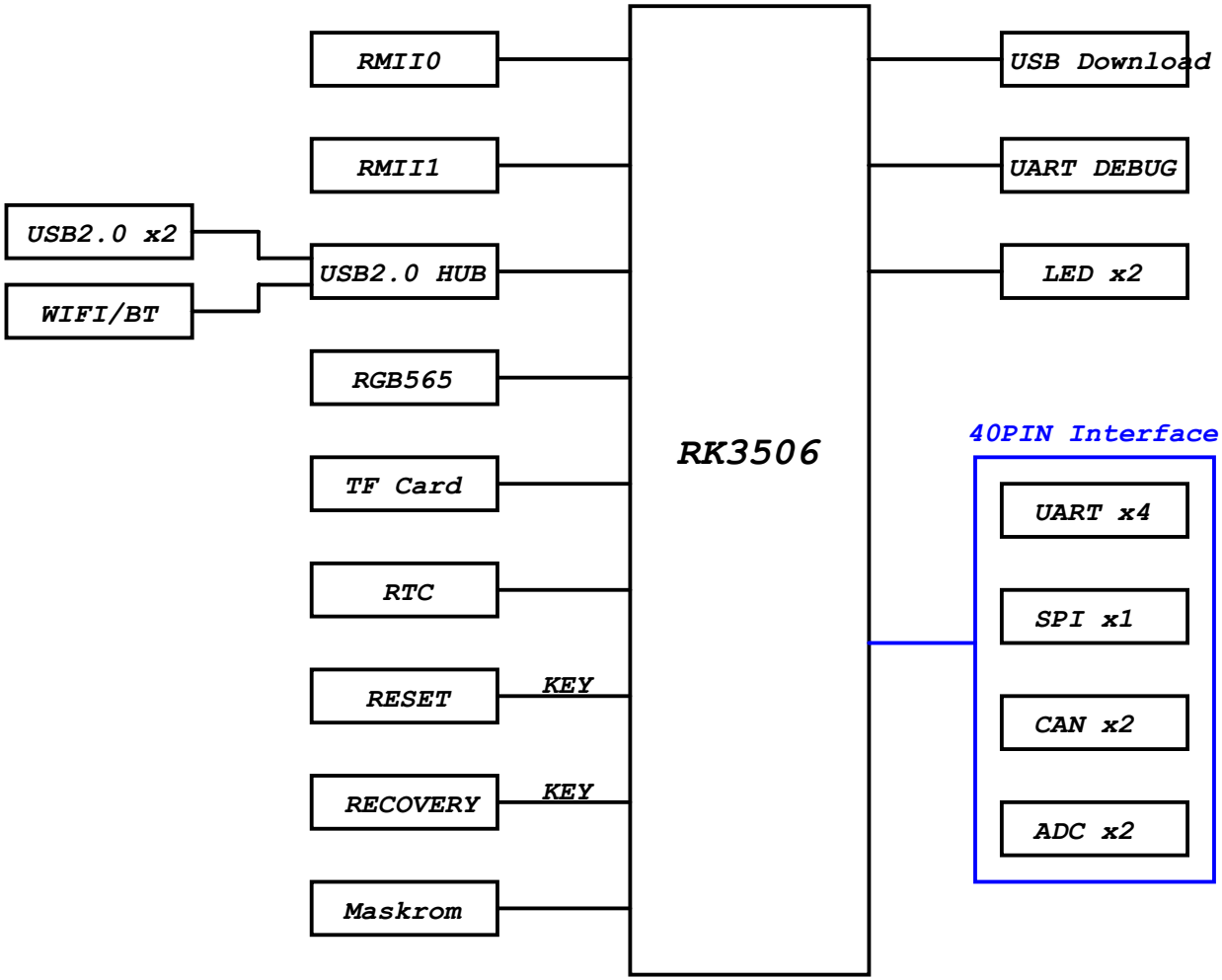

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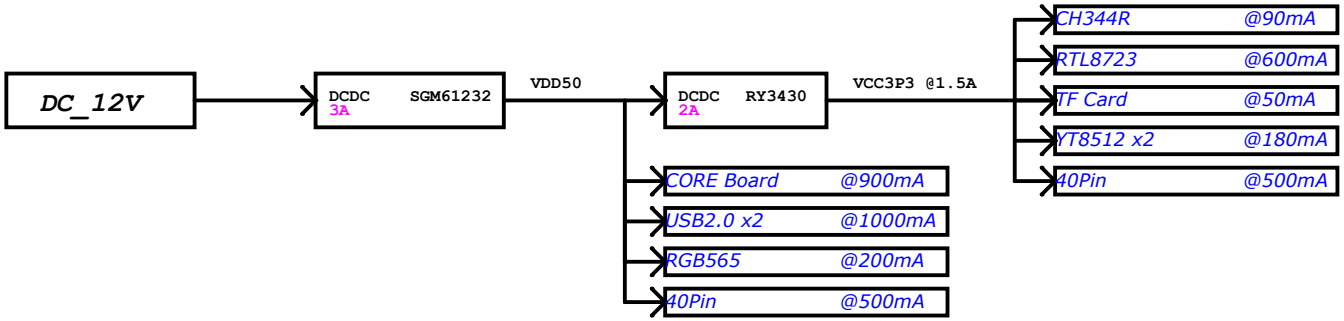

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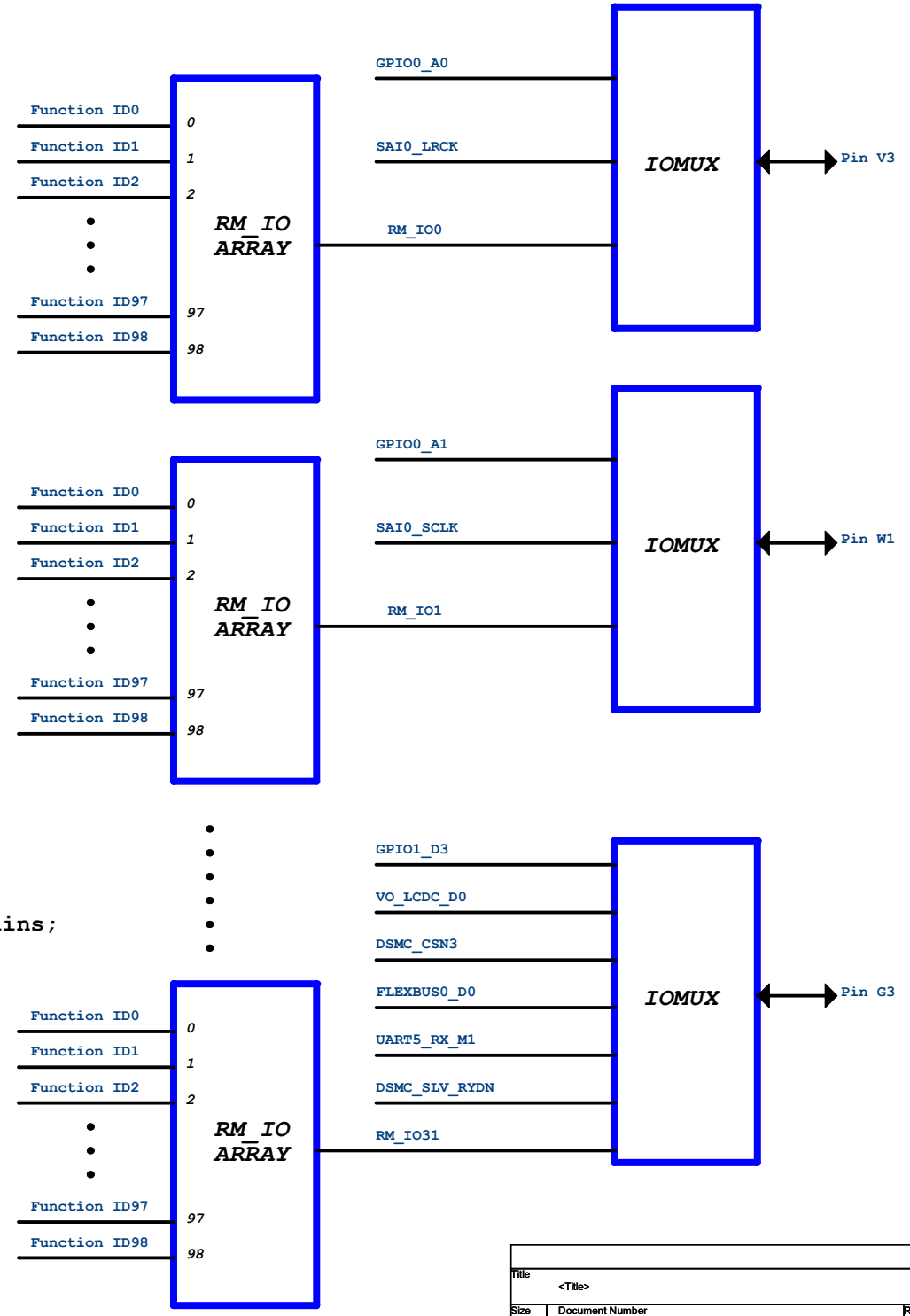
RM_IO List of optional function ID0~98

ID	function	ID	function	ID	function	ID	function
0	NC	26	CAN1_TX	51	SAI0_MCLK	77	TSADC_CTRL
1	UART1_TX	27	CAN1_RX	52	SAI0_SCLK	78	PWR_CTRL0(PMU_SLEEP)
2	UART1_RX	28	CAN0_TX	53	SAI0_LRCK	79	PWR_CTRL1(CORE_POWER_OFF)
3	UART2_TX	29	CAN0_RX	54	SAI0_SDI0	80	SPDIF_TX
4	UART2_RX	30	PWM0_CH0	55	SAI0_SDI1	81	SPDIF_RX
5	UART3_TX	31	PWM0_CH1	56	SAI0_SDI2	82	BIPCNTR_A0
6	UART3_RX	32	PWM0_CH2	57	SAI0_SDI3	83	BIPCNTR_A1
7	UART3_CTSN	33	PWM0_CH3	58	SAI0_SDO	84	BIPCNTR_A2
8	UART3_RTSN	34	PWM1_CH0	59	SAI1_MCLK	85	BIPCNTR_A3
9	UART4_TX	35	PWM1_CH1	60	SAI1_SCLK	86	BIPCNTR_A4
10	UART4_RX	36	PWM1_CH2	61	SAI1_LRCK	87	BIPCNTR_A5
11	UART4_CTSN	37	PWM1_CH3	62	SAI1_SDI	88	BIPCNTR_B0
12	UART4_RTSN	38	PWM1_CH4	63	SAI1_SDO0	89	BIPCNTR_B1
13	MIPI_DPHY_DSI_TX_TE	39	PWM1_CH5	64	SAI1_SDO1	90	BIPCNTR_B2
14	CLK_32K	40	PWM1_CH6	65	SAI1_SDO2	91	BIPCNTR_B3
15	I2C0_SCL	41	PWM1_CH7	66	SAI1_SDO3	92	BIPCNTR_B4
16	I2C0_SDA	42	TOUCH_KEYDRIVE	67	SPI0_CLK	93	BIPCNTR_B5
17	I2C1_SCL	43	TOUCH_KEY0	68	SPI0_MOSI	94	PDM_CLK1
18	I2C1_SDA	44	TOUCH_KEY1	69	SPI0_MISO	95	ETH_RMII0_PPSCLK
19	I2C2_SCL	45	TOUCH_KEY2	70	SPI0_CSN0	96	ETH_RMII0_PPSTRIG
20	I2C2_SDA	46	TOUCH_KEY3	71	SPI0_CSN1	97	ETH_RMII1_PPSCLK
21	PDM_CLK0	47	TOUCH_KEY4	72	SPI1_CLK	98	ETH_RMII1_PPSTRIG
22	PDM_SDI0	48	TOUCH_KEY5	73	SPI1_MOSI		
23	PDM_SDI1	49	TOUCH_KEY6	74	SPI1_MISO		
24	PDM_SDI2	50	TOUCH_KEY7	75	SPI1_CSN0		
25	PDM_SDI3			76	SPI1_CSN1		

NOTE:

(1)RM_IOX(X=0~31) can select functions from function ID0~ID98;
(2)RM_IOX(X=0~31) cannot repeatedly select the function ID0~ID98;
(3)SAI、PDM、SPI signals should not cross the PMUIO and VCCIO domains;
(4)The signal of the same function in RM_IO is used across PMUIO and VCCIO domains, and the operating level needs to be consistent



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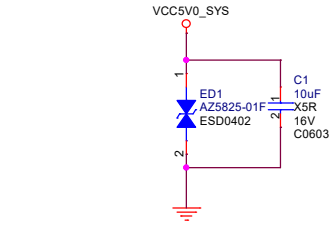

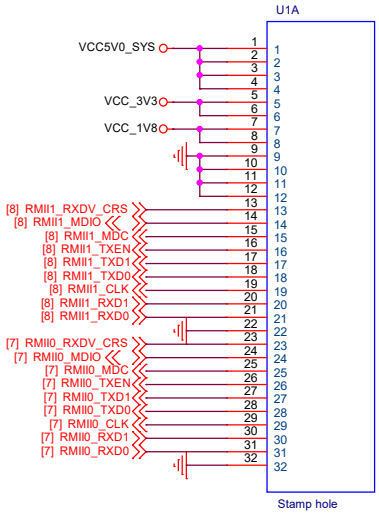

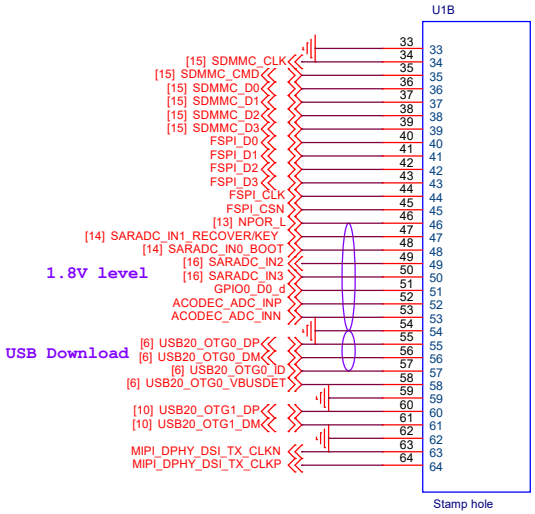

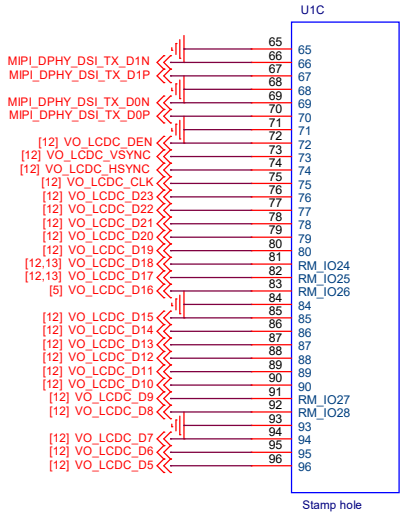

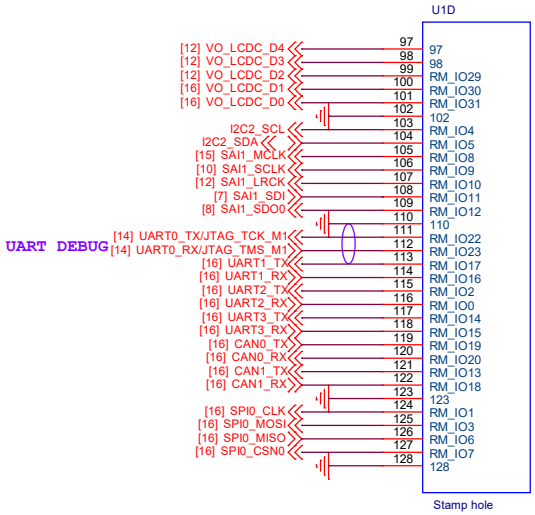

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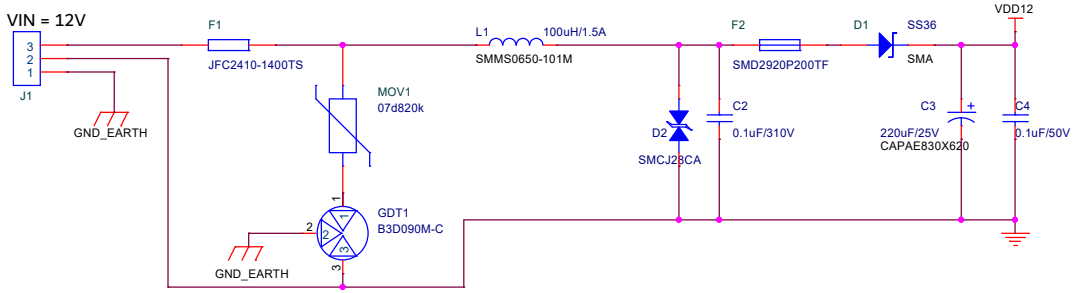
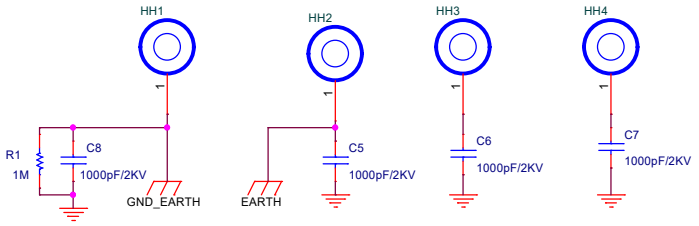
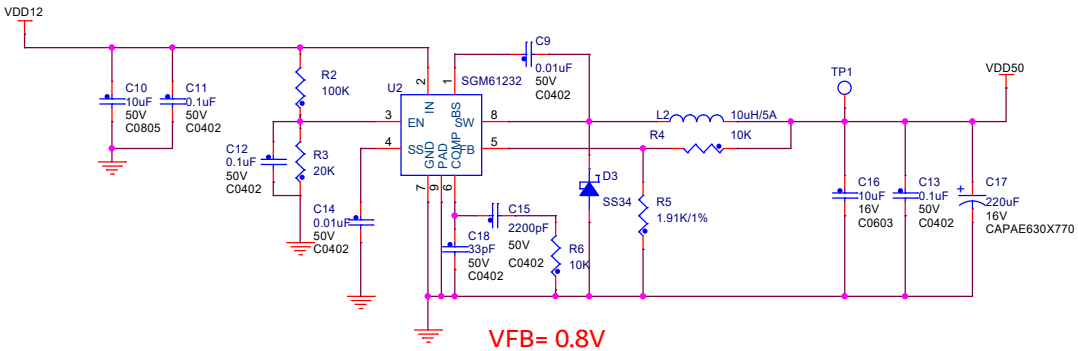


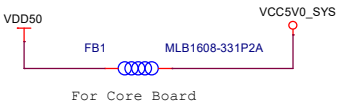


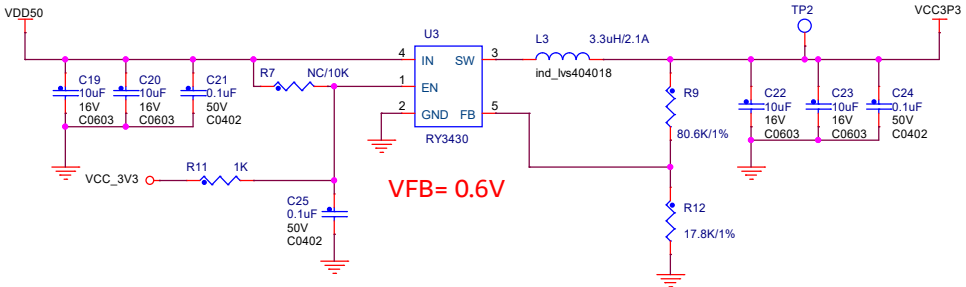


VFB= 0.8V

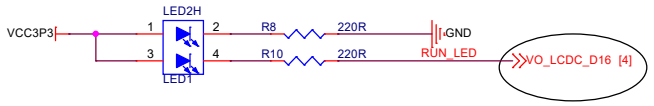
说明：
1) VIN = 3.5 ~ 28V
2) IOUT = 3A
3) VOUT =5V

4) EN=1.25V





VFB= 0.6V



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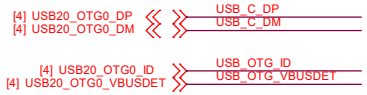

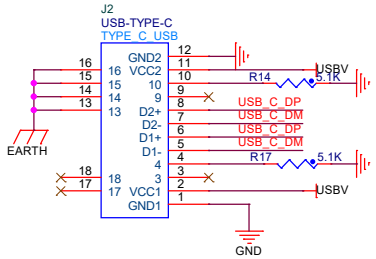

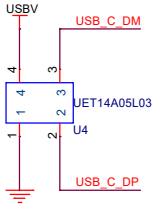

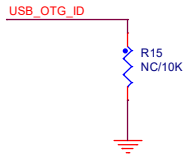

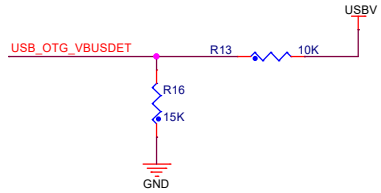

USB烧录口
强烈建议不要用作其他用途

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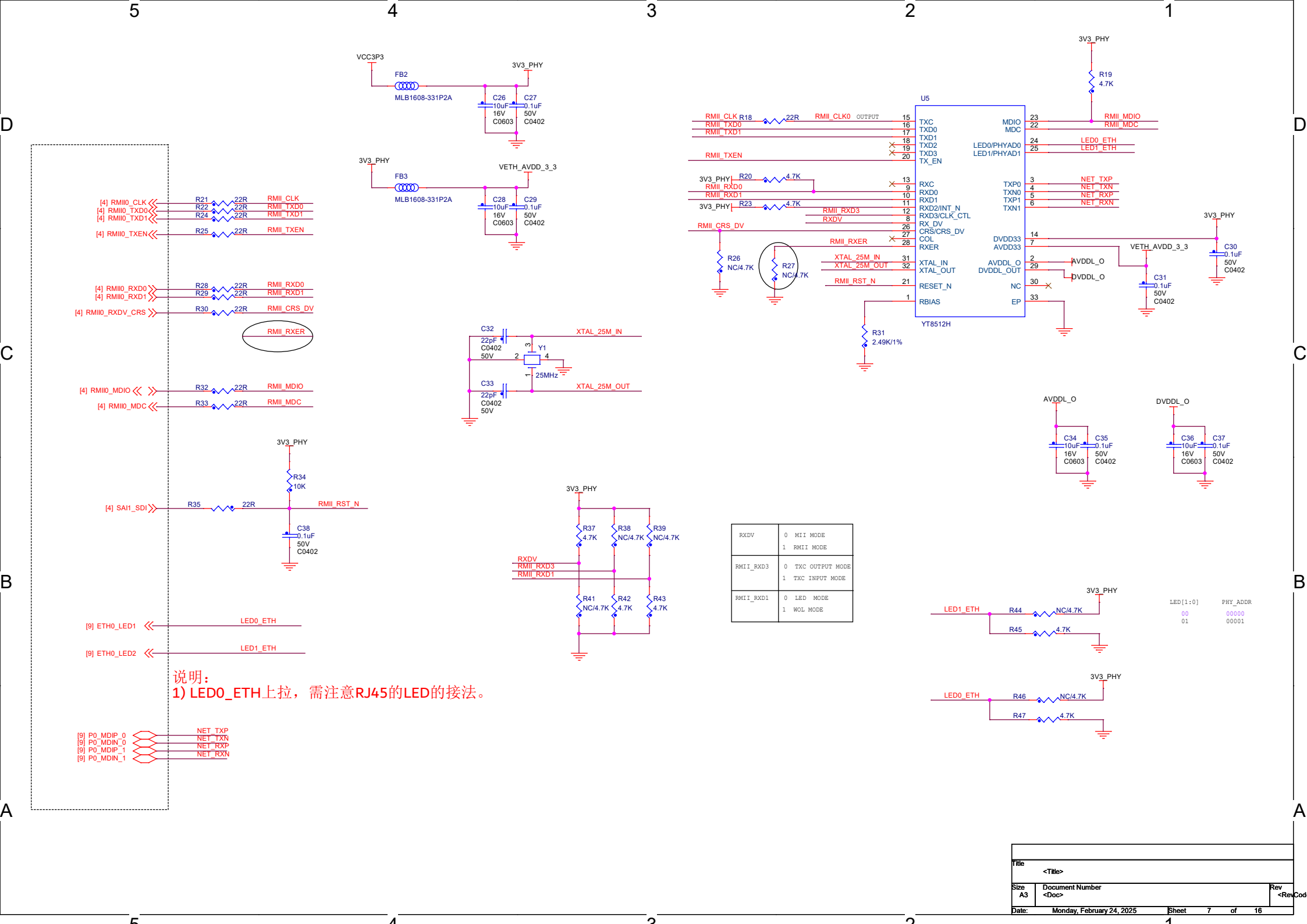
VCC3P3
FB2
MLB1608-331P2A
3V3_PHY
C26 10uF 16V C0603
C27 0.1uF 50V C0402
FB3
VETH_AVDD_3_3
C28 10uF 16V C0603
C29 0.1uF 50V C0402
[4] RMII0_CLK
[4] RMII0_TXD0
[4] RMII0_TXD1
[4] RMII0_TXEN
R21 22R RMII_CLK
R22 22R RMII_TXD0
R24 22R RMII_TXD1
R25 22R RMII_TXEN
[4] RMII0_RXD0
[4] RMII0_RXD1
[4] RMII0_RXDV_CRS
R28 22R RMII_RXD0
R29 22R RMII_RXD1
R30 22R RMII_CRS_DV
RMII_RXER
[4] RMII0_MDIO
[4] RMII0_MDC
R32 22R RMII_MDIO
R33 22R RMII_MDC
C32 22pF C0402 50V
C33 22pF C0402 50V
Y1 25MHz
XTAL_25M_IN
XTAL_25M_OUT
3V3_PHY
R34 10K
[4] SAI1_SDI
R35 22R RMII_RST_N
C38 0.1uF 50V C0402
R37 4.7K
R38 NC/4.7K
R39 NC/4.7K
R41 NC/4.7K
R42 4.7K
R43 4.7K
RXDV
RMII_RXD3
RMII_RXD1
[9] ETH0_LED1
[9] ETH0_LED2
LED0_ETH
LED1_ETH
[9] P0_MDIP_0
[9] P0_MDIN_0
[9] P0_MDIP_1
[9] P0_MDIN_1
NET_TXP
NET_TXN
NET_RXP
NET_RXN
U5
YT8512H
R18 22R RMII_CLK0 OUTPUT
RMII_CLK
RMII_TXD0
RMII_TXD1
RMII_TXEN
R20 4.7K
R23 4.7K
RMII_RXD0
RMII_RXD1
RMII_RXD3
RXDV
RMII_CRS_DV
RMII_RXER
R26 NC/4.7K
R27 NC/4.7K
XTAL_25M_IN
XTAL_25M_OUT
RMII_RST_N
R31 2.49K/1%
TXC
TXD0
TXD1
TXD2
TXD3
TX_EN
RXC
RXD0
RXD1
RXD2/INT_N
RXD3/CLK_CTL
RX_DV
CRS/CRS_DV
COL
RXER
XTAL_IN
XTAL_OUT
RESET_N
RBIAS
MDIO
MDC
LED0/PHYAD0
LED1/PHYAD1
TXP0
TXN0
TXP1
TXN1
DVDD33
AVDD33
AVDDL_O
DVDDL_OUT
NC
EP
R19 4.7K
RMII_MDIO
RMII_MDC
LED0_ETH
LED1_ETH
NET_TXP
NET_TXN
NET_RXP
NET_RXN
AVDDL_O
DVDDL_O
VETH_AVDD_3_3
C30 0.1uF 50V C0402
C31 0.1uF 50V C0402
C34 10uF 16V C0603
C35 0.1uF 50V C0402
C36 10uF 16V C0603
C37 0.1uF 50V C0402
RXDV 0 MII MODE 1 RMII MODE
RMII_RXD3 0 TXC OUTPUT MODE 1 TXC INPUT MODE
RMII_RXD1 0 LED MODE 1 WOL MODE
LED[1:0] PHY_ADDR
00 00000
01 00001
LED1_ETH R44 NC/4.7K R45 4.7K
LED0_ETH R46 NC/4.7K R47 4.7K
说明：
1) LED0_ETH上拉，需注意RJ45的LED的接法。
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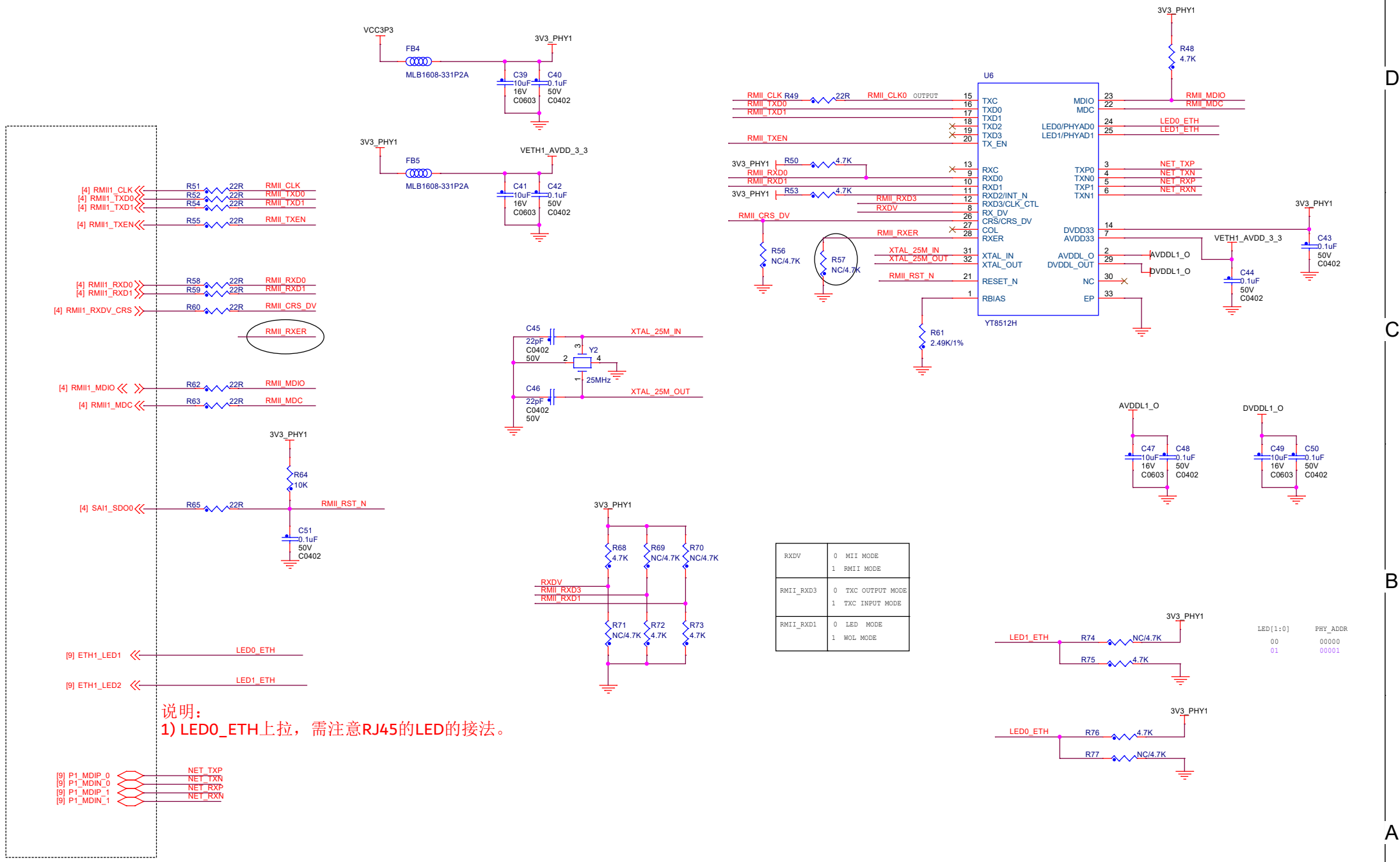

说明：

1) LED0_ETH上拉，需注意RJ45的LED的接法。

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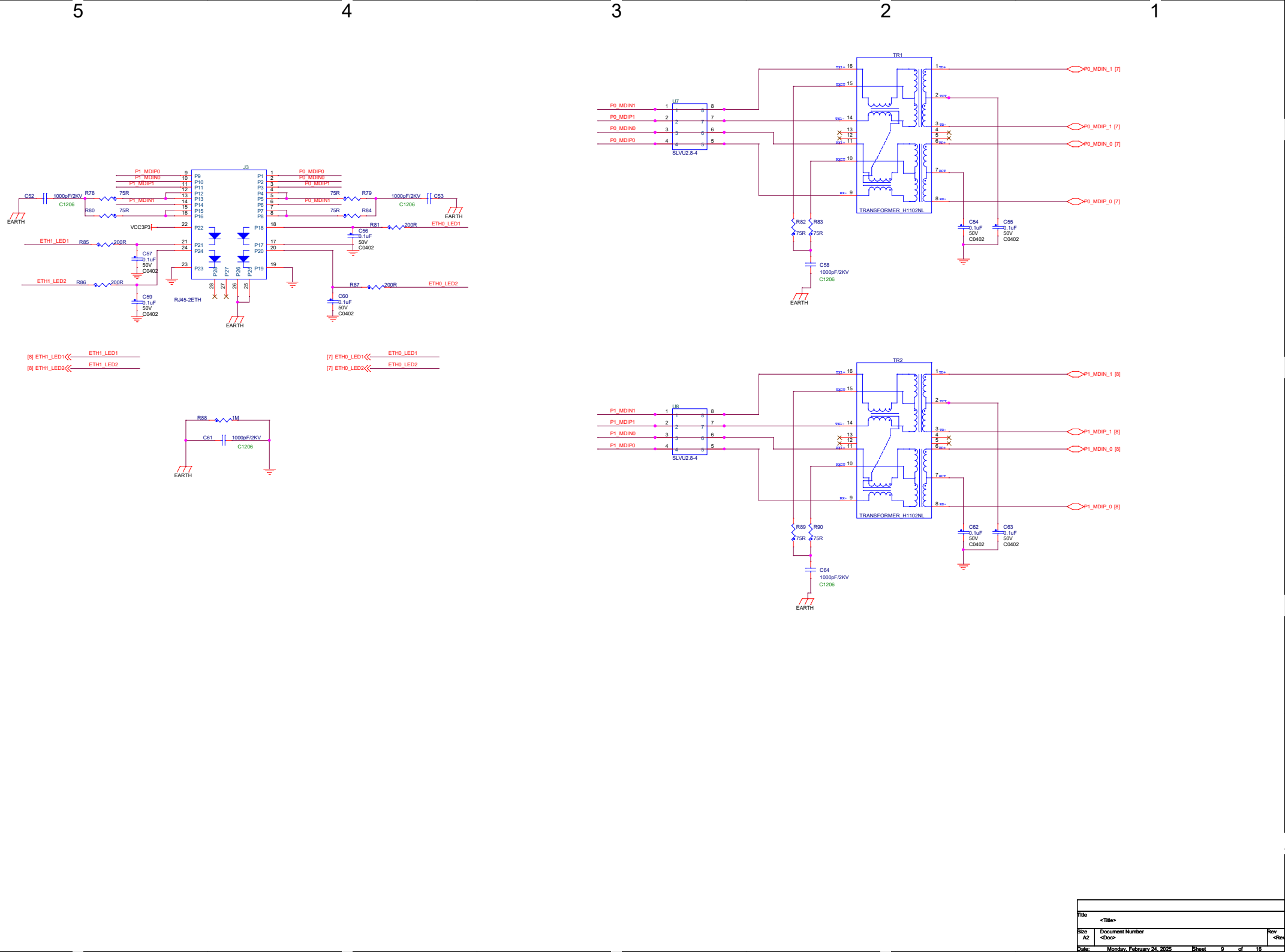
5
4
3
2
1
D
C
B
A
J3
P1_MDIP0
P1_MDIN0
P1_MDIP1
P1_MDIN1
P0_MDIP0
P0_MDIN0
P0_MDIP1
P0_MDIN1
C52
1000pF/2KV
C1206
R78
75R
R80
75R
R79
75R
R84
75R
C53
1000pF/2KV
C1206
EARTH
VCC3P3
R81
200R
ETH0_LED1
ETH1_LED1
R85
200R
C56
0.1uF
50V
C0402
C57
0.1uF
50V
C0402
ETH1_LED2
R86
200R
R87
200R
ETH0_LED2
C59
0.1uF
50V
C0402
C60
0.1uF
50V
C0402
RJ45-2ETH
[8] ETH1_LED1
[8] ETH1_LED2
ETH1_LED1
ETH1_LED2
[7] ETH0_LED1
[7] ETH0_LED2
ETH0_LED1
ETH0_LED2
R88
1M
C61
1000pF/2KV
C1206
U7
P0_MDIN1
P0_MDIP1
P0_MDIN0
P0_MDIP0
SLVU2.8-4
TR1
TRANSFORMER_H1102NL
P0_MDIN_1 [7]
P0_MDIP_1 [7]
P0_MDIN_0 [7]
P0_MDIP_0 [7]
R82
75R
R83
75R
C58
1000pF/2KV
C1206
C54
0.1uF
50V
C0402
C55
0.1uF
50V
C0402
U8
P1_MDIN1
P1_MDIP1
P1_MDIN0
P1_MDIP0
SLVU2.8-4
TR2
TRANSFORMER_H1102NL
P1_MDIN_1 [8]
P1_MDIP_1 [8]
P1_MDIN_0 [8]
P1_MDIP_0 [8]
R89
75R
R90
75R
C64
1000pF/2KV
C1206
C62
0.1uF
50V
C0402
C63
0.1uF
50V
C0402
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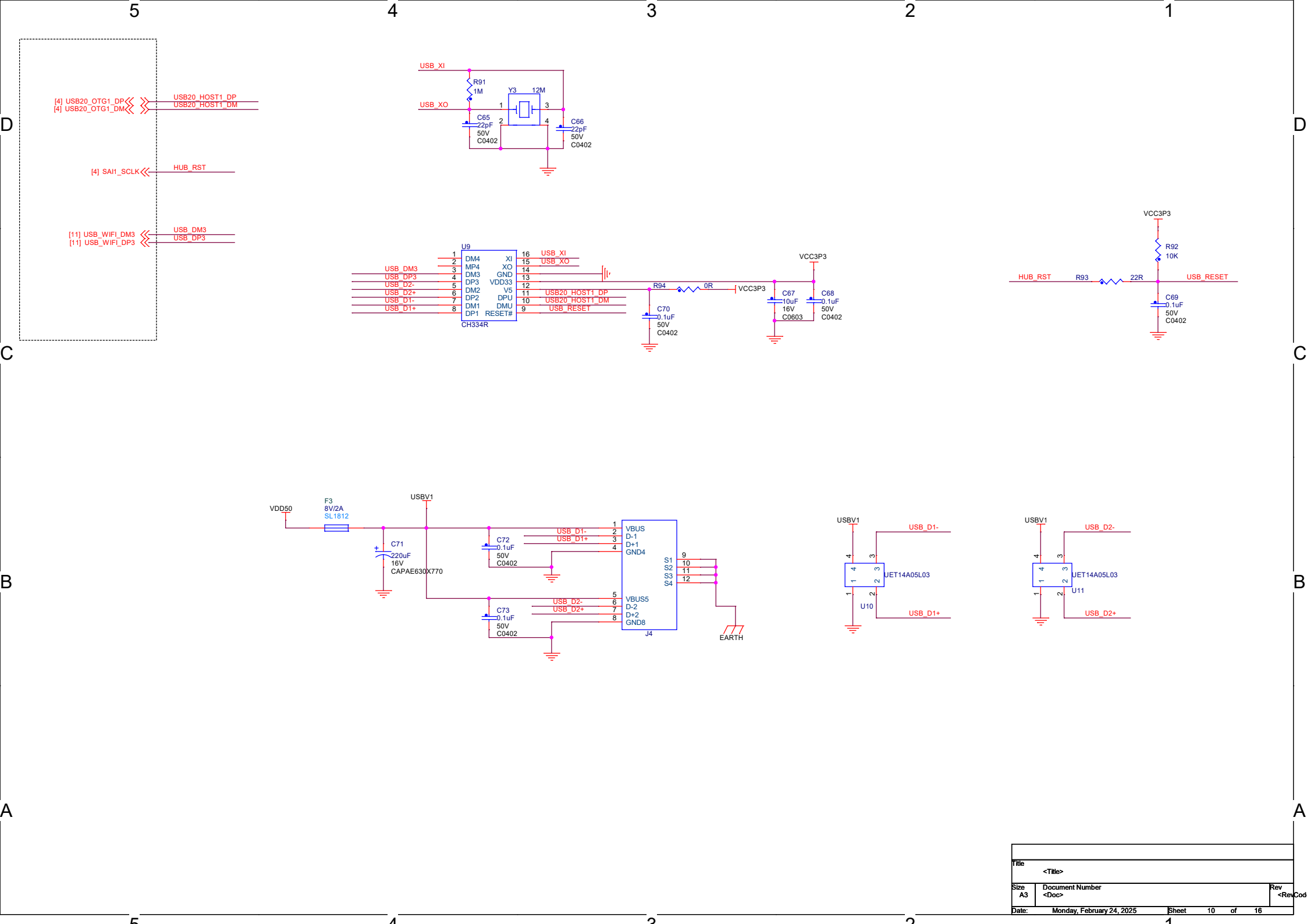
5
4
3
2
1
D
C
B
A
[4] USB20_OTG1_DP
[4] USB20_OTG1_DM
USB20_HOST1_DP
USB20_HOST1_DM
[4] SAI1_SCLK
HUB_RST
[11] USB_WIFI_DM3
[11] USB_WIFI_DP3
USB_DM3
USB_DP3
USB_XI
USB_XO
R91
1M
Y3
12M
C65
22pF
50V
C0402
C66
22pF
50V
C0402
U9
DM4
MP4
DM3
DP3
DM2
DP2
DM1
DP1
XI
XO
GND
VDD33
V5
DPU
DMU
RESET#
CH334R
USB_D2-
USB_D2+
USB_D1-
USB_D1+
USB20_HOST1_DP
USB20_HOST1_DM
USB_RESET
R94
0R
VCC3P3
C70
0.1uF
50V
C0402
C67
10uF
16V
C0603
C68
0.1uF
50V
C0402
R92
10K
R93
22R
C69
0.1uF
50V
C0402
VDD50
F3
8V/2A
SL1812
USBV1
C71
220uF
16V
CAPAE630X770
C72
0.1uF
50V
C0402
C73
0.1uF
50V
C0402
VBUS
D-1
D+1
GND4
VBUS5
D-2
D+2
GND8
S1
S2
S3
S4
J4
EARTH
U10
U11
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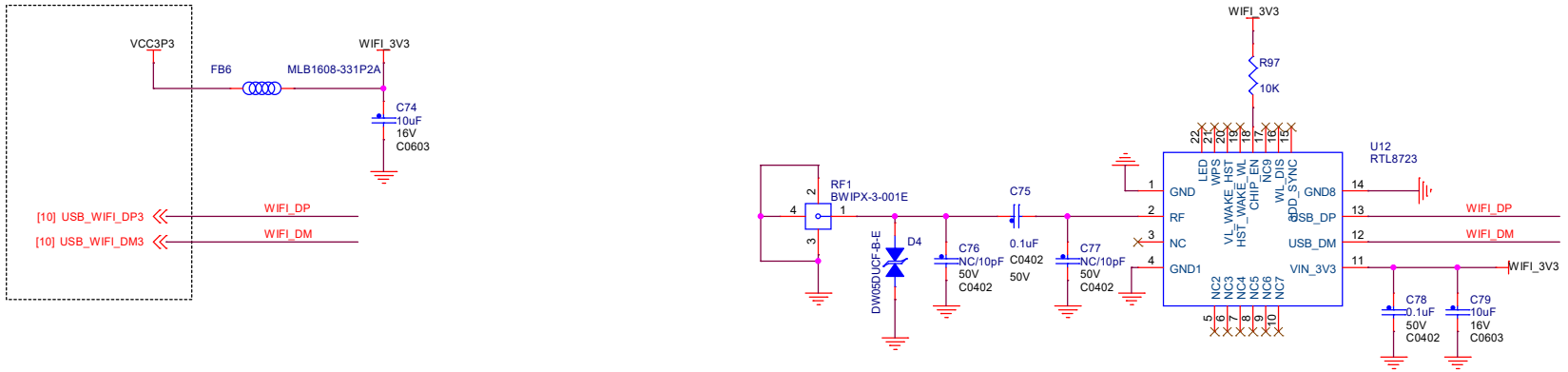

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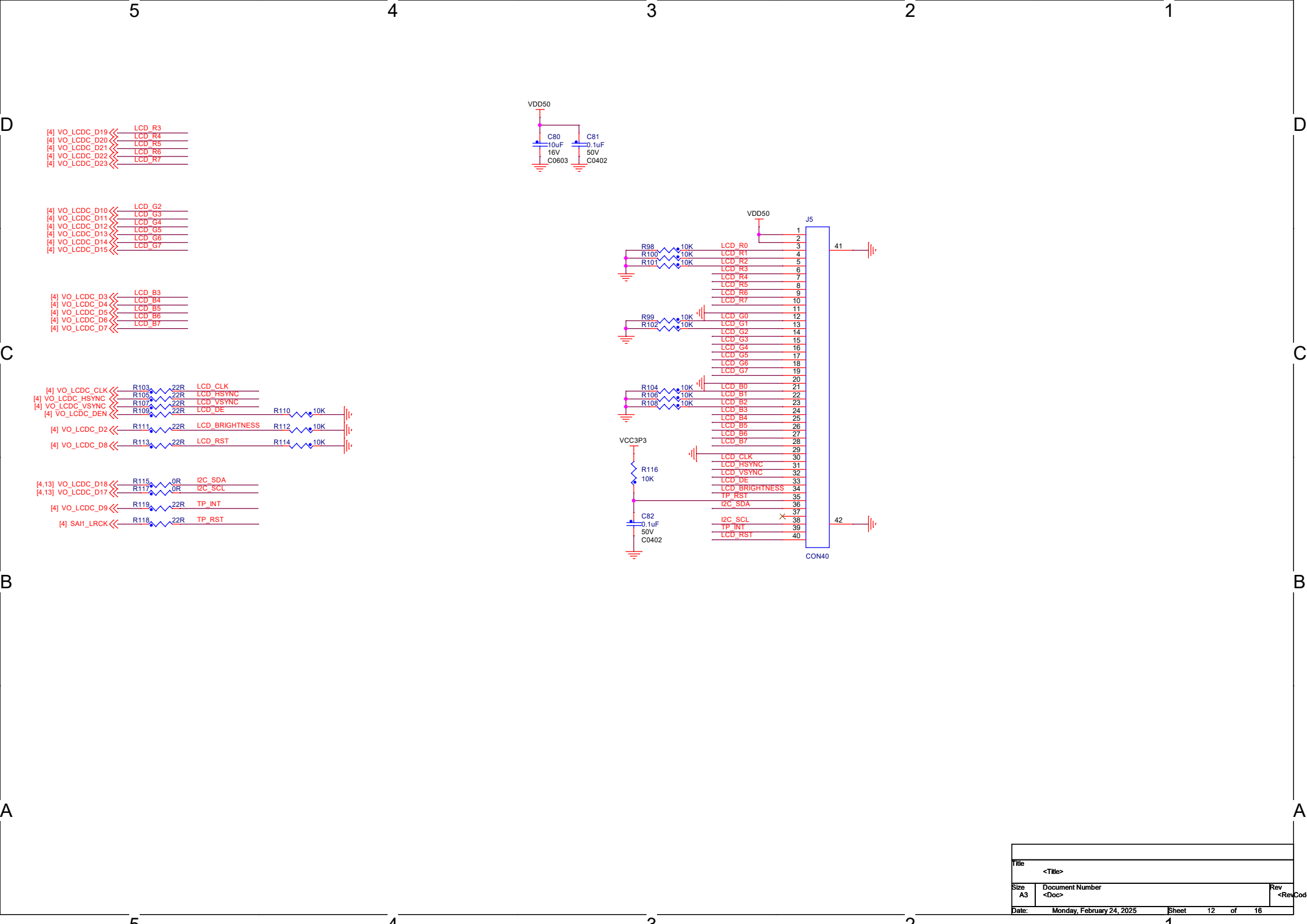
5
4
3
2
1
D
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B
A
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LCD_R3
LCD_R4
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LCD_B7
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[4] VO_LCDC_D2
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R103 22R LCD_CLK
R105 22R LCD_HSYNC
R107 22R LCD_VSYNC
R109 22R LCD_DE
R111 22R LCD_BRIGHTNESS
R113 22R LCD_RST
R110 10K
R112 10K
R114 10K
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R117 0R I2C_SCL
R119 22R TP_INT
R118 22R TP_RST
VDD50
C80 10uF 16V C0603
C81 0.1uF 50V C0402
VDD50
J5
R98 10K
R100 10K
R101 10K
R99 10K
R102 10K
R104 10K
R106 10K
R108 10K
LCD_R0
LCD_R1
LCD_R2
LCD_R3
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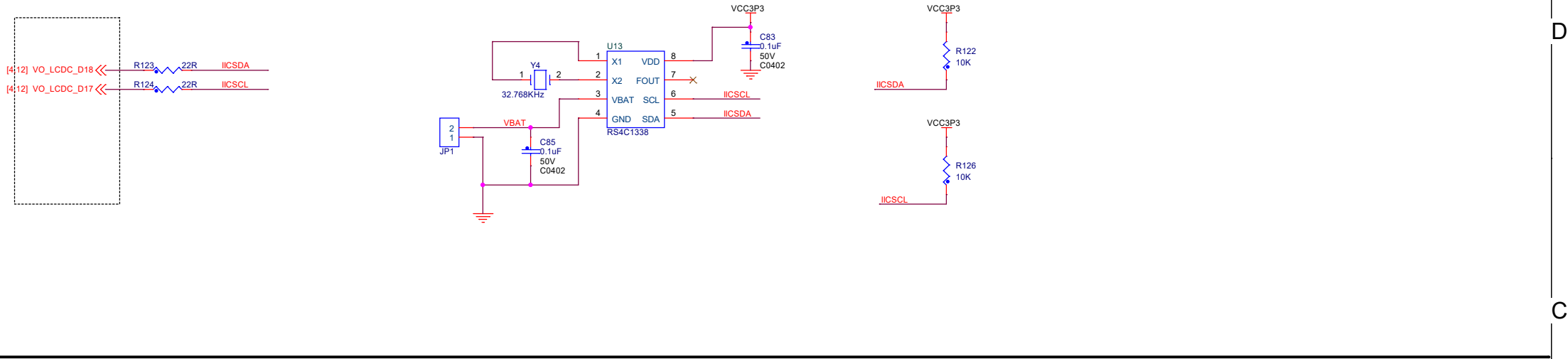

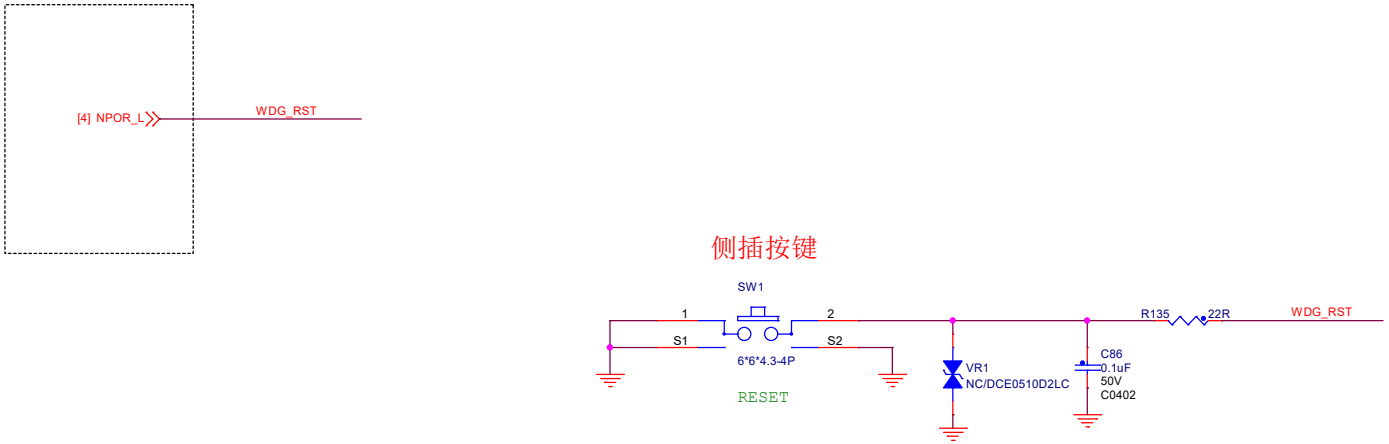

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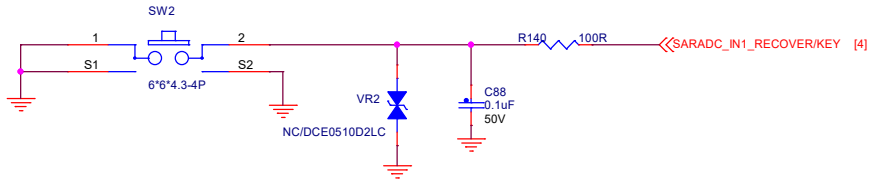
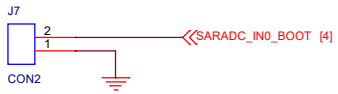
侧插按键

RECOVERY



MASKROM



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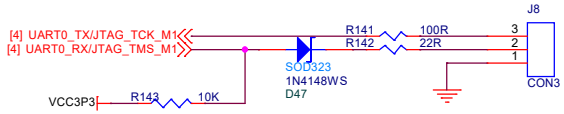

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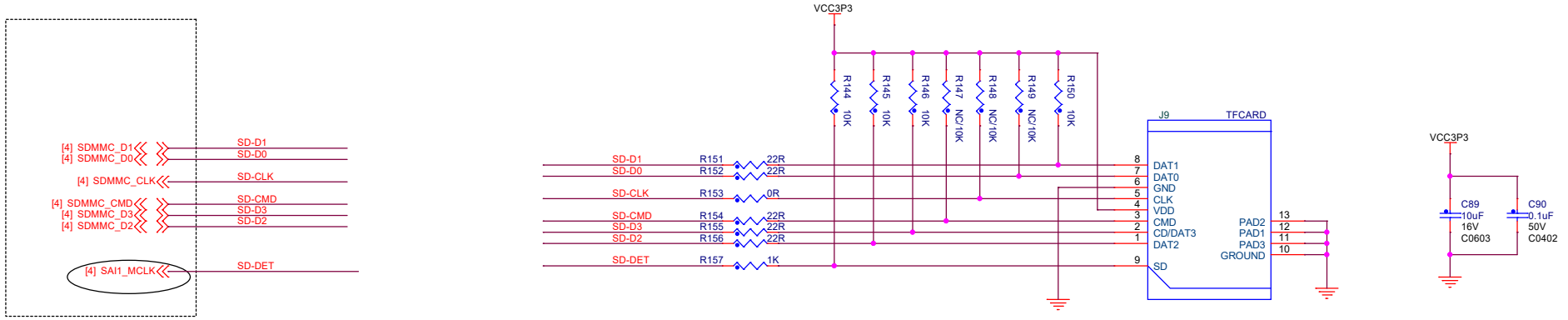
EMMC and TF Cards are optional



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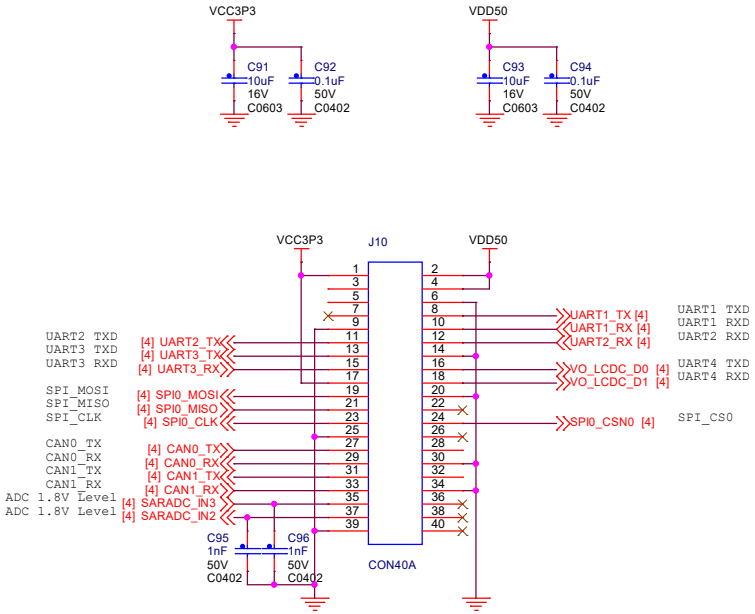

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