

Product Specification

Product Name: VGM128032A7W01

Product Code: M01000

| Customer | | | | |
|----------|-------|----------------------|--|--|
| | | | | |
| | | Approved by Customer | | |
| | | | | |
| | | | | |
| Approved | Date: | | | |

| | Designed Py | Checked By | Approved By | | |
|---|-------------|------------|-------------|------------|--|
| ١ | Designed By | Checked by | R&D | QA | |
| | 新车线 | 南方为水水山 | アルナルカンパカ | 20 11 11 b | |

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REVISION RECORD

| REV. | REVISION DESCRIPTION | REV. DATE | REMARK |
|------|----------------------|------------|--------|
| A01 | Initial Release | 2012-03-12 | |
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1 Overview

VGM128032A7W01 is a monochrome OLED display module with 128×32 dot matrix. The characteristics of this display module are high brightness, self-emission, high contrast ratio, slim/thin outline, wide viewing angle, wide temperature range, and low power consumption.

2 Features

Display Color: WhiteDot Matrix:128×32Driver IC: SH1106G

➤ Interface: I²C

 \triangleright Wide range of operating temperature: -40°C to 70°C

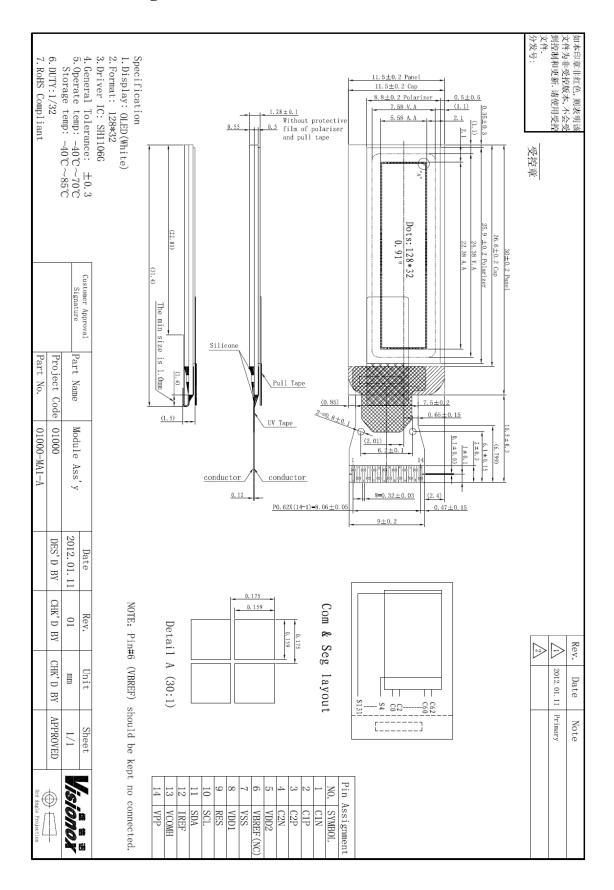
3 Mechanical Data

| NO. | ITEM | SPECIFICATION | UNIT |
|-----|-------------------|--------------------------|-----------------|
| 1 | Dot Matrix | 128(W)×32(H) | - |
| 2 | Dot Size | 0.159(W)×0.159(H) | mm ² |
| 3 | Dot Pitch | 0.175(W)×0.175(H) | mm ² |
| 4 | Aperture Rate | 82 | % |
| 5 | Active Area | 22.38 (W)×5.58 (H) | mm ² |
| 6 | Panel Size | 30(W)×11.5(H) ×1.05(T) | mm ³ |
| 7 | Module Size | 40.5(W)×11.5(H) ×1.28(T) | mm ³ |
| 8 | Diagonal A/A Size | 0.91 | inch |
| 9 | Module Weight | 0.88±10% | gram |

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4 Mechanical Drawing



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5 Module Interface

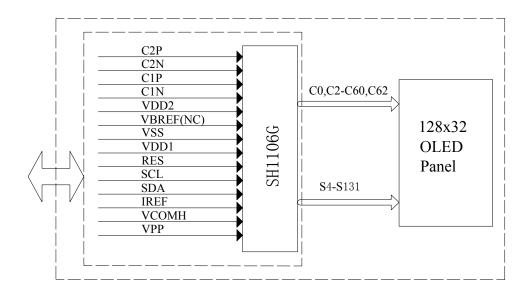
| PIN NO. | PIN NAME | DESCRIPTION | | | |
|---------|-----------|---|--|--|--|
| 1 | C1N | Connect to charge pump capacitor. | | | |
| 2 | C1P | These pins are not used and should be disconnedted when Vpp is supplied externa | | | |
| 3 | C2P | Connect to charge pump capacitor. | | | |
| 4 | C2N | These pins are not used and should be disconnedted when Vpp is supplied externally. | | | |
| 5 | VDD2 | 3.0 – 4.2V power supply pad for Power supply for charge pump circuit. This pin can be disconnected or connect to VDD1 when VPP is supplied externally. | | | |
| 6 | VBREF(NC) | No Connection. | | | |
| 7 | VSS | Ground. | | | |
| 8 | VDD1 | Power supply input: 1.65 - 3.5V | | | |
| 9 | RES | This is a reset signal input pad. When RES is set to "L", the settings are initialized. The reset operation is performed by the RES signal level. | | | |
| 10 | SCL | serial clock input. | | | |
| 11 | SDA | serial data input. | | | |
| 12 | IREF | This is a segment current reference pad. A resistor should be connected between this pad and VSS. Set the current at 10μ A. | | | |
| | | This is a pad for the voltage output high level for common signals. A capacitor should be connected between this pad and VSS. | | | |
| 14 | VPP | OLED panel power supply. Generated by internal charge pump. Connect to capacitor. It could be supplied externally. | | | |

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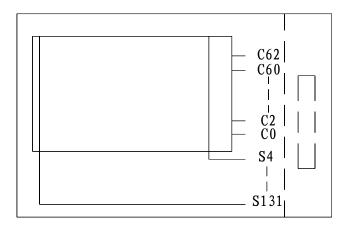


6 Function Block Diagram

6.1 Function Block Diagram



6.2 Panel Layout Diagram



Com & Seg layout

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7 Absolute Maximum Ratings

| ITEM | SYMBOL | MIN | MAX | UNIT | REMARK |
|------------------------|--------------------|------|------|------------|-------------------|
| Cumula Valta aa | V_{DD1} | -0.3 | 3.6 | V | IC maximum rating |
| Supply Voltage | V_{DD2} | -0.3 | 4.3 | V | IC maximum rating |
| OLED Operating Voltage | V_{PP} | 0 | 13.5 | V | IC maximum rating |
| Operating Temp. | Тор | -40 | 70 | $^{\circ}$ | - |
| Storage Temp | Tstg | -40 | 85 | $^{\circ}$ | - |

Note (1): All of the voltages are on the basis of "VSS = 0V".

Note (2): Permanent breakage of module may occur if the module is used beyond the maximum rating. The module can be normal operated under the conditions according to Section 8 "Electrical Characteristics".

Malfunctioning of the module may occur and the reliability of the module may deteriorate if the module is used beyond the conditions.

8 Electrical Characteristics

8.1 DC Electrical Characteristics

| ITEM | SYMBOL | TEST CONDITION | MIN | TYP | MAX | UNIT |
|---|--------------------|----------------------|----------------------|-----|----------------------|------|
| Logic Supply Voltage | V_{DD1} | 22±3°C, 55±15%R.H | 1.65 | 2.8 | 3.5 | V |
| OLED Driver Supply Voltage (Supplied Externally) | V_{PP} | 22±3°C, 55±15%R.H | 7.5 | 8.0 | 8.5 | V |
| Supply Voltage for DC/DC | V_{DD2} | 22±3°C, 55±15%R.H | 3.0 | - | 4.2 | V |
| OLED Driver Supply Voltage (Generated by Internal DC/DC) | V_{PP} | 22±3°C, 55±15%R.H | 7.5 | 8.0 | 8.5 | V |
| High-level Input Voltage | V_{IHC} | - | $0.8 \times V_{DD1}$ | ī | V_{DD1} | V |
| Low-level Input Voltage | $V_{\rm ILC}$ | - | V_{SS} | - | $0.2 \times V_{DD1}$ | V |
| High-level Output Voltage | V _{OHC} | - | $0.8 \times V_{DD1}$ | - | V_{DD1} | V |
| Low-level Output Voltage | V_{OLC} | - | V _{SS} | - 1 | $0.2 \times V_{DD1}$ | V |

Note: The V_{PP} input must be kept in a stable value; ripple and noise are not allowed.





8.2 Electro-optical Characteristics

| ITEM | SYMBOL | TEST CONDITION | MIN | TYP | MAX | UNIT |
|---|-------------|---|---------|------|-------|-------------------|
| Normal Mode Brightness (With Polarizer) | L_{br} | All pixels ON(1)(2) | 120 | 150 | - | cd/m ² |
| Normal Mode Power Consumption | Pt | All pixels ON(1) (V _{PP} generated by internal DC/DC) | ı | 81.4 | 98.05 | mW |
| Normal Mode Power Consumption | Pt | All pixels ON(2) (V _{PP} Supply Externally) | - | - | - | mW |
| Sleep mode current consumption in $V_{\rm DD1}$ & $V_{\rm DD2}$ | ī | During sleep, $TA = +25^{\circ}C$, VDD1 = 3V, $VDD2 = 3V$. | 1 | 1 | 5 | uA |
| Sleep mode current consumption in VPP | $ m I_{SP}$ | During sleep, $TA = +25^{\circ}C$, VPP = 9V (External) | - | - | 5 | uA |
| C.I.E(White) | (x) | x,y(CIE1931) | 0.26 | 0.30 | 0.34 | - |
| C.I.E(WIIIte) | (y) | | 0.29 | 0.33 | 0.37 | - |
| Dark Room Contrast | CR | - | ≥2000:1 | - | ı | ı |
| Response Time | - | - | - | 10 | - | μѕ |
| View Angle | - | - | ≥160 | - | - | Degree |

Note(1): Normal Mode test conditions are as follows:

- Driving voltage: VDD2:3.7V(VPP Generated by Internal DC/DC).

Contrast setting: 0x33Frame rate: 105HzDuty setting: 1/32

Note(2): Normal Mode test conditions are as follows:

- Driving voltage: 8.0V(VPP Supplied Externally).

Contrast setting: 0x33Frame rate: 105HzDuty setting: 1/32



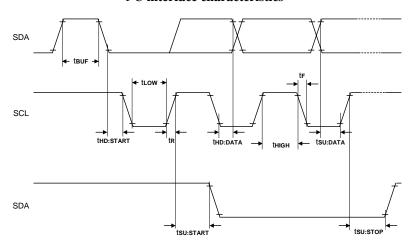
8.3 AC Electrical Characteristics

I²C Interface Timing Characteristics

 $(VDD - VSS = 1.65V \text{ to } 3.5V, TA = 25^{\circ}C)$

| Symbol | Parameter | Min. | Тур. | Max. | Unit | Condition |
|-----------|---|----------|------|------|------|-----------|
| fscl | SCL clock frequency | DC | - | 400 | kHz | |
| TLOW | SCL clock Low pulse width | 1.3 | - | - | uS | |
| Тнідн | SCL clock H pulse width | 0.6 | - | - | uS | |
| TSU:DATA | data setup time | 100 | - | - | nS | |
| THD:DATA | data hold time | 0 | - | 0.9 | uS | |
| Tr | SCL , SDA rise time | 20+0.1Cb | - | 300 | nS | |
| TF | SCL , SDA fall time | 20+0.1Cb | - | 300 | nS | |
| Cb | Capacity load on each bus line | - | - | 400 | pF | |
| Tsu:start | Setup timefor re-START | 0.6 | - | - | uS | |
| THD:START | START Hold time | 0.6 | - | - | uS | |
| Tsu:stop | Setup time for STOP | 0.6 | - | - | uS | |
| Твиғ | Bus free times between STOP and START condition | 1.3 | - | - | uS | |

I²C interface characteristics



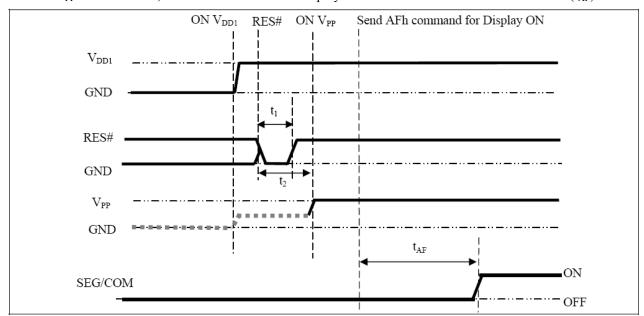


9 Functional Specification and Application Circuit

9.1 Power ON and Power OFF Sequence

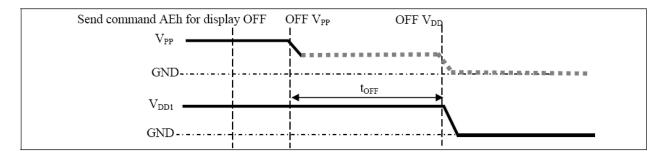
Power ON Sequence:

- 1. Power ON V_{DD1}.
- 2. After V_{DD1} become stable, set RES pin LOW (logic low) for at least 5us (t₁) and then HIGH (logic high).
- 3. After set RES# pin LOW (logic low), wait for at least 5us (t_2). Then Power ON V_{PP} . (1)
- 4. After V_{PP} become stable, send command AFh for display ON. SEG/COM will be ON after 100ms(t_{AF}).



Power OFF Sequence:

- 1. Send command AEh for display OFF.
- 2. Power OFF VPP. (1), (2)
- 3. Wait for t_{OFF}. Power OFF V_{DD1}. (where Minimum t_{OFF}=0ms, Typical t_{OFF}=100ms)



Note:

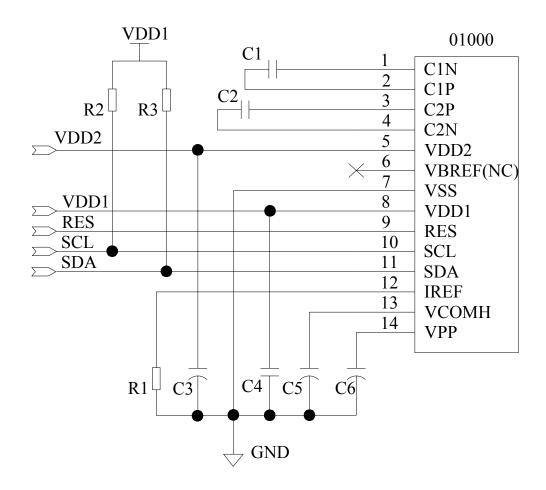
- (1) Since an ESD protection circuit is connected between V_{DD1} and V_{PP} , V_{PP} becomes lower than V_{DD1} whenever V_{DD1} is ON and V_{PP} is OFF as shown in the dotted line of VPP in above figures.
- (2) V_{PP} should be kept float (disable) when it is OFF.

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9.2 Application Circuit

1) The configuration for IIC interface mode, VPP Generated by Internal DC/DC Circuit is shown in the following diagram:



Pin connected to MCU interface: SCL, SDA, RES

Under Internal DC/DC Mode, the Charge Pump Setting(ADh) must be set as follow:

ADh: Charge Pump Setting 8Bh; Enable Charge Pump

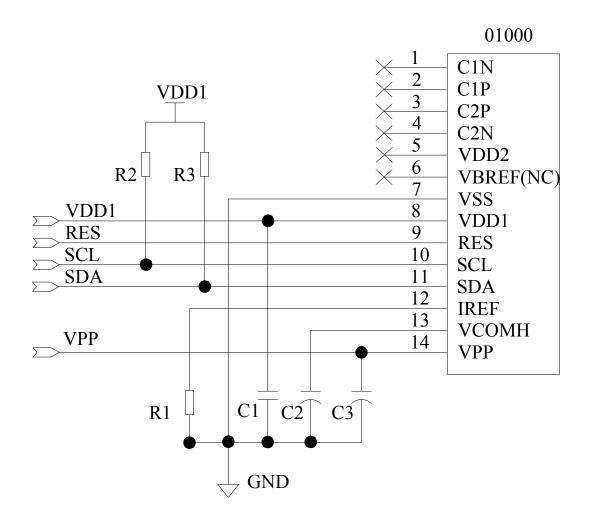
Recommended components

C3,C5,C6: 4.7µF/16V.ROHS (Tantalum Capacitors)

C1,C2,C4: 1uF-0603-X7R±10%.RoHS R1: 0603 1/10W +/-5% 390Kohm.RoHS R2,R3: 0603 1/10W +/-5% 10Kohm.RoHS



2) The configuration for IIC interface mode, external VPP is shown in the following diagram



Pin connected to MCU interface: SCL, SDA, RES C1P, C1N, C2P, C2N, VDD2 should be left open.

Under external V_{PP} Mode, the Charge Pump Setting(ADh) must be set as follow:

ADh: Charge Pump Setting 8Ah; Disable Charge Pump

Recommended components

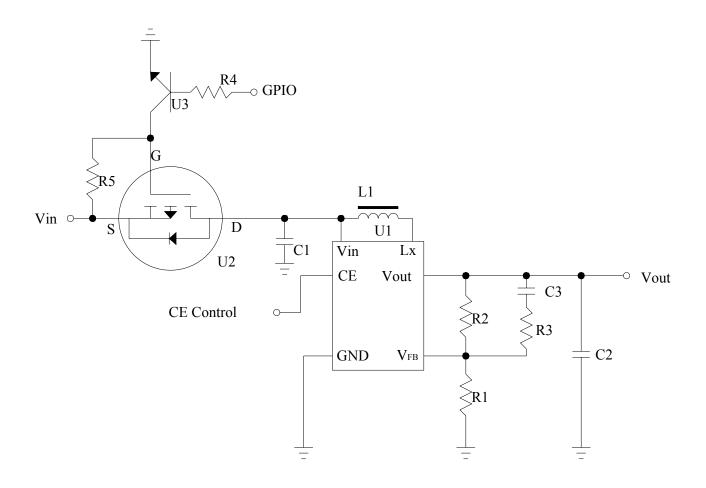
C2,C3: 4.7µF/16V.ROHS (Tantalum Capacitors)

C1: 1uF-0603-X7R±10%.RoHS

R1: 0603 1/10W +/-5% 390Kohm.RoHS R2,R3: 0603 1/10W +/-5% 10Kohm.RoHS



9.3 External DC-DC application circuit



Recommend component

The C1 : 1 uF-0603-X7R \pm 10%.ROHS

The C2 : 1 uF-0603-X7R±10%.ROHS

The C3 : 220pF-0603-X7R±10%.ROHS

The R1 : 0603 1/10W +/-5% 10Kohm.ROHS

The R2 : 0603 1/10W +/-1% 70Kohm.ROHS

The R3 : 0603 1/10W +/-5% 2Kohm.ROHS

The R4 : 0603 1/10W +/-5% 1Kohm.ROHS

The R5 : 0603 1/10W +/-5% 10Kohm.ROHS

The L1 : 22uH

The U1 : R1200

The U2 : FDN338P

The U3 : 8050



9.4 Display Control Instruction

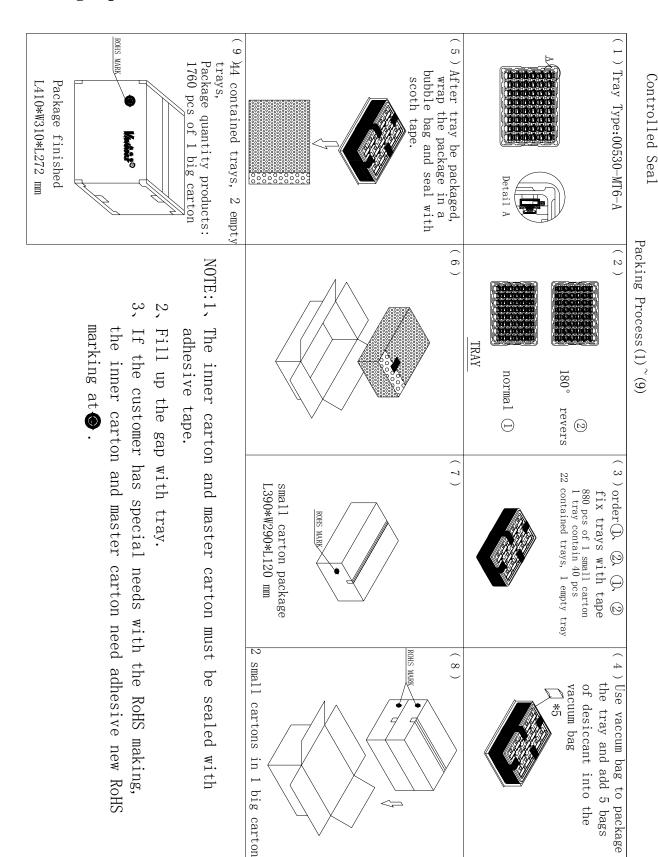
Refer to SH1106G IC Specification.

9.5 Recommended Software Initialization

```
void Init IC()
   Write Command(0xAE);
                               //Set Display Off
   Write Command(0xd5);
                              //display divide ratio/osc. freq. mode
   Write Command(0x91);
                              //
   Write Command(0xA8);
                               //multiplex ration mode:63
   Write Command(0x1F);
   Write Command(0xD3);
                               //Set Display Offset
   Write Command(0x00);
   Write Command(0x40);
                              //Set Display Start Line
   Write Command(0xAD);
                               //DC-DC Control Mode Set
                              //DC-DC ON/OFF Mode Set
   Write Command(0x8b);
   Write Command(0x32);
                              //Set Pump voltage value
   Write Command(0xA1);
                               //Segment Remap
                              //Sst COM Output Scan Direction
   Write Command(0xC8);
   Write Command(0xDA);
                               //common pads hardware: alternative
   Write Command(0x02);
   Write Command(0x81);
                              //contrast control
   Write Command(0x33);
                              //
   Write Command(0xD9);
                              //set pre-charge period
   Write Command(0x1f);
   Write Command(0xDB);
                               //VCOM deselect level mode
   Write Command(0x40);
   Write_Command(0xA4);
                               //Set Entire Display On/Off
   Write Command(0xA6);
                               //Set Normal Display
   Write Command(0xAF);
                               //Set Display On
}
```



10 Package Specification





11 Reliability

11.1 Reliability Test

| NO. | ITEM | CONDITION | QUANTITY |
|-----|--|---|----------|
| 1 | High Temperature (Non-operation) | 85℃,240hrs | 4 |
| 2 | Low Temperature (Non-operation) | -40°C,240hrs | 4 |
| 3 | High Temperature (Operation) | 70℃,240hrs | 4 |
| 4 | Low Temperature (Operation) | -40°C,240hrs | 4 |
| 5 | High Temperature / High Humidity (Operation) | 60℃,90%RH,240hrs | 4 |
| 6 | Thermal shock (Non-operation) | -40°C~85°C(-40°C/30min;transit/3min;85°C/30min;transit/3min) 1 cycle: 66min,30 cycles | 4 |
| 7 | Vibration | Frequency: 5~50Hz,0.5G Scan rate: 1 oct/min Time: 2 hrs/axis Test axis: X,Y, Z | 1 Carton |
| 8 | Drop | Height: 100 cm Sequence: 1 angle, 3 edges and 6 faces | 1 Carton |

Test and measurement conditions

- 1. All measurements shall not be started until the specimens attain to temperature stability, the stable time is at least 15 minutes.
- 2. The degradation of polarizer is ignored for item 5.
- 3. The tolerance of temperature is $\pm 3^{\circ}$ C, and the tolerance of relative humidity is $\pm 5\%$.

Evaluation criteria

- 1. The function test is OK.
- 2. No observable defects.
- 3. Luminance: ≥50% of initial value.
- 4. Current consumption: within \pm 50% of initial value.

11.2 Lifetime

End of lifetime is specified as 50% of initial brightness and the test pattern at operating condition is 50% alternating checkerboard.

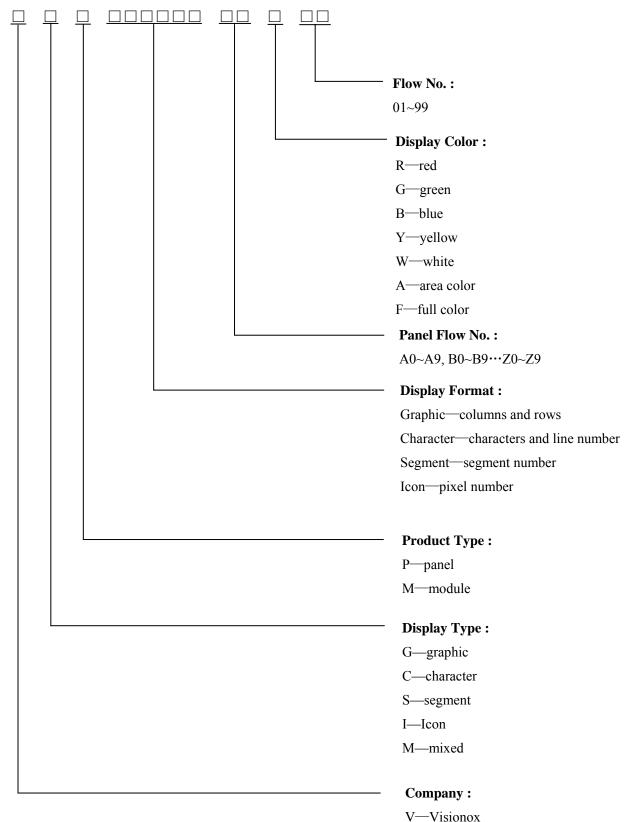
| ITEM | MIN | MAX | UNIT | CONDITION |
|---------------------|--------|-----|------|--|
| Operation Life Time | 10,000 | - | hrs | 150 cd/m², 50% alternating checkerboard, 22±3°C, 55±15% RH |

11.3 Failure Check Standard

After the completion of the described reliability test, the samples were left at room temperature for 2 hrs prior to conducting the failure test at 22 ± 3 °C; 55 ± 15 % RH.



12 Illustration of OLED Product Name





13 Outgoing Quality Control Specifications

13.1 Sampling Method

(1) GB/T 2828.1-2003/ISO2859-1: 1999, inspection level II, normal inspection, single sample inspection

(2) AQL: Major 0.65; Minor 1.0

13.2 Inspection Conditions

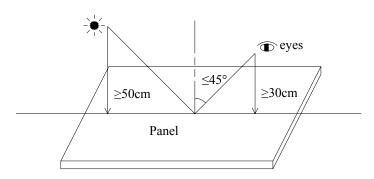
The environmental conditions for test and measurement are performed as follows.

Temperature: 22±3°C Humidity: 55±15%R.H Fluorescent Lamp: 30W

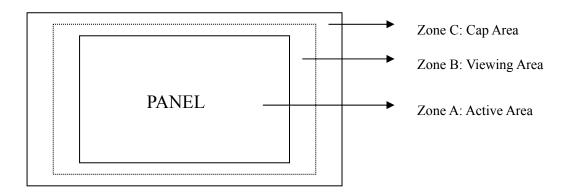
Distance between the Panel & Lamp: ≥50cm Distance between the Panel & Eyes: ≥30cm

Viewing angle from the vertical in each direction: ≤45°

(See the sketch below)



13.3 Quality Assurance Zones

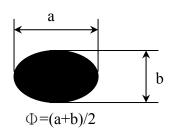


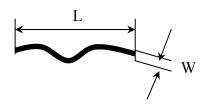
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13.4 Inspection Standard

Definition of Φ&L&W (Unit: mm)





I . Appearance Defects

| NO. | ITEM | | CLASSIFICATION | | | | | |
|-----|--|--|-------------------|--------------------------------|----|------------------------|------------|--|
| 1 | Polarizer Black or White spot, Dirty spot, Foreign matter, Dent on the polarizer | Average Diameter (mm) Φ≤0.15 0.15<Φ≤0.30 Φ>0.30 | Zone Ign 3 | ore | Zo | one C gnore | Minor | |
| 2 | Scratch/line on the glass/Polarizer | Width (mm) W≤0.03 0.03 <w≤0.08 W>0.08</w≤0.08 | Length (mm) L≤5.0 | Accep Zone A Ignore 3 | ,В | Number Zone C Ignore | Minor | |
| 3 | Polarizer Bubble | Average Diamete (mm) Φ>0.5 0.2<Φ≤0.5 Φ≤0.2 | Z | Acceptabone A,B 0 3 Ignore | Z | mber one C gnore | Minor | |
| 4 | Any Dirt & Scratch on Polarizer's Protective Film | Ignore for not affect the polarizer. | | | | | Acceptable | |
| 5 | Any Dirt on Cap Glass | Average Diamete (mm) Φ≤0.5 0.5<Φ≤1.0 Φ>1.0 | er | Acceptable Number Ignore 3 0 | | Minor | | |

| 6 | Glass Crack | | Major |
|----|-----------------------------|--|-------|
| | | Propagation crack is not acceptable. | |
| 7 | Corner Chip | | Minor |
| | | t= Glass thickness Accept a≤2.0mm or b≤2.0mm, c≤t | |
| 8 | Corner Chip on Cap Glass | t= Glass thickness | Minor |
| | 1 | Accept | |
| | | a≤1.5mm or b≤1.5mm, c≤t | |
| 9 | Chip on Contact Pad | t= Glass thickness | Minor |
| | | Accept a≤3.0mm or b≤0.8mm, c≤t (on the contact pin) a≤3.0mm or b≤1.5mm, c≤t (outside of the contact pin) | |
| 10 | Chip on Face of Display | | Minor |
| | Display | t= Glass thickness Accept | |
| | | a≤1.5mm or b≤1.5mm, c≤t | |
| 11 | Chip on Cap Glass | 1. | Minor |
| | | t= Glass thickness | |
| | | Accept a≤3.0mm or b≤3.0mm, c≤t/2 | |
| | | a≤1.5mm or b≤1.5mm, t/2≤c≤t | |
| 12 | Stain on Surface | Stain removable by soft cloth or air blow is acceptable. | Minor |
| 13 | TCP/FPC Damage | (1) Crack, deep scratch, deep hole and deep pressure mark on the TCP/FPC are not acceptable. | Minor |
| | | (2) Terminal lead twisted or broken is not allowable. (3) Copper exposed is not allowed by naked eye inspection. | |
| 14 | Dimension Unconformity | Checking by mechanical drawing. | Major |





II. Displaying Defects

| NO. | ITEM | | CLASSIFICATION | | |
|-----|--|--|--------------------------------------|---------------|----------------|
| 1 | Black/White spot Dirty spot Foreign matter | Average Diameter (mm) Φ≤0.10 0.10<Φ≤0.20 Φ>0.20 | Pieces Po Zone A,B Ignore 3 | Zone C Ignore | Minor |
| 2 3 | No Display Irregular Display | Not allowable. Not allowable. | | | Major Major |
| 4 | Missing Line (row or column) | Not allowable. | | | Major |
| 5 | Short | Not allowable. | | | Major |
| 6 | Flicker | Not allowable. | | | Major |
| 7 | Abnormal Color | Refer to the SPEC. | | | Major |
| 8 | Luminance NG | Ref | Major | | |
| 9 | Over Current | Refer to the SPEC. | | | Major |



14 Precautions for operation and Storage

14.1 Precautions for Operation

- (1) Since OLED panel is made of glass, do not apply any mechanical shock or impact or excessive force to it when installing the OLED module. Any strong mechanical impact due to falling dropping etc. may cause damage (breakage or cracking).
- (2) The polarizer on the OLED surface is made of soft material and is easily scratched. Please take most care when handing. When the surface of the polarizer of OLED Module is contaminated, please wipe it off gently by using moisten soft cloth with isopropyl alcohol, do not use water, ketone or aromatics. If there is saliva or water on the OLED surface, please wipe it off immediately.
- (3) When handling OLED module, please be sure that the body and the tools are properly grounded. And do not touch I/O pins with bare hands or contaminate I/O pins, it will cause disconnection or defective insulation of terminals.
- (4) Do not attempt to disassemble or process the OLED module.
- (5) OLED module should be used under recommended operating conditions shown in the specification. Since the higher voltage leads to the shorter lifetime, be sure to use the specified operating voltage.
- (6) Foggy dew, moisture condensation or water droplets deposited on surface and contact terminals will cause polarizer stain or damage, the deteriorated display quality and electrochemical reaction then leads to shorter life time and permanent damage to the module probably. Please pay attention to the environmental temperature and humidity.
- (7) An afterimage is created by the difference in brightness between unused dot and the fixed dot, according to the decrease of brightness of the emitting time. Therefore, to avoid having an afterimage, the full set should be thoroughly used instead of using a fixed dot. When the fixed dot emits, an afterimage can be created.
- (8) Flicker could be come out at full on display. And it disappears when frame frequency increase, but brightness decreases too.

14.2 Soldering

- (1) Soldering should be performed only on the I/O terminals.
- (2) Use soldering irons with proper grounding and no leakage.
- (3) Iron: no higher than 300°C and 3~4 sec during soldering.

14.3 Precautions for Storage

- (1) Please store OLED module in a dark place. Avoid exposure to sunlight, the light of fluorescent lamp or any ultraviolet ray.
- (2) Keep the environment temperature between 10°C and 35°C and the relative humidity less than 60%. Avoid high temperature and high humidity.
- (3) Keep the OLED modules stored in the container when shipped from supplier before using them is recommended.
- (4) Do not leave any article on the OLED module surface for an extended period of time.

14.4 Warranty period

Visionox Display Co., Ltd. warrants for a period of 12 months from the shipping date when stored or used under normal condition.